

FS65 Safety SBC Safety Monitoring Capabilities

Robust fit for ASIL-D with fail-silent and Grade 0 capabilities

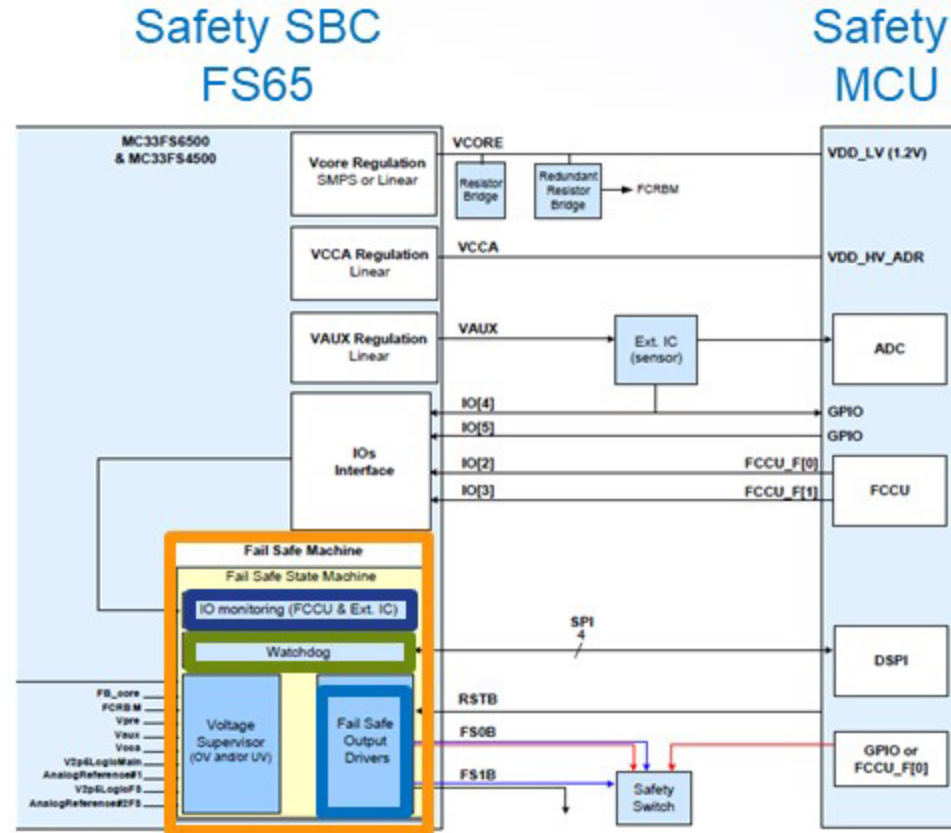


Independent Fail Safe State Machine

- Physical & electrical independence to fit for ASIL-D
- Power management monitoring unit (UV / OV)
- Analog & digital built in self test to minimize latent faults
- Own reference & supply to reduce common cause failure

Advanced Watchdog

- Challenger
- Replace external MCU monitoring



HW Redundancy

- Vcore external monitoring

MCU Monitoring

- FCCU: fault collection control unit
- Monitor dual-core lock-step modes

RSTb – Fail Silent Mode

- Configurable RSTb activation giving more system availability

Fail Safe Pin (FS0b)

- Redundant system fail safe enabler
- Second fail safe pin to assert safety path with configurable delay