

CHIP COIL (CHIP INDUCTORS) LQW18AN□□□□8ZD
Murata Standard Reference Specification 【AEC-Q200】

1. Scope

This reference specification applies to LQW18AN_8ZD series, Chip coil (Chip Inductors) for automotive Electronics based on AEC-Q200 except for Power train and Safety.

2. Part Numbering

(ex) LQ W 18 A N 2N2 C 8 Z D
 Product ID Structure Dimension Applications Category Inductance Tolerance Features Application Packaging
 (L×W) and Characteristics Z:Automotive D:Taping

3. Rating

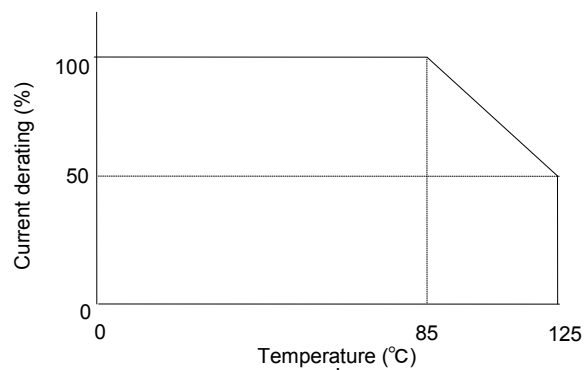
- Operating Temperature Range. -55°C to +125°C (Rated Current is derated as following figure depending)
- Storage Temperature Range. -55°C to +125°C

Customer Part Number	MURATA Part Number	Inductance		Q (min.)	DC Resistance (Ω max.)	Self Resonant Frequency (MHz min.)	Rated Current (mA)	ESD Rank 6: 25kV
		(nH)	Tolerance					
	LQW18AN2N2C8ZD	2.2	C:±0.2nH	24	0.018	15000	3200	6
	LQW18AN2N4C8ZD	2.4		18	0.026		2400	
	LQW18AN3N0C8ZD	3.0		13	0.17		670	
	LQW18AN3N9B8ZD	3.9	B:±0.1nH C:±0.2nH G:±2%	30	0.028	10000	2200	
	LQW18AN3N9C8ZD							
	LQW18AN3N9G8ZD							
	LQW18AN4N1B8ZD	4.1						
	LQW18AN4N1C8ZD							
	LQW18AN4N1G8ZD							
	LQW18AN4N2B8ZD	4.2						
	LQW18AN4N2C8ZD							
	LQW18AN4N2G8ZD							
	LQW18AN4N3B8ZD	4.3						
	LQW18AN4N3C8ZD							
	LQW18AN4N3G8ZD							
	LQW18AN4N7B8ZD	4.7						
	LQW18AN4N7C8ZD							
	LQW18AN4N7G8ZD							
	LQW18AN4N9B8ZD	4.9						
	LQW18AN4N9C8ZD							
	LQW18AN4N9G8ZD							
	LQW18AN5N6C8ZD	5.6	C:±0.2nH G:±2%	40	0.040	6650	1900	
	LQW18AN5N6G8ZD							
	LQW18AN6N0C8ZD	6						
	LQW18AN6N0G8ZD							
	LQW18AN6N5C8ZD	6.5						
	LQW18AN6N5G8ZD							
	LQW18AN6N8C8ZD	6.8						
	LQW18AN6N8G8ZD							
	LQW18AN7N2C8ZD	7.2						
	LQW18AN7N2C8ZD							
	LQW18AN7N5C8ZD	7.5						
	LQW18AN7N5G8ZD							
				38				
				35	0.048	7000	1500	

Customer Part Number	MURATA Part Number	Inductance		Q (min.)	DC Resistance (Ω max.)	Self Resonant Frequency (MHz min.)	Rated Current (mA)	ESD Rank 6: 25kV
		(nH)	Tolerance					
	LQW18AN8N2C8ZD	8.2	C: \pm 0.2nH G: \pm 2%	38	0.052	4750	1600	6
	LQW18AN8N2G8ZD							
	LQW18AN8N4C8ZD	8.4						
	LQW18AN8N4G8ZD							
	LQW18AN8N7C8ZD	8.7						
	LQW18AN8N7G8ZD							
	LQW18AN9N1C8ZD	9.1						
	LQW18AN9N1G8ZD	9.5						
	LQW18AN9N5C8ZD							
	LQW18AN9N5G8ZD	9.9						
	LQW18AN9N9C8ZD							
	LQW18AN9N9G8ZD	10						
	LQW18AN10NG8ZD							
	LQW18AN10NJ8ZD	11	40					
	LQW18AN11NG8ZD							
	LQW18AN11NJ8ZD	12	37	0.064	5000	1500		
	LQW18AN12NG8ZD							
	LQW18AN12NJ8ZD	13	38					
	LQW18AN13NG8ZD							
	LQW18AN13NJ8ZD	15	38					
	LQW18AN15NG8ZD							
	LQW18AN15NJ8ZD	16	40	0.075	4600	1400		
	LQW18AN16NG8ZD							
	LQW18AN16NJ8ZD	17	40					
	LQW18AN17NG8ZD							
	LQW18AN17NJ8ZD	18	40	0.086	3450	1300		
	LQW18AN18NG8ZD							
	LQW18AN18NJ8ZD	19	40					
	LQW18AN19NG8ZD							
	LQW18AN19NJ8ZD	22	40	0.098	3600	1200		
	LQW18AN22NG8ZD							
	LQW18AN22NJ8ZD	23	40					
	LQW18AN23NG8ZD							
	LQW18AN23NJ8ZD	24	40					
	LQW18AN24NG8ZD							
	LQW18AN24NJ8ZD	25	40	0.12	2880	1100		
	LQW18AN25NG8ZD							
	LQW18AN25NJ8ZD	27	40					
	LQW18AN27NG8ZD							
	LQW18AN27NJ8ZD	28	40					
	LQW18AN28NG8ZD							
	LQW18AN28NJ8ZD	30	40					
	LQW18AN30NG8ZD							
	LQW18AN30NJ8ZD							

Customer Part Number	MURATA Part Number	Inductance		Q (min.)	DC Resistance (Ω max.)	Self Resonant Frequency (MHz min.)	Rated Current (mA)	ESD Rank 6: 25kV
		(nH)	Tolerance					
	LQW18AN31NG8ZD	31	G:±2% J:±5%	40	0.11	3150	1100	6
	LQW18AN31NJ8ZD							
	LQW18AN33NG8ZD	33			0.15	1050		
	LQW18AN33NJ8ZD							
	LQW18AN34NG8ZD	34		37	0.20	3000	910	
	LQW18AN34NJ8ZD							
	LQW18AN36NG8ZD	36		40	0.16	3280	1000	
	LQW18AN36NJ8ZD							
	LQW18AN37NG8ZD	37		0.21	2780	2780	840	
	LQW18AN37NJ8ZD							
	LQW18AN39NG8ZD	39		47	0.23	2700	830	
	LQW18AN39NJ8ZD							
	LQW18AN41NG8ZD	41		51	0.27	2750	750	
	LQW18AN41NJ8ZD							
	LQW18AN43NG8ZD	43		56	0.26	2600	770	
	LQW18AN43NJ8ZD							
	LQW18AN44NG8ZD	44		58	0.30	2400	700	
	LQW18AN44NJ8ZD							
	LQW18AN47NG8ZD	47		68	0.38	2380	630	
	LQW18AN47NJ8ZD							
	LQW18AN48NG8ZD	48	69	0.47	2330	560		
	LQW18AN48NJ8ZD							
	LQW18AN51NG8ZD	51	73	0.41	2280	590		
	LQW18AN51NJ8ZD							
	LQW18AN52NG8ZD	52	78	0.5	2230	550		
	LQW18AN52NJ8ZD							
	LQW18AN56NG8ZD	56	82					
	LQW18AN56NJ8ZD							
	LQW18AN58NG8ZD	58						
	LQW18AN58NJ8ZD							
	LQW18AN68NG8ZD	68						
	LQW18AN68NJ8ZD							
	LQW18AN69NG8ZD	69						
	LQW18AN69NJ8ZD							
	LQW18AN72NG8ZD	72						
	LQW18AN72NJ8ZD							
	LQW18AN73NG8ZD	73						
	LQW18AN73NJ8ZD							
	LQW18AN75NG8ZD	75						
	LQW18AN75NJ8ZD							
	LQW18AN78NG8ZD	78						
	LQW18AN78NJ8ZD							
	LQW18AN82NG8ZD	82						
	LQW18AN82NJ8ZD							

Customer Part Number	MURATA Part Number	Inductance		Q (min.)	DC Resistance (Ω max.)	Self Resonant Frequency (MHz min.)	Rated Current (mA)	ESD Rank 6: 25kV
		(nH)	Tolerance					
	LQW18AN83NG8ZD	83	G:±2% J:±5%	34	0.5	2230	550	6
	LQW18AN83NJ8ZD							
	LQW18AN91NG8ZD	91		33	0.54	1900	520	
	LQW18AN91NJ8ZD							
	LQW18AN94NG8ZD	94		34	0.63	1750	490	
	LQW18AN94NJ8ZD							
	LQW18ANR10G8ZD	100		32	0.7	1730	450	
	LQW18ANR10J8ZD							
	LQW18ANR11G8ZD	110		32	0.72	1650	450	
	LQW18ANR11J8ZD							
	LQW18ANR12G8ZD	120		28	0.87	1580	420	
	LQW18ANR12J8ZD							
	LQW18ANR15G8ZD	150		25	1.65	1380	310	
	LQW18ANR15J8ZD							
	LQW18ANR18G8ZD	180		25	1.74	1350	290	
	LQW18ANR18J8ZD							
	LQW18ANR20G8ZD	200		27	1.98	1330	280	
	LQW18ANR20J8ZD							
	LQW18ANR21G8ZD	210		25	2.08	1330	280	
	LQW18ANR21J8ZD							
	LQW18ANR22G8ZD	220	24	2.28	1250	260		
	LQW18ANR22J8ZD							
	LQW18ANR25G8ZD	250	24	2.42	1250	260		
	LQW18ANR25J8ZD							
	LQW18ANR27G8ZD	270	25	3.12	1200	220		
	LQW18ANR27J8ZD							
	LQW18ANR30G8ZD	300	25	3.84	1100	190		
	LQW18ANR30J8ZD							
	LQW18ANR33G8ZD	330	25	3.98	1050	190		
	LQW18ANR33J8ZD							
	LQW18ANR36G8ZD	360	25	4.23	1100	190		
	LQW18ANR36J8ZD							
	LQW18ANR39G8ZD	390	25	4.23	1100	190		
	LQW18ANR39J8ZD							



Derating of Rated Current depend on Operating Temperature

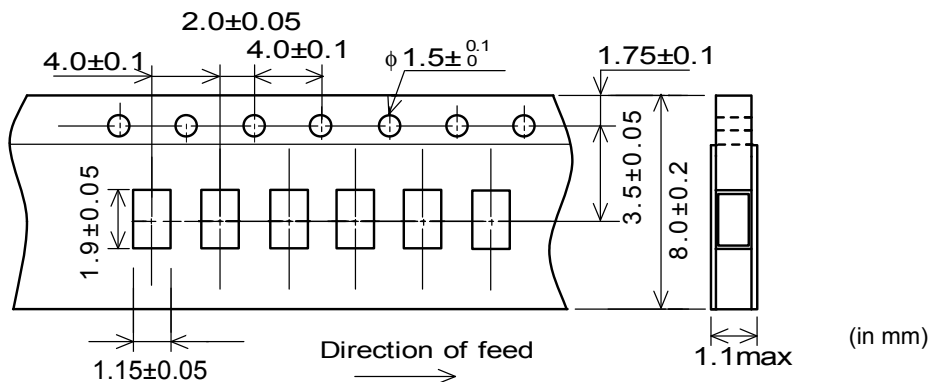
7. Q200 Requirement**7.1.Performance (based on Table 5 for Magnetics(Inductors / Transformer)****AEC-Q200 Rev.D issued June 1. 2010**

AEC-Q200			Murata Specification / Deviation					
No	Stress	Test Method						
3	High Temperature Exposure	1000hours at 125 deg C Set for 24hours at room temperature, then measured.	Meet Table A after testing. Table A	<table border="1"> <tr> <td>Appearance</td> <td>No damage</td> </tr> <tr> <td>Inductance change (at 100MHz)</td> <td>Within $\pm 5\%$</td> </tr> </table>	Appearance	No damage	Inductance change (at 100MHz)	Within $\pm 5\%$
Appearance	No damage							
Inductance change (at 100MHz)	Within $\pm 5\%$							
4	Temperature Cycling	1000cycles -40 deg C to +125 deg C Set for 24hours at room temperature, then measured.	Meet Table A after testing.					
7	Biased Humidity	1000hours at 85 deg C, 85%RH unpowered.	Meet Table A after testing.					
8	Operational Life	Apply Rated Current 125 deg C 1000hours Set for 24hours at room temperature, then measured	Meet Table A after testing.					
9	External Visual	Visual inspection	No abnormalities					
10	Physical Dimension	Meet ITEM 4 (Style and Dimensions)	No defects					
12	Resistance to Solvents	Per MIL-STD-202 Method 215	Not Applicable					
13	Mechanical Shock	Per MIL-STD-202 Method 213 Condition C : 100g's(0.98N), 6ms, Half sine, 12.3ft/s	Meet Table A after testing.					
14	Vibration	5g's(0.049N) for 20 minutes, 12cycles each of 3 orientations Test from 10-2000Hz.	Meet Table A after testing.					
15	Resistance to Soldering Heat	No-heating Solder temperature 260C+/-5 deg C Immersion time 10s	Pre-heating : 150C +/-10 deg C, 60s to 90s Meet Table A after testing.					
17	ESD	Per AEC-Q200-002	ESD Rank : Refer to Item 3. Rating. Meet Table A after testing.					
18	Solderbility	Per J-STD-002	Method b : Not Applicable 95% of the terminations is to be soldered. (Except exposed wire)					
19	Electrical Characterization	Measured : Inductance	No defects					
20	Flammability	Per UL-94	Not Applicable					

AEC-Q200			Murata Specification / Deviation				
No	Stress	Test Method					
21	Board Flex	Epoxy-PCB(1.6mm) Deflection 2mm(min) Holding time 60s	Meet Table B after testing. <table border="1" style="margin-left: auto; margin-right: auto; border-collapse: collapse;"> <tr> <td style="text-align: center;">Appearance</td> <td style="text-align: center;">No damage</td> </tr> <tr> <td style="text-align: center;">DC resistance change</td> <td style="text-align: center;">Within ±10%</td> </tr> </table>	Appearance	No damage	DC resistance change	Within ±10%
Appearance	No damage						
DC resistance change	Within ±10%						
22	Terminal Strength	Per AEC-Q200-006 A force of 17.7N for 60s	Murata Deviation Request : 10N/5s No defect				

8. Specification of Packaging

8.1 Appearance and Dimensions of paper tape (8mm-wide).



8.2 Specification of Taping

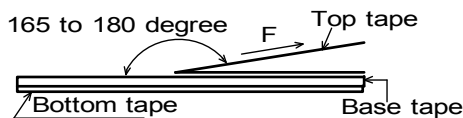
- (1) Packing quantity (standard quantity)
4,000 pcs. / reel
- (2) Packing Method
Products shall be packed in the cavity of the base tape and sealed by top tape and bottom tape.
- (3) Sprocket hole
The sprocket holes are to the right as the tape is pulled toward the user.
- (4) Spliced point
Base tape and Top tape has no spliced point.
- (5) Missing components number
Missing components number within 0.1 % of the number per reel or 1 pc., whichever is greater, and are not continuous. The Specified quantity per reel is kept.

8.3 Pull Strength

Top tape	5N min.
Bottom tape	

8.4 Peeling off force of cover tape

Speed of Peeling off	300mm/min
Peeling off force	0.1N to 0.6N (minimum value is typical)

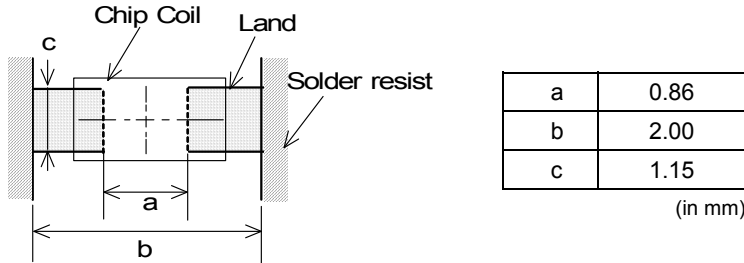


10. Notice

Products can only be soldered with reflow.
 This product is designed for solder mounting.
 Please consult us in advance for applying other mounting method such as conductive adhesive.

10.1 Land pattern designing

Recommended land patterns for reflow soldering are as follows :
 These have been designed for Electric characteristics and solderability.
 Please follow the recommended patterns. Otherwise, their performance which includes electrical performance or solderability may be affected, or result to "position shift" in soldering process.

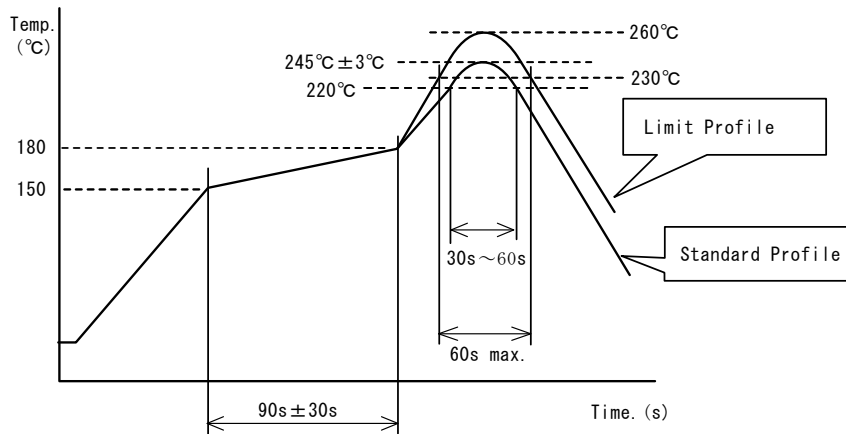


10.2 Flux, Solder

- Use rosin-based flux.
 Includes middle activator equivalent to 0.06(wt)% to 0.1(wt)% Chlorine.
 Don't use highly acidic flux with halide content exceeding 0.2(wt)% (chlorine conversion value).
 Don't use water-soluble flux.
- Use Sn-3.0Ag-0.5Cu solder.
- Standard thickness of solder paste : 100 μ m to 150 μ m.

10.3 Reflow soldering conditions

- Pre-heating should be in such a way that the temperature difference between solder and product surface is limited to 150°C max. Cooling into solvent after soldering also should be in such a way that the temperature difference is limited to 100°C max.
 Insufficient pre-heating may cause cracks on the product, resulting in the deterioration of products quality.
- Standard soldering profile and the limit soldering profile is as follows.
 The excessive limit soldering conditions may cause leaching of the electrode and / or resulting in the deterioration of product quality.
- Reflow soldering profile



	Standard Profile	Limit Profile
Pre-heating	150°C~180°C , 90s±30s	
Heating	above 220°C, 30s~60s	above 230°C, 60s max.
Peak temperature	245°C±3°C	260°C,10s
Cycle of reflow	2 times	

10.4 Reworking with soldering iron

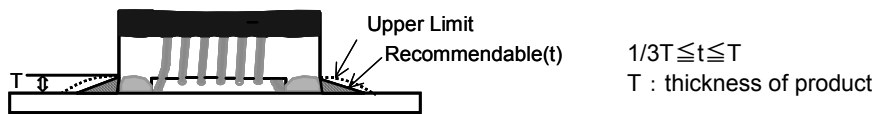
The following conditions must be strictly followed when using a soldering iron.

Pre-heating	150°C, 1 min
Tip temperature	350°C max.
Soldering iron output	80W max.
Tip diameter	φ3mm max.
Soldering time	3(+1,-0)s
Time	2 times

Note : Do not directly touch the products with the tip of the soldering iron in order to prevent the crack on the products due to the thermal shock.

10.5 Solder Volume

- Solder shall be used not to be exceeded the upper limits as shown below.
- Accordingly increasing the solder volume, the mechanical stress to Chip is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance.

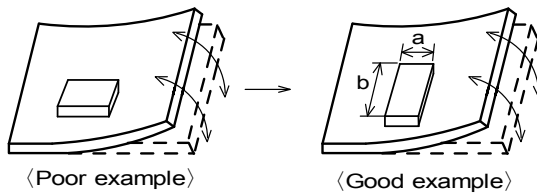


10.6 Product's location

The following shall be considered when designing and laying out P.C.B.'s.

- (1) P.C.B. shall be designed so that products are not subject to the mechanical stress due to warping the board.

[Products direction]



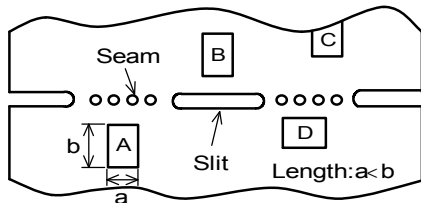
Products shall be located in the sideways direction (Length: a < b) to the mechanical stress.

- (2) Components location on P.C.B. separation.

It is effective to implement the following measures, to reduce stress in separating the board.

It is best to implement all of the following three measures; however, implement as many measures as possible to reduce stress.

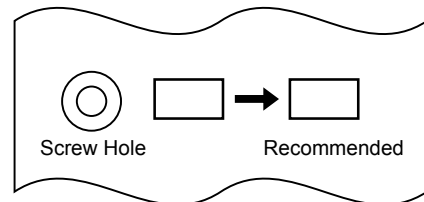
Contents of Measures	Stress Level
(1) Turn the mounting direction of the component parallel to the board separation surface.	A > D *1
(2) Add slits in the board separation part.	A > B
(3) Keep the mounting position of the component away from the board separation surface.	A > C



*1 A > D is valid when stress is added vertically to the perforation as with Hand Separation. If a Cutting Disc is used, stress will be diagonal to the PCB, therefore A > D is invalid.

- (3) Mounting Components Near Screw Holes

When a component is mounted near a screw hole, it may be affected by the board deflection that occurs during the tightening of the screw. Mount the component in a position as far away from the screw holes as possible.



10.7 Cleaning Conditions

Products shall be cleaned on the following conditions.

- (1) Cleaning temperature shall be limited to 60°C max. (40°C max for IPA)
- (2) Ultrasonic cleaning shall comply with the following conditions with avoiding the resonance phenomenon at the mounted products and P.C.B.
Power : 20 W / l max. Frequency : 28kHz to 40kHz Time : 5 min max.
- (3) Cleaner
 1. Alcohol type cleaner
Isopropyl alcohol (IPA)
 2. Aqueous agent
PINE ALPHA ST-100S
- (4) There shall be no residual flux and residual cleaner after cleaning.
In the case of using aqueous agent, products shall be dried completely after rinse with de-ionized water in order to remove the cleaner.
- (5) Other cleaning Please contact us.

10.8 Resin coating

The inductance value may change due to high cure-stress of resin to be used for coating/molding products.

An open circuit issue may occur by mechanical stress caused by the resin, amount/cured shape of resin, or operating condition etc. Some resin contains some impurities or chloride possible to generate chlorine by hydrolysis under some operating condition may cause corrosion of wire of coil, leading to open circuit.

So, please pay your careful attention when you select resin in case of coating/molding the products with the resin.

Prior to use the coating resin, please make sure no reliability issue is observed by evaluating products mounted on your board.

10.9 Caution for use

- Sharp material such as a pair of tweezers or other material such as bristles of cleaning brush , shall not be touched to the winding portion to prevent the breaking of wire.
- Mechanical shock should not be applied to the products mounted on the board to prevent the breaking of the core.

10.10 Notice of product handling at mounting

In some mounting machines,when picking up components support pin pushes up the components from the bottom of base tape. In this case, please remove the support pin. The support pin may damage the components and break wire.

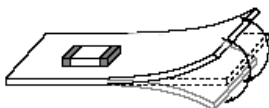
In rare case ,the laser recognition can not recognize this component. Please contact us when you use laser recognition. (There is no problem with the permeation and reflection type.)

10.11 Handling of a substrate

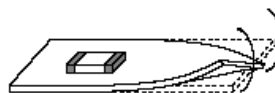
After mounting products on a substrate, do not apply any stress to the product caused by bending or twisting to the substrate when cropping the substrate, inserting and removing a connector from the substrate or tightening screw to the substrate.

Excessive mechanical stress may cause cracking in the product.

Bending



Twisting

**10.12 Storage and Handling Requirements****(1) Storage period**

Use the products within 12 months after delivered.

Solderability should be checked if this period is exceeded.

(2) Storage conditions

- Products should be stored in the warehouse on the following conditions.

Temperature : -10°C to 40°C

Humidity : 15% to 85% relative humidity No rapid change on temperature and humidity

- Don't keep products in corrosive gases such as sulfur, chlorine gas or acid, or it may cause oxidization of electrode, resulting in poor solderability.
- Products should not be stored on bulk packaging condition to prevent the chipping of the core and the breaking of winding wire caused by the collision between the products.
- Products should be stored on the palette for the prevention of the influence from humidity, dust and so on.
- Products should be stored in the warehouse without heat shock, vibration, direct sunlight and so on.

(3) Handling Condition

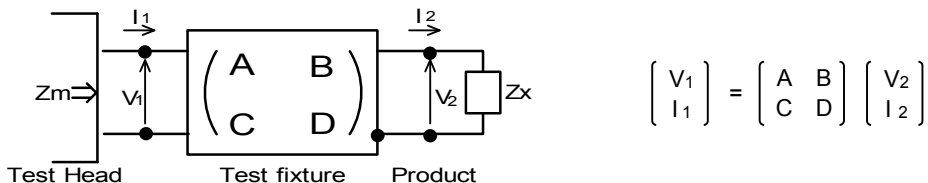
Care should be taken when transporting or handling product to avoid excessive vibration or mechanical shock.

11. ⚠ Note

- (1) Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
- (2) You are requested not to use our product deviating from the reference specifications.
- (3) The contents of this reference specification are subject to change without advance notice.
Please approve our product specifications or transact the approval sheet for product specifications before ordering.

<Electrical Performance:Measuring Method of Inductance / Q>

(1) Residual elements and stray elements of test fixture can be described by F-parameter shown in following.



(2) The impedance of chip coil Z_x and measured value Z_m can be described by input/output current/voltage.

$$Z_m = \frac{V_1}{I_1} , \quad Z_x = \frac{V_2}{I_2}$$

(3) Thus, the relation between Z_x and Z_m is following;

$$Z_x = \alpha \frac{Z_m - \beta}{1 - Z_m \Gamma} \quad \text{where, } \alpha = D / A = 1$$

$$\beta = B / D = Z_{sm} - (1 - Y_{om} Z_{sm}) Z_{ss}$$

$$\Gamma = C / A = Y_{om}$$

- Z_{sm} : measured impedance of short chip
- Z_{ss} : residual impedance of short chip (0.771nH)
- Y_{om} : measured admittance when opening the fixture

(4) L_x and Q_x shall be calculated with the following equation.

$$L_x = \frac{\text{Im}(Z_x)}{2\pi f} , \quad Q_x = \frac{\text{Im}(Z_x)}{\text{Re}(Z_x)}$$

L_x : Inductance of chip coil
 Q_x : Q of chip coil
 f : Measuring frequency