

Low Profile Shielded High Current Inductors



FEATURES

- Frequency range up to 5 MHz
- Ferrite core with polyurethane enameled copper wire
- Epoxy resin used for adhesive
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


**RoHS
COMPLIANT**

APPLICATIONS

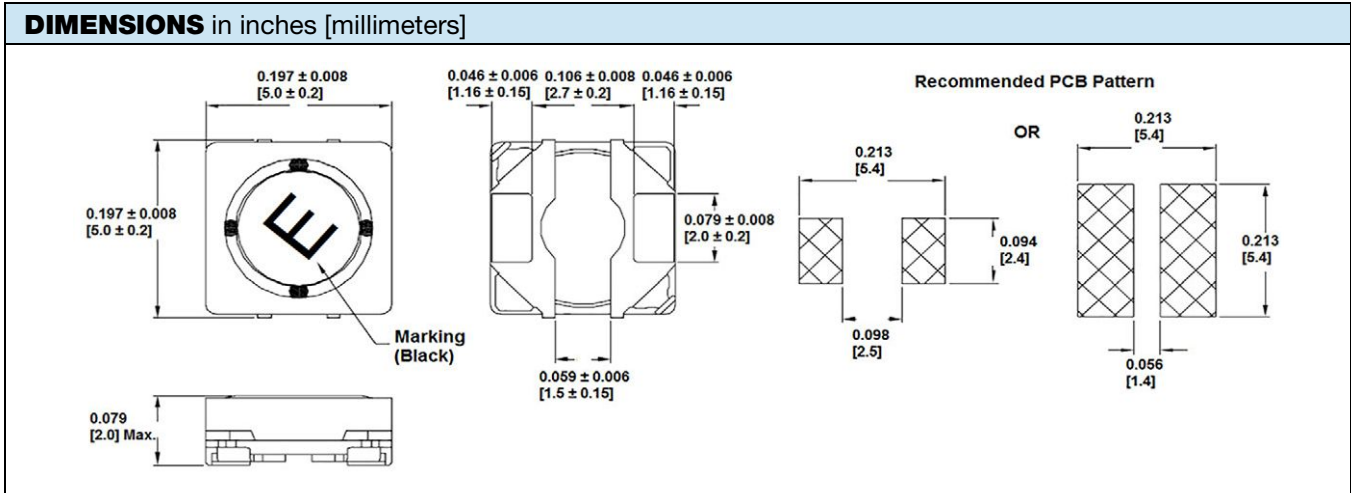
- PDA / notebook / desktop / server applications
- High current POL converters
- Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for Field Programmable Gate Array (FPGA)

STANDARD ELECTRICAL SPECIFICATIONS

PART NUMBER	L ₀ INDUCTANCE AT 100 kHz, 1 V, 0 A (μH)	TOLERANCE (%)	DCR NOM. 25 °C (mΩ)	DCR MAX. 25 °C (mΩ)	HEAT RATING CURRENT DC TYP. (A) ⁽³⁾	SATURATION CURRENT DC TYP. (A) ⁽⁴⁾	MARKING
IFLS2020BZER1R0N	1.0	30	43	52	2.60	3.80	A
IFLS2020BZER1R2N	1.2	30	43	52	2.60	3.80	B
IFLS2020BZER1R5N	1.5	30	55	66	2.30	3.00	C
IFLS2020BZER2R2N	2.2	30	61	73	2.20	2.60	E
IFLS2020BZER2R7N	2.7	30	75	90	2.00	2.10	F
IFLS2020BZER3R3M	3.3	20	77	92	1.90	1.90	G
IFLS2020BZER4R7M	4.7	20	100	120	1.70	1.80	I
IFLS2020BZER6R8M	6.8	20	110	132	1.60	1.40	K
IFLS2020BZER8R2M	8.2	20	120	144	1.55	1.30	L
IFLS2020BZER100M	10	20	130	156	1.50	1.10	M
IFLS2020BZER150M	15	20	200	240	1.15	0.90	O
IFLS2020BZER220M	22	20	230	276	1.00	0.75	Q
IFLS2020BZER330M	33	20	290	348	0.90	0.60	S
IFLS2020BZER470M	47	20	400	480	0.75	0.55	U
IFLS2020BZER680M	68	20	650	780	0.50	0.46	W

Notes

- (1) All test data is referenced to 25 °C ambient
- (2) Operating and Storage temperature range -40 °C to +105 °C
- (3) DC current (A) that will cause an approximate ΔT of 40 °C
- (4) DC current (A) that will cause L₀ to drop approximately 30 %
- (5) The part temperature (ambient + temp. rise) should not exceed 105 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application



DESCRIPTION					
IFLS-2020BZ	4.7 μH	± 20 %	ER	e3	
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD	

GLOBAL PART NUMBER															
I	F	L	S	2	0	2	0	B	Z	E	R	4	R	7	M
PRODUCT FAMILY				SIZE					PACKAGE CODE		INDUCTANCE VALUE			TOL.	



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