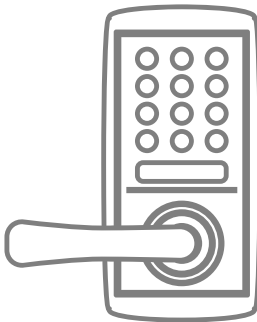
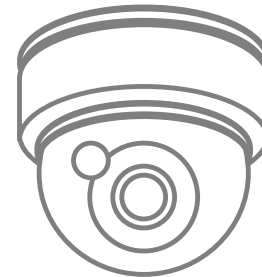
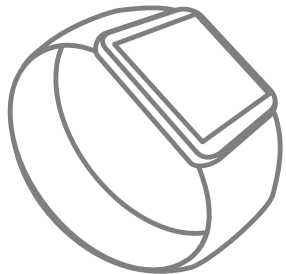


TOSHIBA

Robotics with an Emphasis on Servo Motors and Sensing/Display Solutions

Solution Proposal by Toshiba

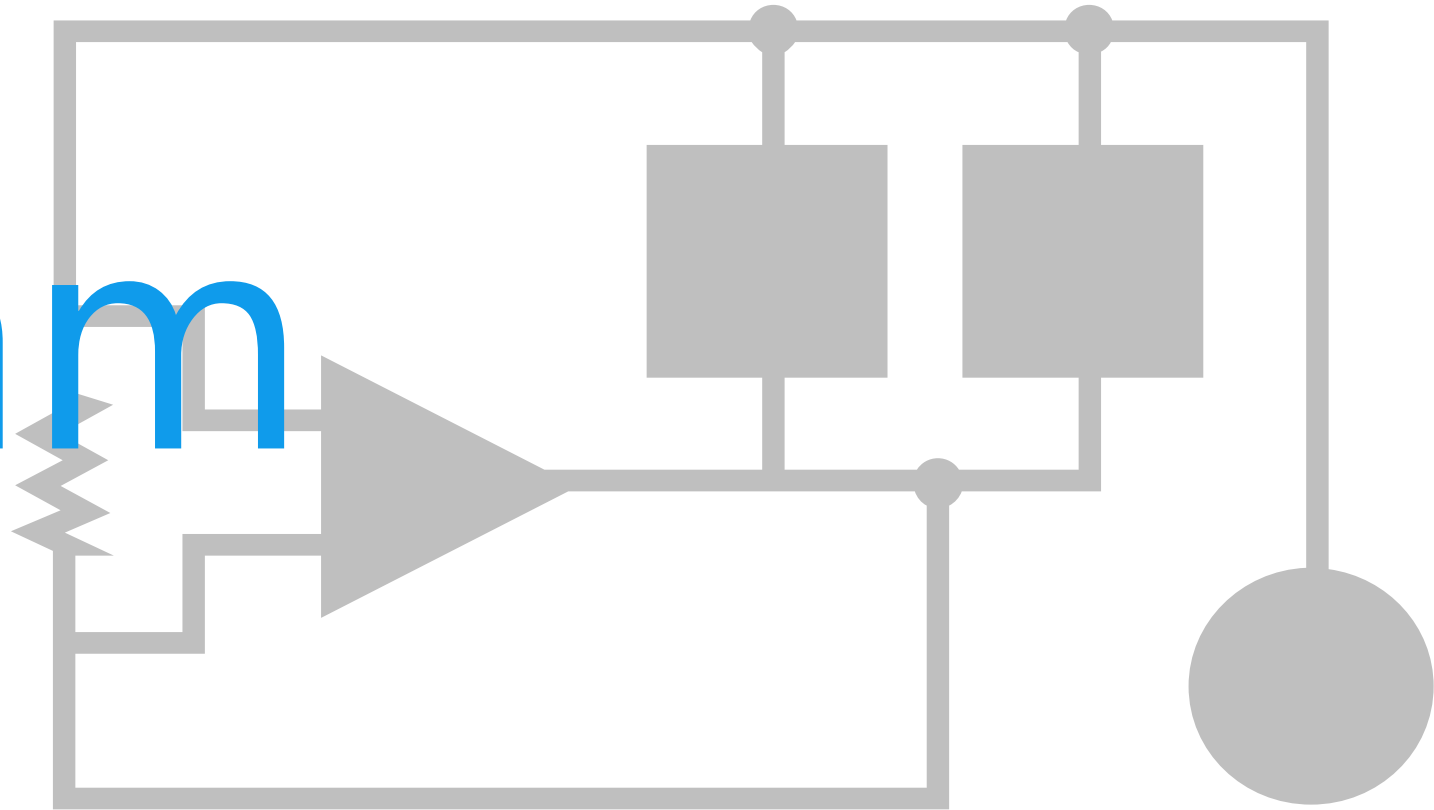




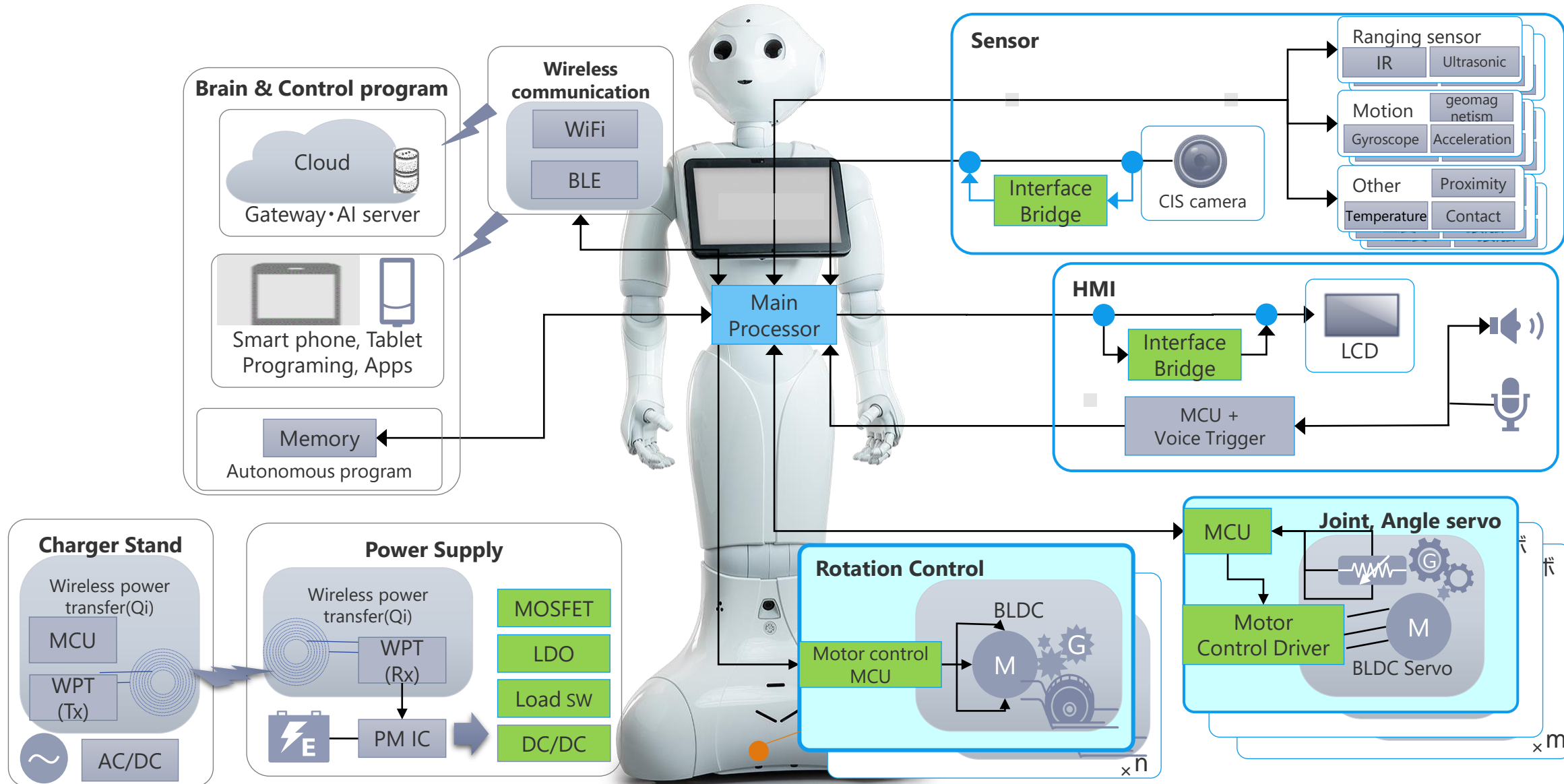
Toshiba Electronic Devices & Storage Corporation provides comprehensive device solutions to customers developing new products by applying its thorough understanding of the systems acquired through the analysis of basic product designs.



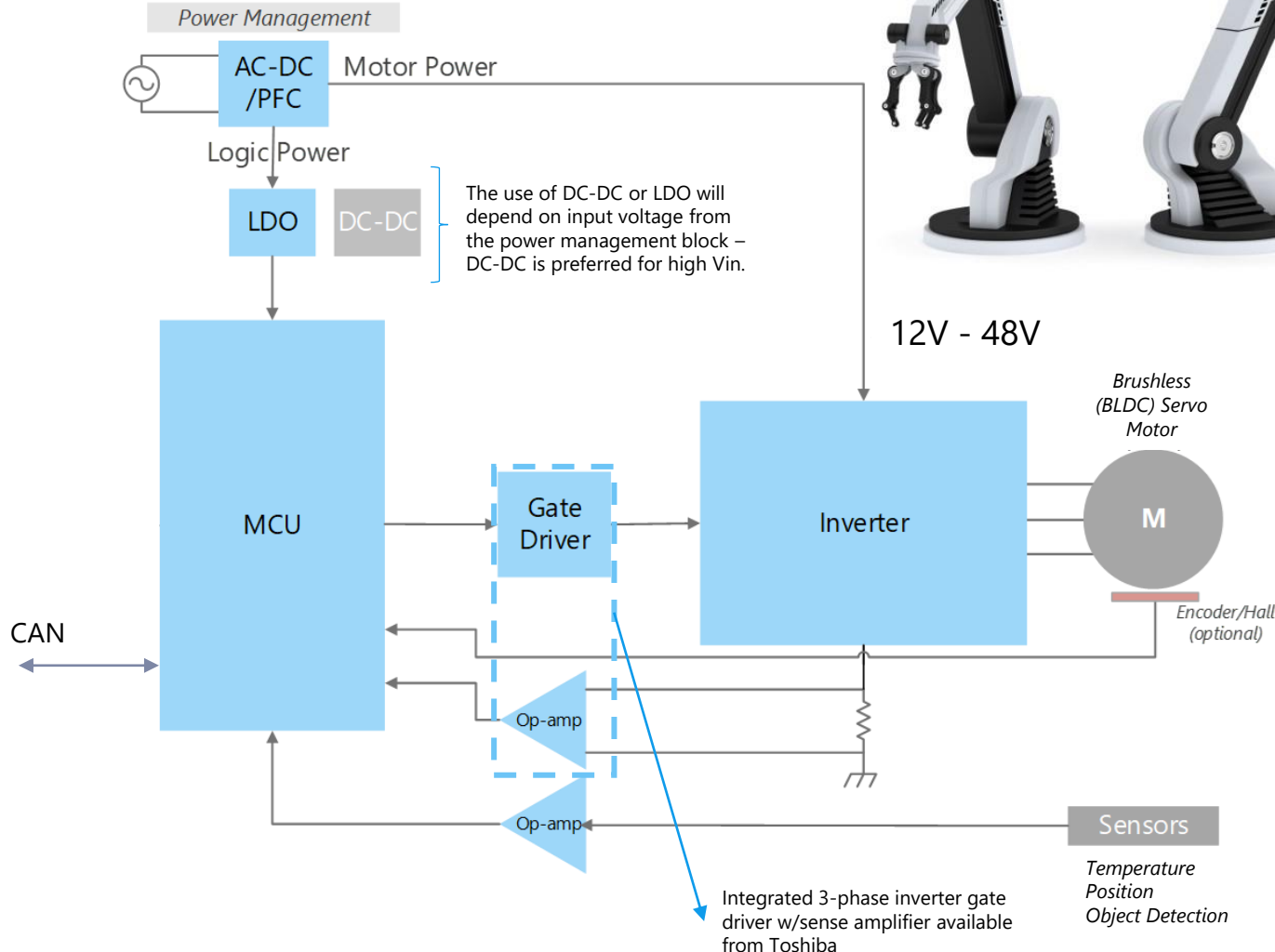
Block Diagram



Humanoid Robot Conceptual Block Diagram



Servo Motors for Robotics General Block Diagram



System Overview

- In one robot arm system, there could be up to 8+ motors to handle not only x-y-z positioning, but also rotational and grab/release motion.
- The general block diagram is showing supporting devices for one motor set up for a typical industrial robot using 12V-48V servo motors.
- Industrial power management systems can be as high as 400-600V that can be handled by Toshiba's IGBT and SiC Power MOSFETs.

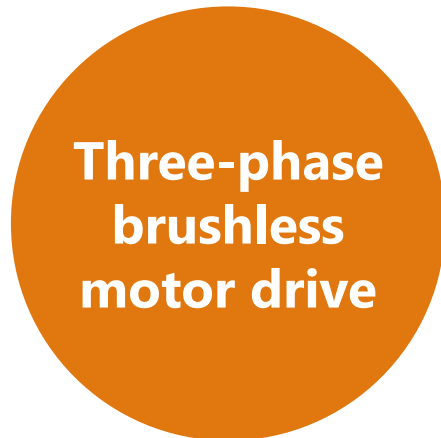
Primary Focus is on 12V-48V BLDC servo motor systems for this Solution Proposal.

1: ACIM = AC Induction Motor
2: BLDC = Brushless DC Motor

Device solutions to address customer needs

Robot design criteria has many wide and various facets. Key focus areas would be “**Smooth precise control and high efficiency of the BLDC servo motor**”, “**Low power consumption with good power efficiency**” and “**Miniaturization of circuit boards.**” Toshiba’s proposals are based on these three solution perspectives.

High torque and
high efficiency of the motor



Low power consumption
and thermal design of the set



Miniaturization of
circuit boards



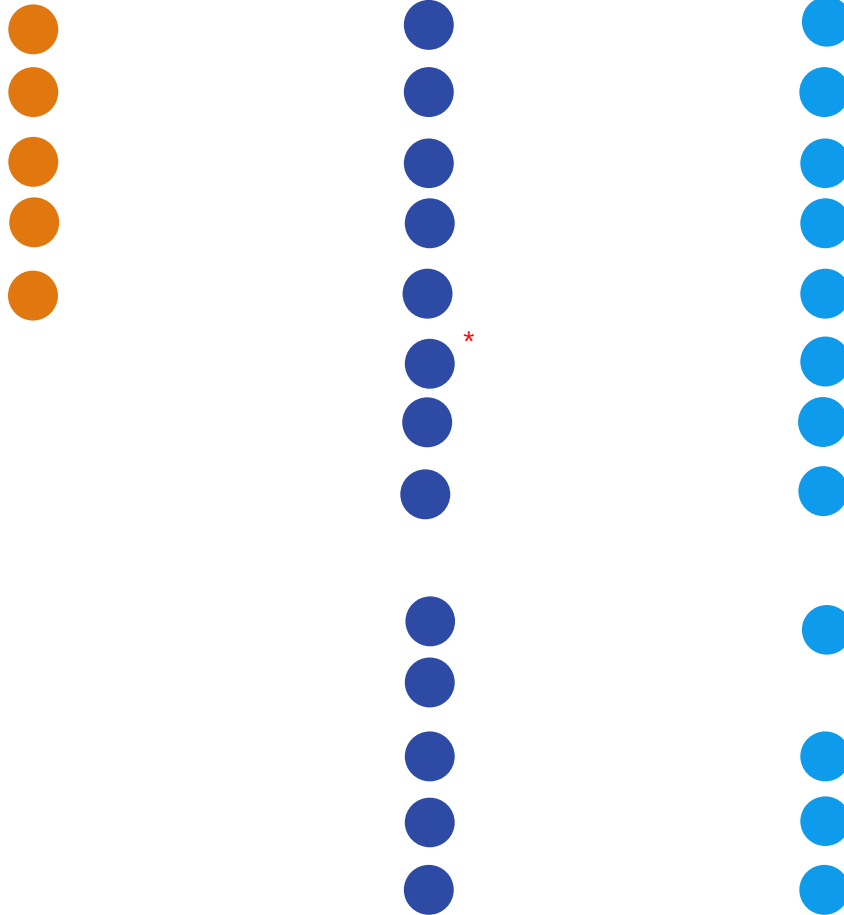
Device solutions to address customer needs

Three-phase brushless motor drive

Low loss, Low heat, and power efficiency

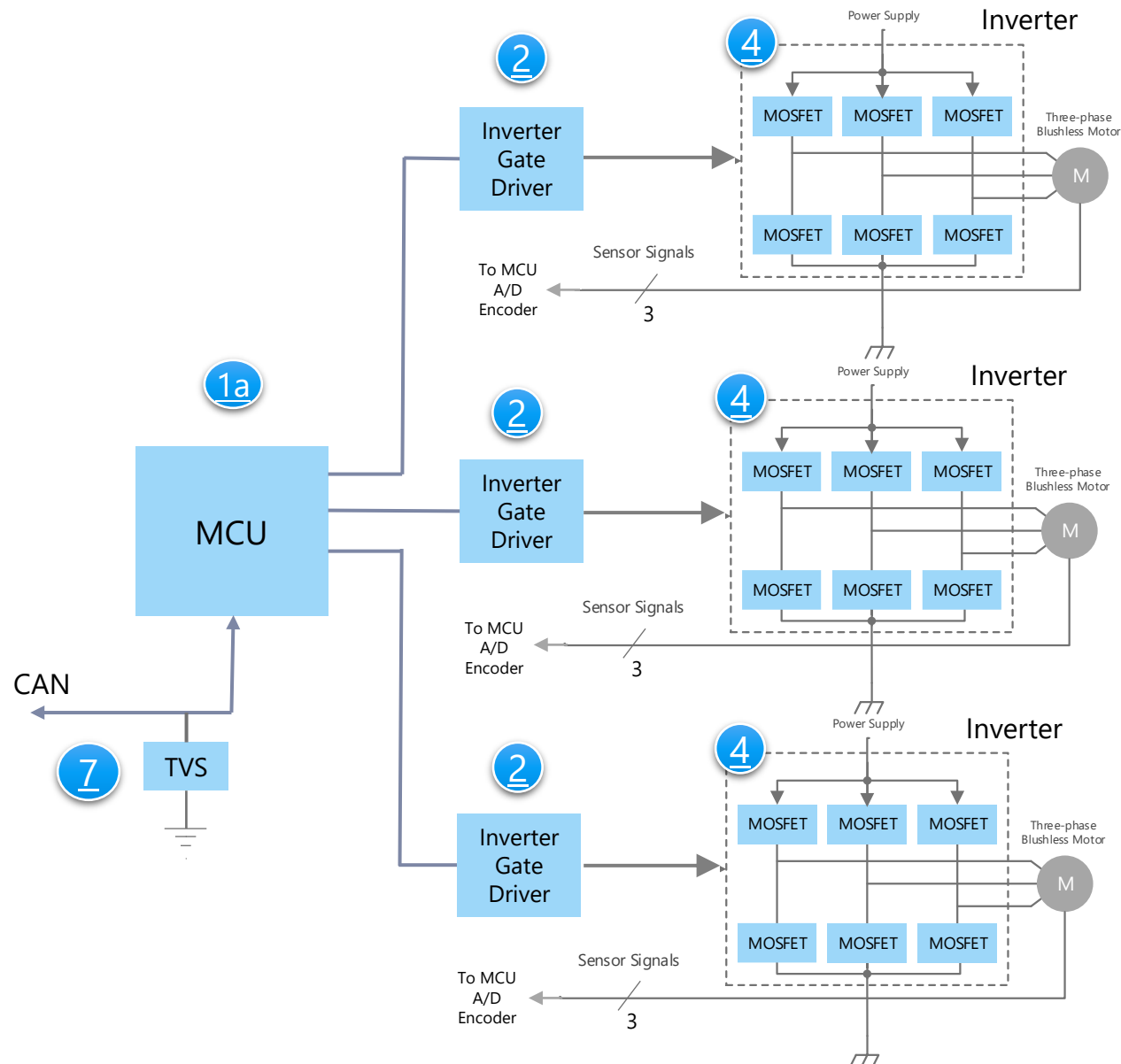
Small size packages

- 1a TPM4M 1b TPM3H Microcontroller
- 2 3-Phase Inverter Gate Driver
- 3 3-Phase Sensor-less Brushless MCD
- 4 UMOS series 100V LV MOSFET
- 5 Low noise operational amplifier
- 6 Small surface mount LDO regulator
- 7 TVS – ESD Protection Diodes
- 8 Thermoflagger™
- 9 MPD Camera and Display Bridges
- 10a 10b Gate Drive 10c Transistor Output Photocoupler
- 11a IGBT 11b SiC 650V MOSFET
- 12a 150V 12b 80V UMOS Power MOSFET
- 13 Efuse
- 14 SiC Schottky Barrier Diode



* For post DC-DC switching regulation

Motor Control MCU Driving Several Servo Motors



Criteria for device selection

- By using dedicated motor control MCU, user can drive brushless DC motor with great performance cost effectively
- System can drive high-capacity brushless motor by using motor controller with external MOSFET.
- A set with low heat generation and low power consumption can be realized by using MOSFET with low on-resistance and high thermal dissipation characteristics as driver.

Proposal from Toshiba

Motor Control MCU for Inverter & PFC Control

MCU **TMPM4M Group**

U-MOS Series 100V MOSFET with low on-resistance and good thermal performance (1a)

TPH3R70APL, TPW3R70APL (4)

TPH2R70AR5

TVS to protect lines (7)

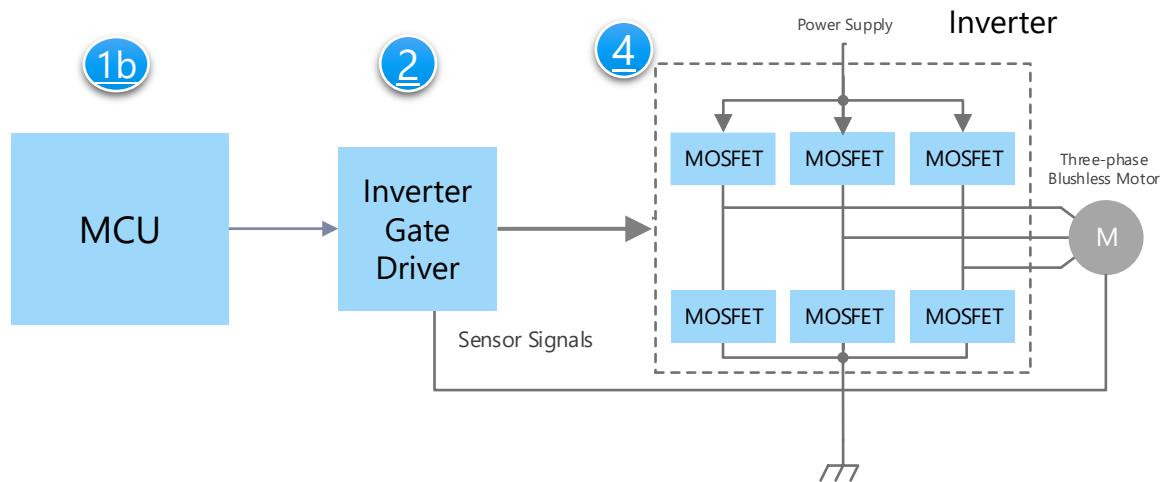
TCKE8 Series

Inverter Gate Driver can optimize FETs for the most demanding application

TB67Z8xx family of 3-Phase Inverter Gate Driver (2)

※ Click the number in the circuit diagram to jump to the detailed description page

New Inverter Gate Driver Designed for Driving 3-Phase BLDC Motors



Criteria for device selection

- By using a MCU with a dedicated 3-phase BLDC gate driver, a user can drive brushless DC motor with great performance cost effectively
- System can drive high-capacity brushless motor by using motor controller with external MOSFET.
- A set with low heat generation and low power consumption can be realized by using MOSFET with low on-resistance and high thermal dissipation characteristics as driver.

Proposal from Toshiba

Motor Control MCU

MCU **TMPM3H Group**

Inverter Gate Driver can optimize FETs for the most demanding applications

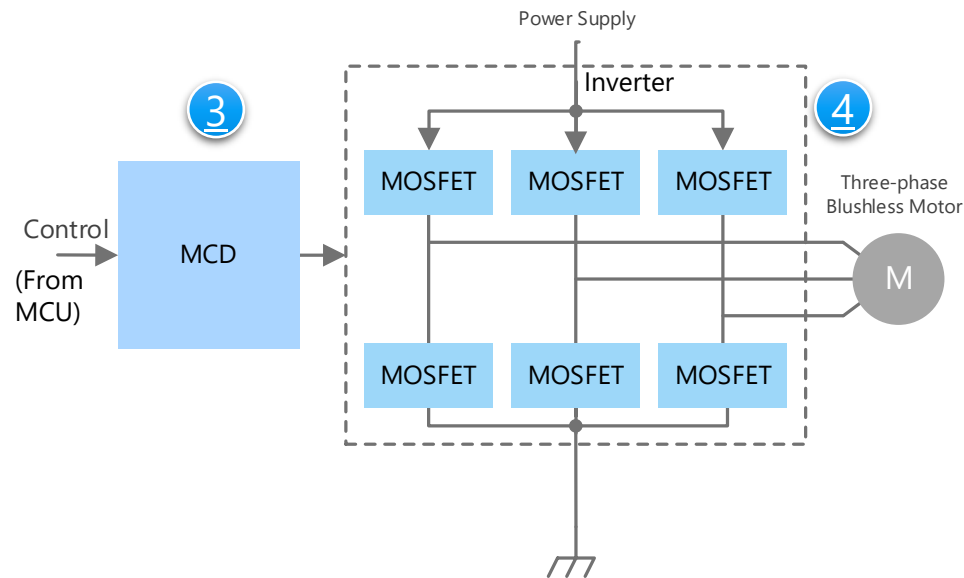
TB67Z8xx family of 3-Phase Inverter Gate Driver

U-MOS Series 100V MOSFET with low on-resistance and good thermal performance

**TPH3R70APL, TPW3R70APL
TPH2R70AR5**

※ Click the number in the circuit diagram to jump to the detailed description page

Simplified Lower Performance Control for Sensor-less BLDC Servos



Criteria for device selection

- For Lower Performance/Lower Cost servo systems
- System can drive lower-capacity brushless motor by using MCD pre-driver with external MOSFET.
- Low gate Load (C_{iss}/Q_g) MOSFETs capable of being driven by low source/sink gate driver

Proposal from Toshiba

Low-Cost Feature Rich Motor Control Device

TC78B009/TC78B011 3-Phase BLDC sensor-less pre-drivers

3

U-MOS Series MOSFET with low Q_g

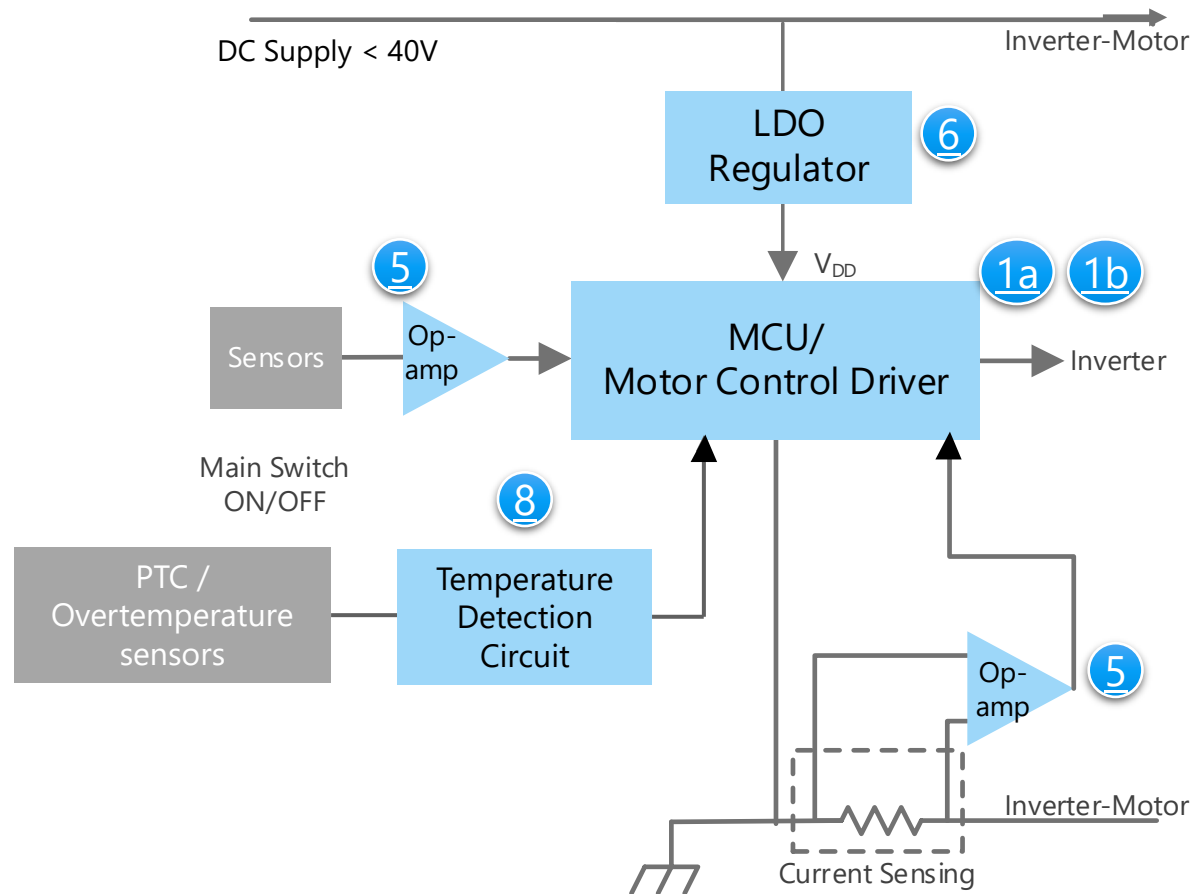
**TPH3R70APL, TPW3R70APL
TPH2R70AR5**

4

※ Click the number in the circuit diagram to jump to the detailed description page

Microcontroller Peripheral Detail

MCU peripheral circuit



Criteria for device selection

- By monitoring the system power supply current, detection of abnormal equipment state and battery overcurrent protection become possible.
- Stable system is realized by adopting op-amps and LDO power supplies resistant to noise from the motor drive section.
- General purpose microcontroller suitable for system control and monitoring.

Proposal from Toshiba

Motor Control MCU for Inverter & PFC Control

MCU **TMPM4M** (1a) **TMPM3H** (1b)

Low noise operational amplifier to capture fluctuations in current consumption accurately

Low noise operational amplifier (5)

Small surface mount LDO regulator suitable systems having high noise

Small surface mount LDO regulator (6)

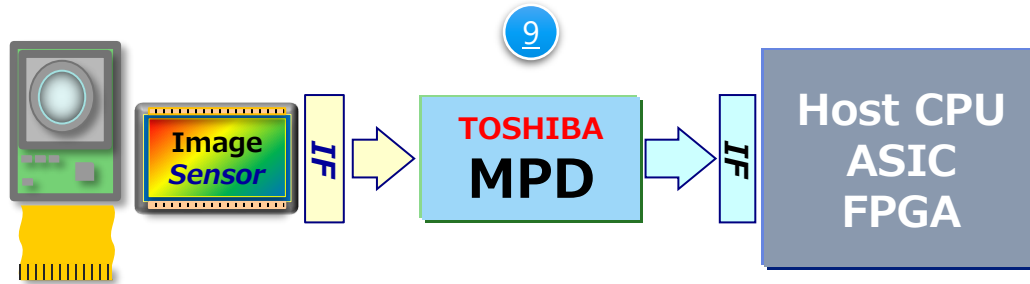
Monitor temperature at multiple points with small package and low current consumption

Over-temperature detection "Thermoflagger™" (8)

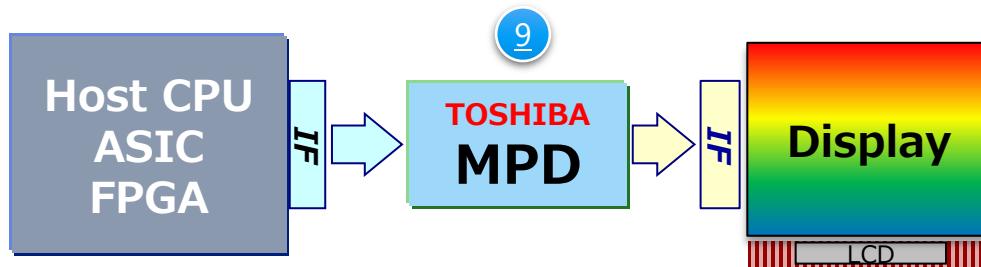
※ Click the number in the circuit diagram to jump to the detailed description page

Sensor / Display Block Diagram Detail

Camera Sensor Interface Bridging to Host CPU interface



Host CPU Interface Bridging to a Display Interface



※ Click the number in the circuit diagram to jump to the detailed description page

Criteria for device selection

- Peripheral device – camera or display – has a different interface to the Host CPU.
- A Toshiba MPD bridge device may be able to help connect the different interfaces
- Ease of design and programmability over I2C/SPI.

Proposal from Toshiba

Bridging different interfaces for cameras or displays

- **TC358xxxXBG** for commercial specs
- **TC959xXBG** for Industrial specs.

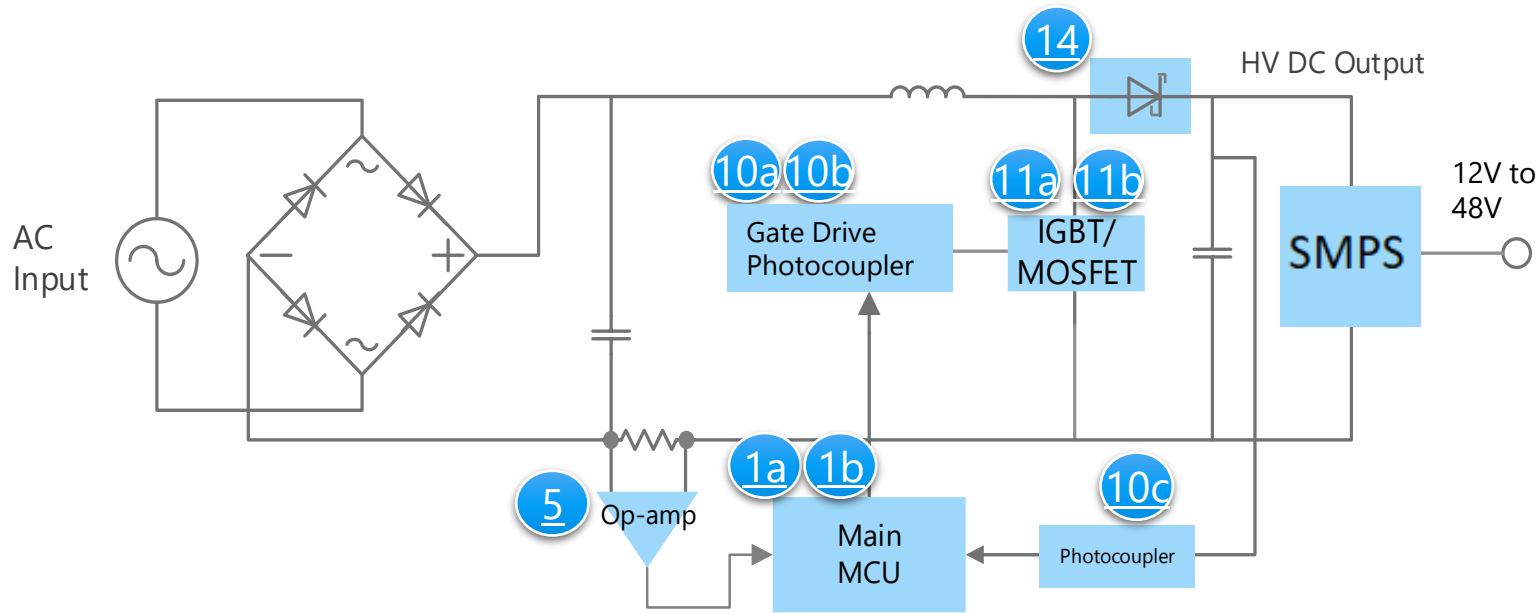
Display Interfaces includes:

- DPISM(RGB), LVDS, MIPI[®] DSISM,
- DisplayPort[™]/eDP, HDMI[®]

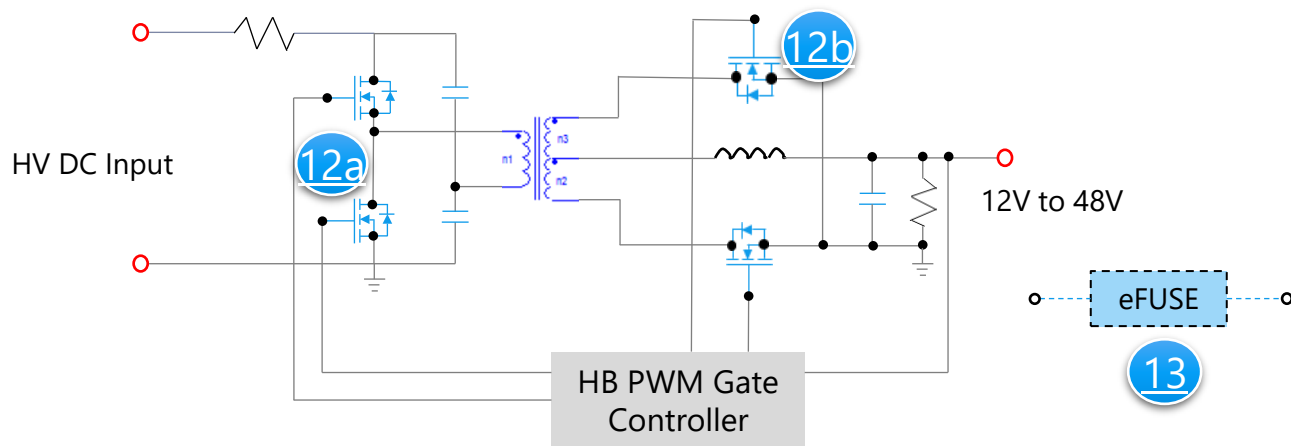
Camera Interfaces includes:

- Parallel (8bit/16bit), MIPI[®] CSI-2SM

AC-DC / PFC



SMPS example using Half Bridge DC-DC:



Criteria for device selection

- Cost, not Efficiency typically main driver
- Main, Motor control MCU can be utilized for PFC control
- SiC Diodes & DTMOS MOSFETs suitable for PFC, SMPS
- U-MOS Series LV MOSFETs suitable for SMPS

Example Proposals from Toshiba

Suitable for power supply switching

IGBT **GT15J341**

SiC 650V MOSFET

U-MOS Series MOSFET **TPH3300CNH** **TPH1400CQ5**

Achieves both high isolation and high functionality

Gate drive photocoupler **TLP5772** **TLP5814**

Transistor Output photocoupler **TLP383**

High current surge resistance and low switching loss

SiC Schottky barrier diode **TRS4E65H**

Motor Control MCU for Inverter & PFC Control

MCU **TMPM4M** **TMPM3H**

Low noise operational amplifier to capture fluctuations in current consumption accurately

Low noise operational amplifier

Protect from short/over voltage event

Efuse IC (optional for <18V lines)

Value provided

System cost down, high efficiency system, development efficiency improvement

1 Built-in Arm® Cortex®-M4 CPU core

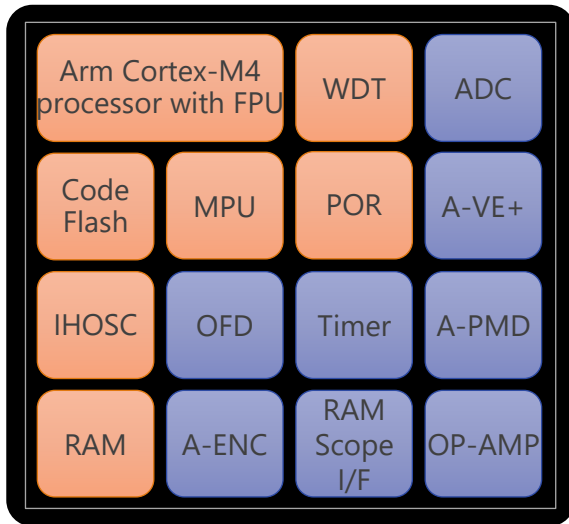
Built-in Cortex-M4 core with Thumb instruction set improves energy efficiency. Various development tool and their partners allow users many options.

2 Suitable for sensing analog signal

Built-in multi-channel ADC and CPU system executes sensing data processing efficiently at low cost

3 Vector Engine - fast precise control of brushless motors

Complicated FOC routines can be off-loaded from CPU to dedicated hardware (Vector Engine) for faster processing and handling of brushless motors



Line up		
Sample Line Up		
Part number	TMPM4MNFWAFG	TMPM4MNFWADFG
Package	LQFP 100	QFP 100
Maximum operation frequency	160 MHz	160 MHz
Code Flash	128 KB	128 KB
Data Flash	32 KB	32 KB
RAM	24 KB	24 KB
Timer	Up to 12ch	Up to 12ch
UART / SIO / CAN	4 / 2 / 1	4 / 2 / 1
I2C	2	2
ADC	32ch (12bit)	32ch (12bit)
I/O (Mux'd - MAX)	87	87

1a TX and TXZ+™ families line up

[Back to AC-DC/PFC](#)

[Back Motor Control MCU](#)

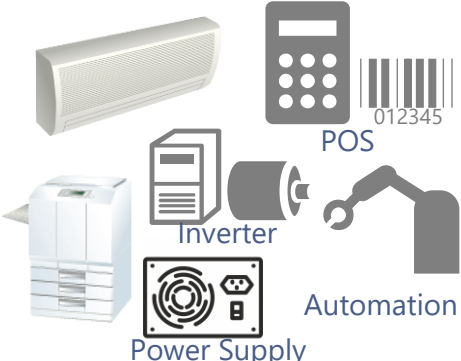
[Back to Micro Peripheral Detail](#)

ARM® Core	M0			M3			M4		
TX 16 to 120MHz	TX00	M030 M060 groups	General Purpose	TX03	M310 M330 M340 M360 M380 M370	Motor Control	TX04	M440 M460 M470	Motor Control
TXZ+™ 120 to 200MHz				TXZ3A+	M3H	General Purpose Consumer Equipment	TXZ4A+	M4K, M4M M4G, M4N	Motor Control Data Processing

TXZ+™3A Series

Consumer Equip
General Purpose

M3H group	Cortex-M3 ~120MHz CPU	2.7~ 5.5V	~1MB ROM	32KB Data Flash	130KB RAM	ADC 12bit DAC 8bit	General I/F SPI/UART /I2C	Motor control PMD Programmable Motor Driver ENC Encoder	Digital LCD Driver
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TXZ+™4A Series

High speed
Data Processing

M4G group	Cortex-M4 ~200MHz CPU+FPU	2.7~ 3.6V	~2MB ROM	32KB Data Flash	~258KB RAM	ADC 12bit DAC 8bit	General I/F SPI/UART /I2C	Motor PMD Programmable Motor Driver	External Bus· Serial Memory I/F	Audio I/F I2S/FIR
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M4N group	Cortex-M4 ~200MHz CPU+FPU	2.7~ 3.6V	~2MB ROM	32KB Data Flash	~258KB RAM	ADC 12bit DAC 8bit	General I/F SPI/UART /I2C	Motor PMD Programmable Motor Driver	External Bus· Serial Memory I/F	Audio I/F I2S/FIR	CAN	Ether MAC	USB
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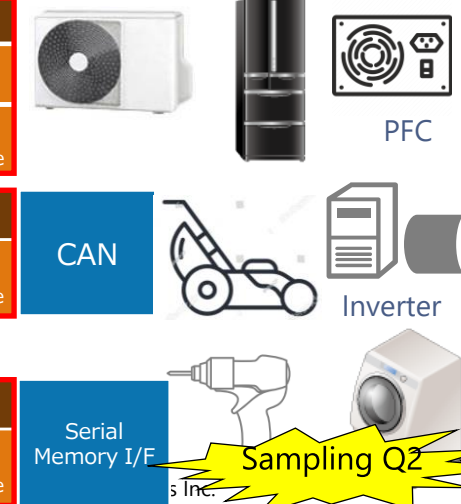
M4K group	Cortex-M4 ~120MHz CPU+FPU	2.7~ 5.5V	~256KB ROM	N/A	~18KB RAM	ADC 12bit	General I/F SPI/UART /I2C	Motor control		
	Cortex-M4 ~160MHz CPU+FPU	4.5~ 5.5V	~1MB ROM	32KB Data Flash	~64KB RAM			PMD Programmable Motor Driver	ENC Encoder	N/A VE Vector Engine

M4M group	Cortex-M4 ~160MHz CPU+FPU	4.5~ 5.5V	~1MB ROM	32KB Data Flash	~64KB RAM	ADC 12bit	General I/F SPI/UART /I2C	Motor control			CAN
								PMD Programmable Motor Driver	ENC Encoder	VE Vector Engine	

TXZ+™4E Series

Motor Control
1 BLDC motor VE

M4L group	Cortex-M4 ~80MHz CPU+FPU	2.7~ 5.5V	~256KB ROM	16KB Data Flash	32KB RAM	ADC 12bit	General I/F SPI/UART /I2C	Motor control			Serial Memory I/F
								PMD Programmable Motor Driver	ENC Encoder	VE Vector Engine	



Sampling Q2

1a TPM4M Vector Engine MCU for BLDC Servo Motor

Arm® Cortex®-M4 MCU 160MHz with Advanced motor control engine and high-precision analog circuits

● Cortex-M4 core, Memory, System Control

- Supply voltage 2.7 to 5.5V
- Max frequency 160MHz
- Operation temperature -40 to 105°C
- Memory Code Flash 256KB to 128KB
- Data Flash 32KB (W/E 100k times)
- RAM 24KB
- Low power Clock gear(1/1 to 1/16 dived)
- Stand-by mode(IDLE/STOP1)
- Built-in OSC(IHOSC) 10MHz
- External interrupt(INT) 22ch
- DMAC 1unit
- Power on reset(POR)/ Low voltage detection(LVD)
- Frequency detection(OFD) 1ch
- Debug circuit JTAG/SW,TRACE(4bit), NBDIF (80,64pins: SW only)
- CRC calculation circuit(CRC) 1ch

● Peripherals (Timer)

- 32(16)-bit timer 6(12)ch
- Independent Watchdog timer(WDT) 1ch

● Peripherals (Analog)

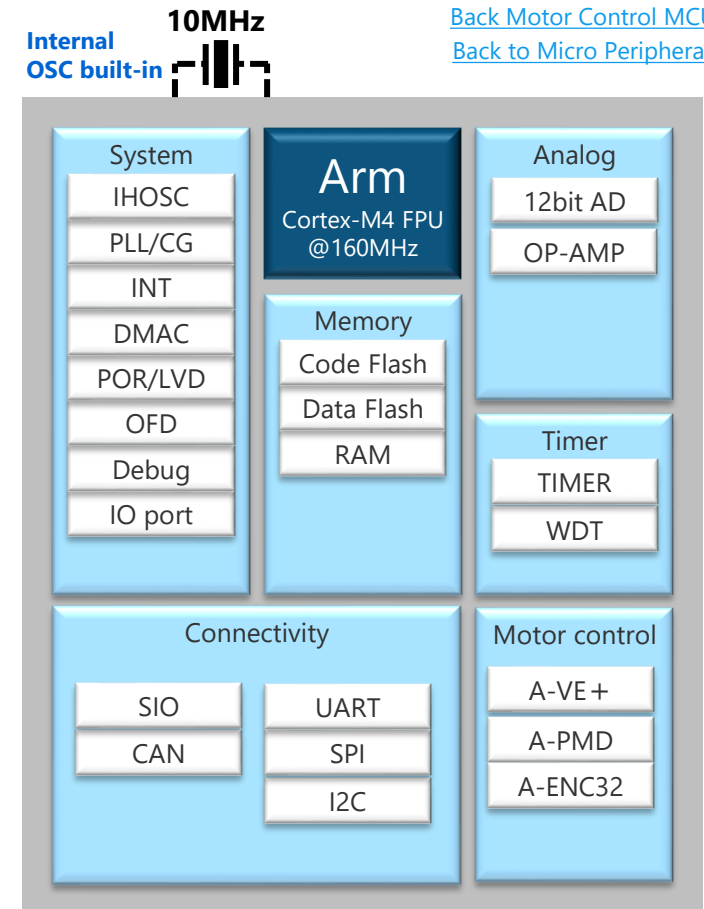
- 12-bit AD converter 32ch (3unit/min. 1μs)
- OP-AMP 3ch

● Peripherals (Connectivity & Motor Control)

- CAN 1ch
- UART 4ch
- TSPI(SPI/SIO) 2ch
- I2C 2ch
- Advanced Vector Engine plus(A-VE+) 1ch
- Advanced programmable motor driver(A-PMD) 3ch
- Advanced encoder input(A-ENC32) 3ch

Part number	Code Flash	Data Flash	RAM	Package
TMPM4MNFYAFG	256KB	32KB	24KB	P-LQFP100-1414-0.50-002
TMPM4MNFYADFG				P-QFP100-1420-0.65-001
TMPM4MMFYAFG				P-LQFP80-1212-0.50-003
TMPM4MLFYAUG				P-LQFP64-1010-0.50-003
TMPM4MLFYAFG	128KB	32KB	24KB	P-LQFP64-1212-0.65-001
TMPM4MNFWAFG				P-LQFP100-1414-0.50-002
TMPM4MNFWADFG				P-QFP100-1420-0.65-003
TMPM4MMFWAFG				P-LQFP80-1212-0.50-003
TMPM4MLFWAUG				P-LQFP64-1010-0.50-003
TMPM4MLFWAFG				P-LQFP64-1414-0.80-002

[Back to AC-DC/PFC](#)
[Back Motor Control MCU](#)
[Back to Micro Peripheral Detail](#)

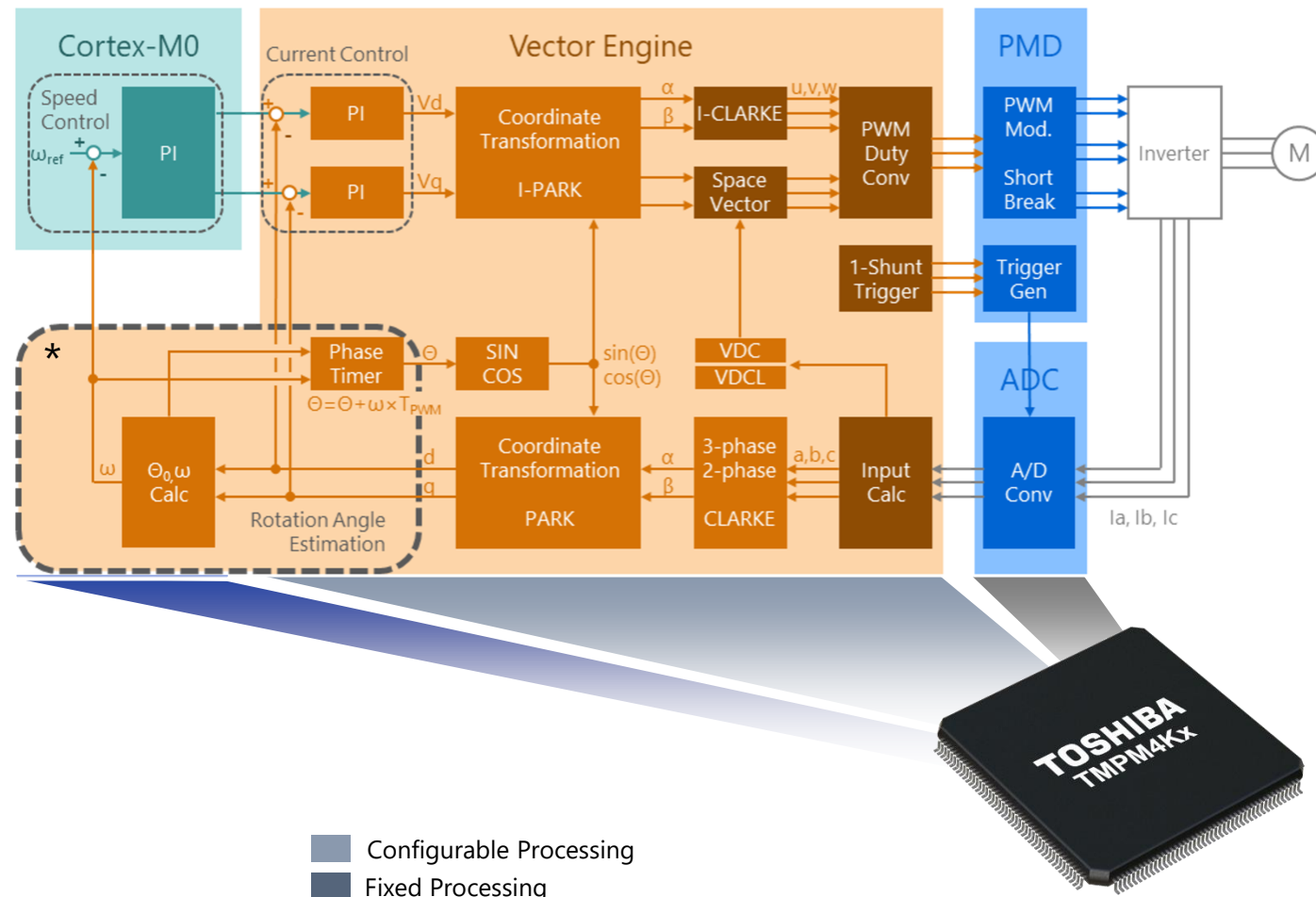


Note1) Specification given in this page is the maximum specification of M4M group(2). Please refer to a function table for details.

For additional information see: [TPM4M](#)

Powerful Hardware CPU Offload Engine for FOC

- PI for current control (d-/q-axis)
- Coordinate transformation (Park / I-Park)
- Phase transformation (Clarke / I-Clarke)
- Space Vector Modulation (SVM)
- Sine and Cosine calculation at phase θ
- Square Root and Arcus Tangent calculation*
- Scheduler for combination of tasks
- Supporting 3-shunt or 1-shunt, 2-phase sensor detection and 2-phase or 3-phase modulation
- PWM shift function (automatic PWM modification for 1-shunt measurement)
- ADC auto-trigger generation with wait function for noise reduction
- Phase timer performing automatic Theta (θ) integration for schedule repetition
- Synchronized interaction between VE, PMD and ADC



* available in VE α

Value provided

Standard MCUs that offer a wide range of lineups with low power consumption and high-performance

1 Global standard Arm® Cortex® -M core

High-performance Arm® Cortex® -M3 core, operating at up to 120MHz

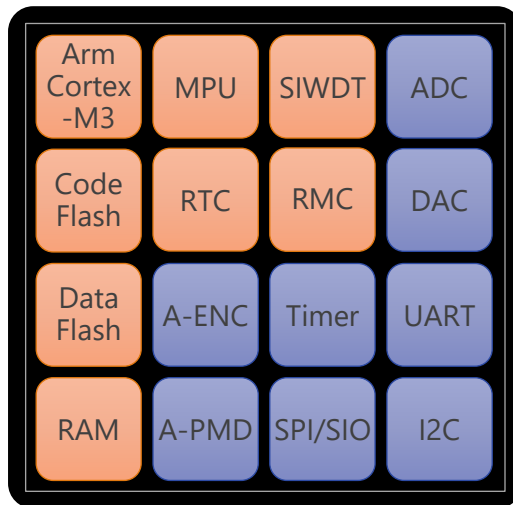
2 General-purpose microcontrollers for a wide range of applications

This group can be used for wide range of applications, such as motor control, consumer and industrial.

ADC, DAC, UART, timer, I2C, SPI/SIO, Programmable motor driver

3 A wide range of memory and package lineup

A wide range of memory and package lineup



Line up

Part number	Code Flash/RAM	Data Flash	Package
TMPM3HQFxyFG	10: 1024KB/130KB D: 512KB/66KB Z: 384KB/66KB Y: 256KB/66KB	32KB	P-LQFP144-2020-0.50
TMPM3HPFxyFG			P-LQFP128-1414-0.40
TMPM3HPFxyDFG			P-LQFP128-1420-0.50
TMPM3HNFxyFG			P-LQFP100-1414-0.50
TMPM3HNFxyDFG			P-QFP100-1420-0.65
TMPM3HMFxyFG			P-LQFP80-1212-0.50
TMPM3HLFxyUG			P-LQFP64-1010-0.50

Note1) "x" of Part number means symbol of code flash size.
 Note2) "y" of Part number means revision. (A: 256KB to 512KB, B: 1024KB)

1b TX and TXZ+™ families line up

[Back to AC-DC/PFC](#) [Back to New Inverter Gate Driver](#)

[Back to Micro Peripheral Detail](#)

ARM® Core	M0	M3	M4
TX 16 to 120MHz	TX00 M030 M060 groups General Purpose	TX03 M310 M330 M340 M360 M380 M370 Motor Control	TX04 M440 M460 M470 Motor Control
TXZ+™ 120 to 200MHz		TXZ3A+ M3H General Purpose Consumer Equipment	TXZ4A+ M4K, M4M M4G, M4N Motor Control Data Processing

TXZ+™3A Series

Consumer Equip
General Purpose

M3H group	Cortex-M3 ~120MHz CPU	2.7~ 5.5V	~1MB ROM	32KB Data Flash	130KB RAM	ADC 12bit DAC 8bit	General I/F SPI/UART /I2C	Motor control PMD Programmable Motor Driver ENC Encoder	Digital LCD Driver	
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TXZ+™4A Series

High speed
Data Processing

M4G group	Cortex-M4 ~200MHz CPU+FPU	2.7~ 3.6V	~2MB ROM	32KB Data Flash	~258KB RAM	ADC 12bit DAC 8bit	General I/F SPI/UART /I2C	Motor PMD Programmable Motor Driver	External Bus· Serial Memory I/F	Audio I/F I2S/FIR	
M4N group	Cortex-M4 ~200MHz CPU+FPU	2.7~ 3.6V	~2MB ROM	32KB Data Flash	~258KB RAM	ADC 12bit DAC 8bit	General I/F SPI/UART /I2C	Motor PMD Programmable Motor Driver	External Bus· Serial Memory I/F	Audio I/F I2S/FIR	CAN Ether MAC USB

Motor Control
2 BLDC motor non-VE
3 BLDC motor VE

M4K group	Cortex-M4 ~120MHz CPU+FPU Cortex-M4 ~160MHz CPU+FPU	2.7~ 5.5V 4.5~ 5.5V	~256KB ROM ~1MB ROM	N/A 32KB Data Flash	~18KB RAM ~64KB RAM	ADC 12bit	General I/F SPI/UART /I2C	Motor control PMD Programmable Motor Driver ENC Encoder N/A VE Vector Engine	
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Motor Control
3 BLDC motor VE
and CAN

M4M group	Cortex-M4 ~160MHz CPU+FPU	4.5~ 5.5V	~1MB ROM	32KB Data Flash	~64KB RAM	ADC 12bit	General I/F SPI/UART /I2C	Motor control PMD Programmable Motor Driver ENC Encoder VE Vector Engine	CAN	
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TXZ+™4E Series

Motor Control
1 BLDC motor VE

M4L group	Cortex-M4 ~80MHz CPU+FPU	2.7~ 5.5V	~256KB ROM	16KB Data Flash	32KB RAM	ADC 12bit	General I/F SPI/UART /I2C	Motor control PMD Programmable Motor Driver ENC Encoder VE Vector Engine	Serial Memory I/F	
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Sampling Q2

1b M3H Group Overview

[Back to AC-DC/PFC](#)
[Back to New Inverter Gate Driver](#)
[Back to Micro Peripheral Detail](#)

Arm® Cortex®-M3 core max frequency 120MHz

Large code & data Flash, Motor control, LCD display control circuit

● Cortex-M3 core, Memory, System Control

- Supply voltage 2.7 to 5.5V
- Max frequency **120MHz**
- Operation temperature -40 to **105°C**
- Memory
 - Code Flash 1024KB to 256KB (W/E **100k times**)
 - Data Flash 32KB (W/E **100k times**)
 - RAM(with parity) 130KB to 66KB
 - Clock gear(1/1 to 1/16 divided) Stand-by

mode(IDLE/STOP1/STOP2)

- Built-in OSC(IHOSC) 10MHz
- External interrupt(INT) 32ch
- DMAC 2unit
- Power on reset(POR)/ Voltage detection circuit(LVD)
- Frequency detection(OFD) 1ch
- Debug interface JTAG/SWD/SWV, 2bit trace
- CRC calculation circuit(CRC) 1ch CRC32, CRC16

● Memory, Package

Part number	Code Flash/RAM	Data Flash	Package
TMPM3HQFxyFG	10: 1024KB/130KB D: 512KB/66KB Z: 384KB/66KB Y: 256KB/66KB	32KB	P-LQFP144-2020-0.50
TMPM3HPFxyFG			P-LQFP128-1414-0.40
TMPM3HPFxyDFG			P-LQFP128-1420-0.50
TMPM3HNFxyFG			P-LQFP100-1414-0.50
TMPM3HNFxyDFG			P-QFP100-1420-0.65
TMPM3HMFxyFG			P-LQFP80-1212-0.50
TMPM3HMFxyDFG			P-LQFP64-1010-0.50
TMPM3HLFxyUG			

Note1) "x" of Part number means symbol of code flash size.

Note2) "y" of Part number means revision. (A: 256KB to 512KB, B: 1024KB)

● Peripherals (Analog)

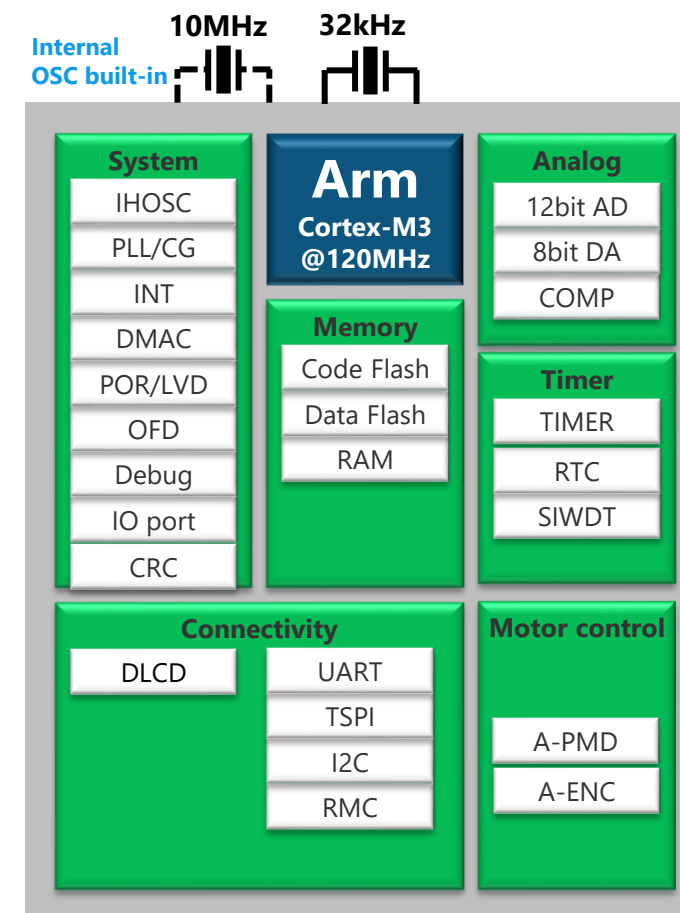
- 12-bit AD converter 21ch (1unit/min 1μs)
Supporting functional safety design
- 8-bit DA converter 2ch
- Comparator(COMP) ※EMG

● Peripherals (Timer)

- 32(16)-bit timer 8(16)ch
- Real time clock(RTC) 1ch
- Independent Watchdog timer(SIWDT) 1ch

● Peripherals (Connectivity & Motor Control)

- **LCD display control circuit 40seg x 4com**
- UART **8ch**
- TSPI(SPI/SIO) 5ch
- I2C 4ch
- Remote control signal processor(RMC) 1ch
- Advanced programmable motor driver(A-PMD) 1ch
- Advanced encoder input(A-ENC) 1ch



Note3) Specification given in this page is the maximum specification of M3H group. Please refer to a function table for details.

M3H Group Function Table

Support wide range of applications by peripheral functions

[Back to AC-DC/PFC](#)
[Back to New Inverter Gate Driver](#)
[Back to Micro Peripheral Detail](#)

		M3H Group				
		M3HLFxy	M3HMFxy	M3HNFxy	M3HPFxy	M3HQFxy
Basic Features	CPU	Arm Cortex-M3				
	Max frequency	120MHz				
	Built-in oscillation frequency	10MHz (+/- 1.0%)				
	Number of IO	57(56 [†])	73(72 [†])	93(92 [†])	119(118 [†])	135(134 [†])
	External interrupt	12	15	19	31	34
Timer	32(16)-bit timer	8(16)				
	PWM output 32(16)-bit	6(12)	6(12)	7(14)	8(16)	8(16)
	Watchdog timer	Yes				
	RTC	Yes (using an external quartz resonator : 32.768kHz)				
Analog	12-bit ADC (input Ch/unit) Minimum conversion time:1.0μsec	12/1	12/1	17/1	19/1	21/1
	8-bit DAC	2	2	2	2	2
Connectivity	UART	7	7	8	8	8
	TSPI(SPI/SIO)	1	4	4	5	5
	I2C	2	3	3	4	4
Peripherals	DMA controller (Ch/unit)	54/2	62/2	62/2	64/2	64/2
	RMC	Yes				
	Advanced PMD (3 phase PWM)	1				
	Advanced encoder input	1				
	LCD driver (Digital)	—	26 x 4	32 x 4	40 x 4	40 x 4
Supply voltage		2.7 to 5.5V				
Package	Pin count	64	80	100	128	144

(Note) "y" of Part number means revision. (A: 256KB to 512KB, B: 1024KB), †: Number of ports of revision B.

Value Provided

3-phase Inverter gate driver IC simplifies connections between the MCU/controller, FET inverters stages and sensor feedback from BLDC motors

1 Excellent Flexibility with Multiple Modes of Operation

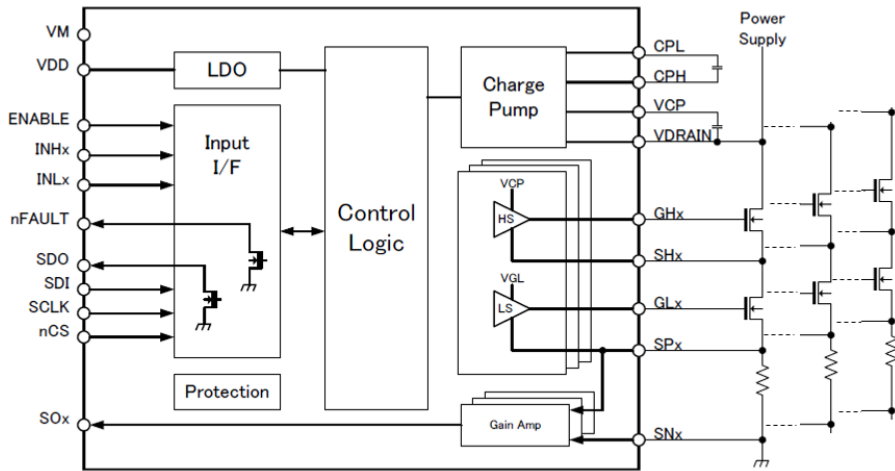
- 6 PWM input mode
- 3 PWM input mode
- Half input mode
- Independent PWM input mode

2 Strong Current Driving Capability with Wide Voltage Operating Range

- Power Supply Voltage: 8V to 75V
- Gate Drive Current: 1A Source/2A Sink
- Choice of Hardware or SPI interfaces

3 Low Loss and Power Efficiency

- Low standby power – 1uA (typ.)
- Adjustable Slew Rate Control
- Automatic Optimization of Source/Sink Currents



QFN32 5x5 or QFN40 6x6

Line Up

Part Number	TB67Z830HFTG	TB67Z830SFTG	TB67Z833HFTG	TB67Z833SFTG
Power Supply Voltage	8V to 75 V (operating range)			
Gate Drive Current	1A source, 2A sink			
Interface	Hardware	SPI	Hardware	SPI
Integrated Current Sense Amplifier			Yes - 3	Yes - 3
LDO Output	3.3V			
Other Features	Adjustable Slew Rate Control Automatic Optimization of Source/Sink Currents & Deadband Protections UVLO, CPUV, OCP, TSD, GDVM, nFault			
Package	QFN 32	QFN 32	QFN 40	QFN 40

2 TB67Z830H/833H/850H/853HFTG Inverter Gate Drivers for <72V Brushless Applications

TB67Z830S/833S/850S/853SFTG (P2P compatible to TI DRV8320S/H and DRV8323S/H)

[Back Motor Control MCU](#)
[Back to New Inverter Gate Driver](#)

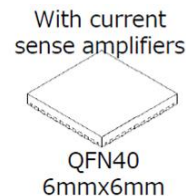
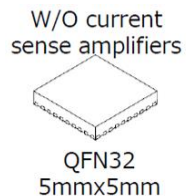
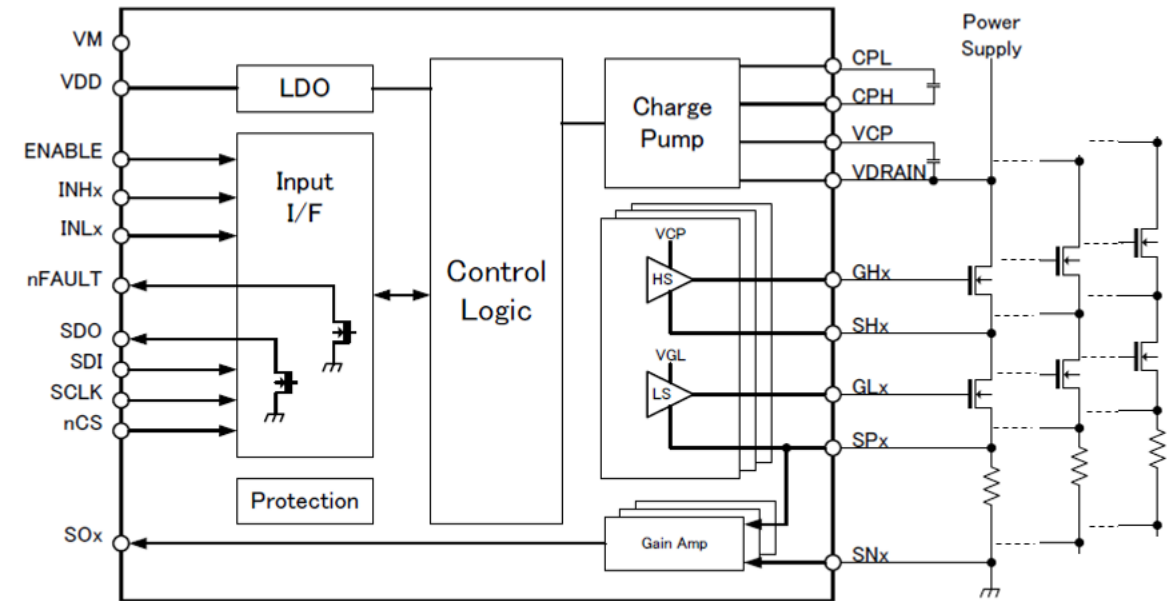
Target Applications

- **Power Tools, Gardening Equipment,** Garage Door and Gate Openers, Robotics, eBike, Handheld Construction Tools, Outboard Motors, LV Inverters

Features

- Pin-to-pin compatible to TI gate drivers
 - TB67Z830HFTG / TB67Z850HFTG → DRV8320H
 - TB67Z830SFTG / TB67Z850SFTG → DRV8320S
 - TB67Z833HFTG / TB67Z853HFTG → DRV8323H
 - TB67Z833SFTG / TB67Z853SFTG → DRV8323S
- Wide Operation Range **8 to 75V**
- Drive external Nch + Nch FETs
 - Source current – 0.01 to **1A**
 - Sink current – 0.02 to **2A**
- Low standby power **1uA** typ
- Built-in 3.3V (Z830/Z833) or 5V (Z850/Z853) linear regulator
- Built-in 3 current sense amplifiers (Z833 and Z853)
- Interface Hardware (xxxHFTG) or SPI (xxxSFTG)
- Adjustable Slew Rate Control
- Automatic Optimization of Source/Sink Currents & Deadband
- Protections UVLO, CPUV, OCP, TSD, GDVM, nFault
- Can also be used as Brushed Motor Controller with Gate Driver

Block Diagram



2 TB67Z830/833/850/853 Selection Guide

[Back Motor Control MCU](#)
[Back to New Inverter Gate Driver](#)

	Linear voltage Regulator	Current Sense Amplifier	Interface	Package	Pin Compatible TI Device
TB67Z830HFTG	3.3V	0	Hardware	QFN32	DRV8320H
TB67Z830SFTG			SPI		DRV8320S
TB67Z833HFTG		3	Hardware	QFN40	DRV8323H
TB67Z833SFTG			SPI		DRV8323S
TB67Z850HFTG	5V	0	Hardware	QFN32	DRV8320H
TB67Z850SFTG			SPI		DRV8320S
TB67Z853HFTG		3	Hardware	QFN40	DRV8323H
TB67Z853SFTG			SPI		DRV8323S

Product part number:

TB67Z8 **3** **0** **H** **FTG**

Linear Voltage Regulator

3: 3.3V
5: 5.0V

Interface

H: Hardware
S: SPI

Number of Current Sense Amplifiers

2 TB67Z8xx Modes of Operation

Supports 4 Modes of Operation Simplifying Interfacing to Controllers

[Back Motor Control MCU](#)
[Back to New Inverter Gate Driver](#)

6-PWM Input Mode

- Each Half Bridge controlled Independently w/ 2 Pin input control
- Half Bridges can be set to LOW, HIGH or Hi-Z
- Typically used for more Flexible 3-Phase BLDC motor control

INLx	INHx	GLx	GHx	SHx
L	L	L	L	Hi-Z
L	H	L	H	H
H	L	H	L	L
H	H	L	L	Hi-Z

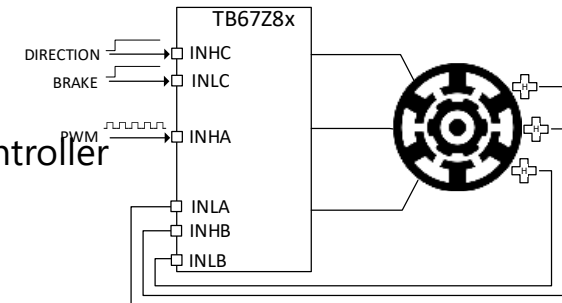
3-PWM Input Mode

- Each Half Bridge controlled w/ Single (INLx) input PWM for Simpler control
- Hi-Z set using INLx input as Enable

INLx (Enable)	INHx (PWM)	GLx	GHx	SHx
L	X	L	L	Hi-Z
H	L	H	L	L
H	H	L	H	H

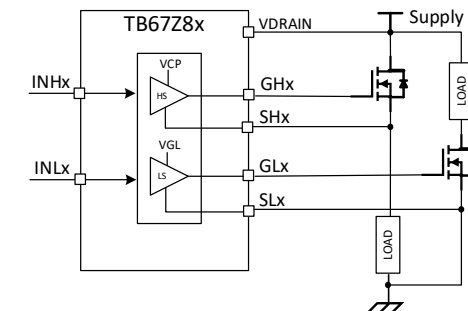
Hall Input Mode

- PWM Controlled by Single (INHA) Input controlling frequency and duty cycle
- For Easy Realization of 120-degree Commutation (6-Step)
- INLA, INHB & INLC Connect to HALL sensor outputs, can also connect to external controller
 - (eg for sensorless operation)
- INHC & INLC set direction and BRAKE respectively
- Typically for Synchronous Rectification but Async rectification possible w/ SPI



Independent PWM Mode

- Each High-Side and Low-Side gate driver can be controlled Independently
- Enables driving of separate Loads
- Typical uses include Brushed DC motors, HS & LS Switches & Solenoids
- Shoot through not prevented by TB67Z8xx in this mode



2 TB67Z8xx Sense Amplifiers & Protection Circuits

[Back Motor Control MCU](#)

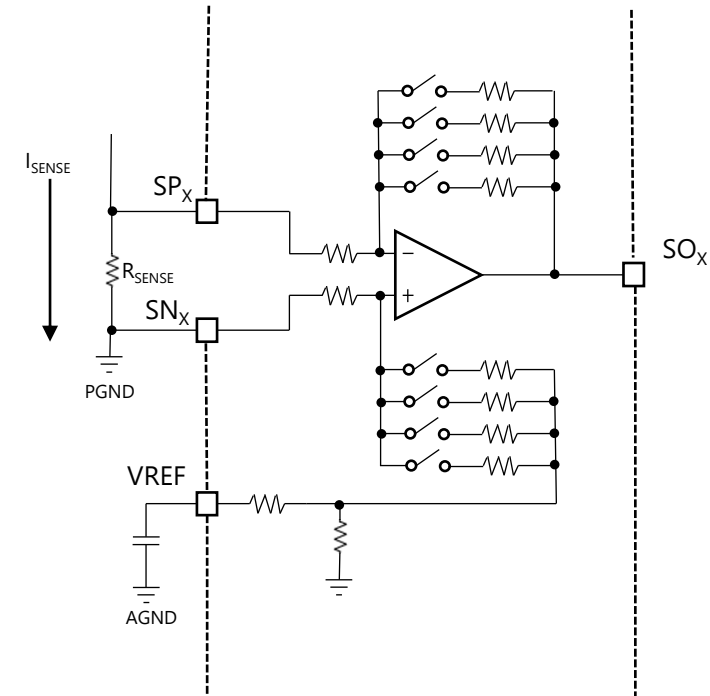
[Back to New Inverter Gate Driver](#)

Integrated Sense Amplifiers

- Available in TB67Z8x3x Part Options
- 3 Separate Amplifiers for current Sensing
- Programmable gain 5,10,20 or 40V/V
- Calibration Function

Multiple Built-in Protection Functions **offloading** burden on Controller

- VM Under Voltage Lockout
- Charge Pump Low Voltage Operation Prevention (CPUV)
- Overcurrent Protection (OCP)
 - Two modes, Vds OCP and Vsense OCP
- Gate Drive Voltage Monitor (GDVM)
 - Can Detect Gate shorts as well as insufficient Idrive or Tdrive setting
- Thermal Shutdown Circuit (TSD)
- Fault Flag Output (nFAULT)



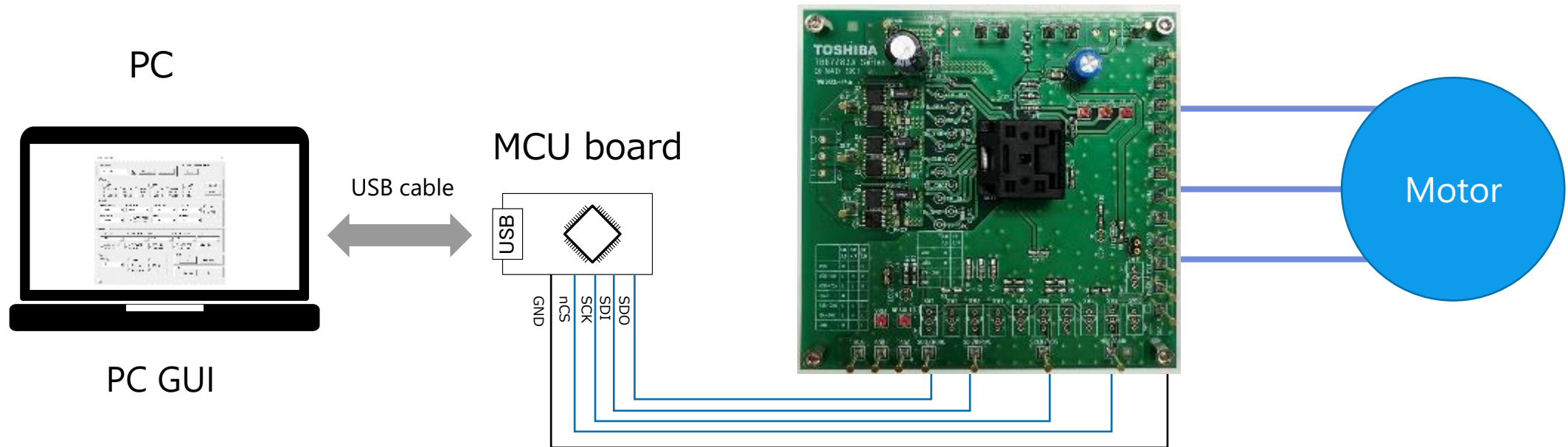
2 Evaluation kit for SPI EVB

[Back Motor Control MCU](#)

[Back to New Inverter Gate Driver](#)

- Configurable through SPI interface
- On-board 3-phase inverter utilizing 80V/120A TPH2R408QM FETs
- External 3-phase connection
- GUI available for easy parameter setting

TB67Z833 Series EVB
SPI version

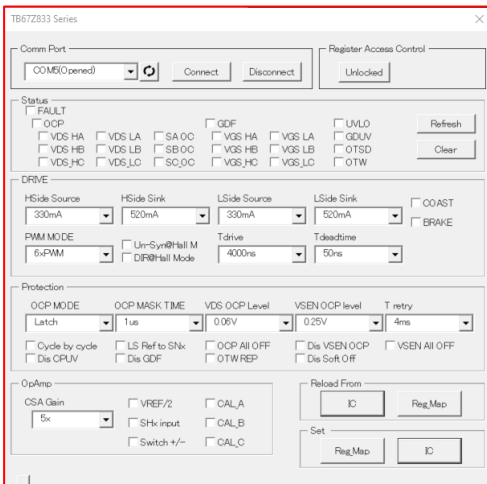


2 TC67Z853S Evaluation: Hybrid Evaluation System Demo w/ Motor Studio

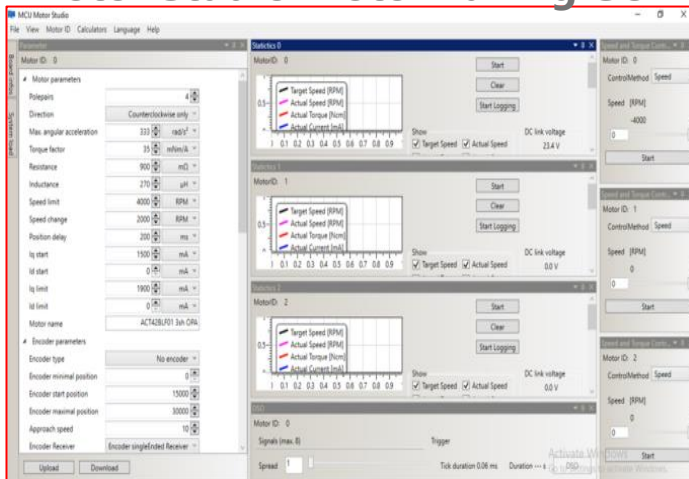
Combines Toshiba Inverter gate driver w/ Toshiba Vector Engine MCU for full motor Control Capability

[Back Motor Control MCU](#)
[Back to New Inverter Gate Driver](#)

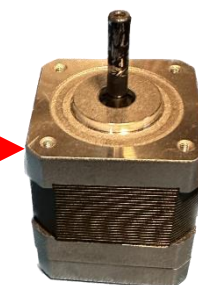
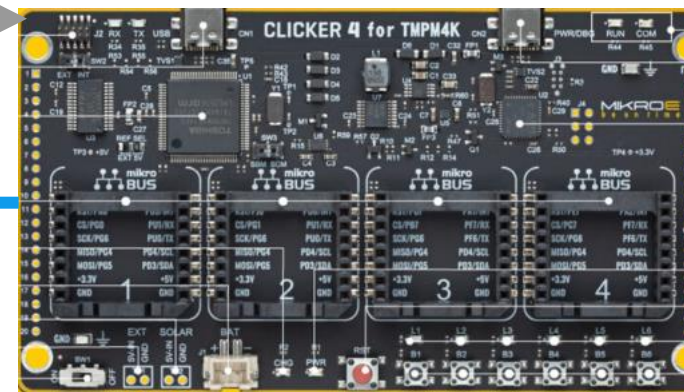
TC67Z853S GUI



Motor Studio Motor Tuning GUI



TMPM4K VE-MCU for Initialization & Motor Control



PC GUI



Inverter Shield w/ TB67Z853S

w/ **SSM6K819** FETs
V_{dss} 100V
I_d=10A

[Back to Simplified Lower Performance](#)

Value provided

A closed loop design allows these motor controllers to run without constant speed adjustment from a host MCU

1 Simple sensor-less brushless motor control

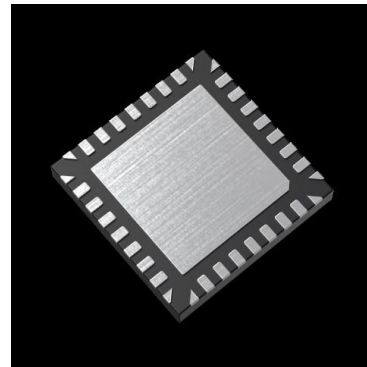
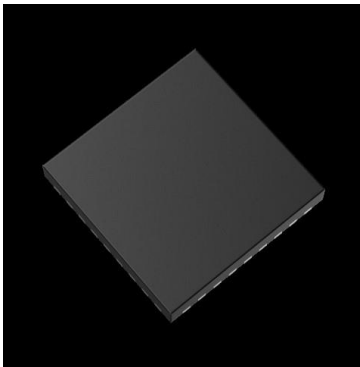
With built-in closed loop speed control with adjustable speed curve, no external MCU host is needed to constantly control these brushless motor control devices

2 Integrated Gate Driver

. Integrated pre driver for high side and low side N-channel MOSFETs – helps reduce external components

3 Small package

In a small QFN36 package.



Line up		
Part Number	TC78B009FTG	TC78B011FTG
Drive	Trapezoidal	Sinewave
V_M (Range) [V]	5.5V to 27	
Features	Lead angle can be set	Automatic lead angle control
	Capable of driving 3 or 4 wire WYE or Delta Configured Motors	
	PWM Duty / Analog voltage / I2C input	
	Current Monitoring function	
	Non-Volatile Memory Storage settings	
	Low-Standby Current 1uA	
	FG/Alert signal feedback	
	Error Detection (over current, temp, etc)	
Package	QFN 36 5mm x 5mm	

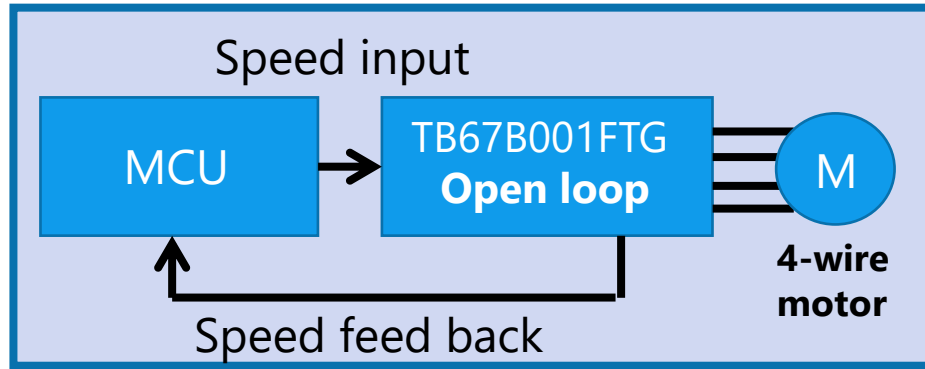
3 Choosing the control architecture – Open Loop or Closed Loop?

[Back to Simplified Lower Performance](#)

TC78B009FTG/TC78B011FTG can handle both open loop and closed loop control.

Case1: 4-wire WYE Configured BLDC motor

Motor speed controlled directly by MCU



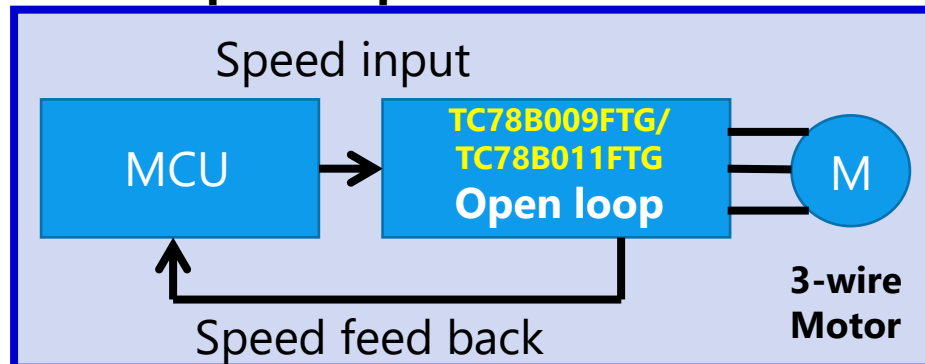
An open loop control is best used in an application where it often requires rapid change of speed. The motor speed is directly controlled by the MCU.

A closed loop control is best with steady speed and the speed does not change often. (i.e. Garage door and gate openers).

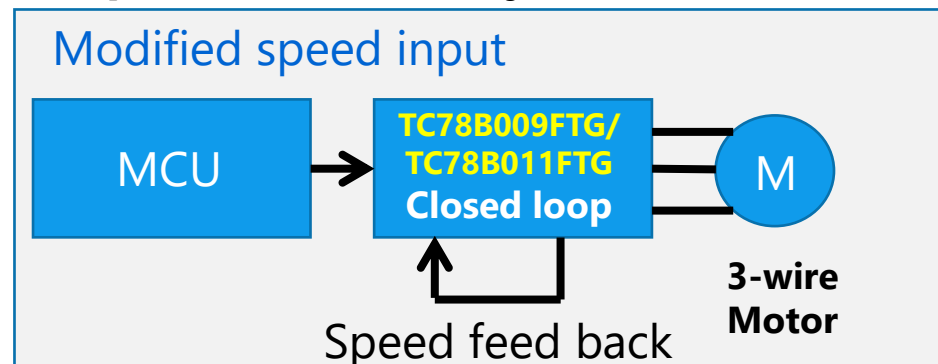
TC78B009FTG/TC78B011FTG can handle both configurations!

Case2: 3-wire WYE or Delta Configured BLDC motor

Same open loop control architecture



Speed controlled by TC78B009FTG



3 TC78B009FTG 30V, 3-Phase Sensorless Trapezoidal BLDC Controller+Gate-Driver

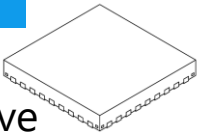
[Back to Simplified Lower Performance](#)

Features

- Wide Operating Range 5.5 - 27V
- Integrated 200mA Gate Driver w/Slew Rate Ctrl
- **Current Monitoring Function**
- **Closed Loop Speed Control Regulate Speed Automatically without a Host**
- **Analog, PWM, or I2C Control**
- Low Standby Current 1uA
- Non-Volatile Memory Storing Settings
- FG/Alert signal feedback
 - Over current, Over Temp, Charge Pump Error, and Motor Lock Detections. Under voltage lockout
- **Drive 3/4-Wire Wye or Delta Configured Motors**

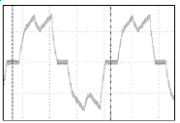
Package

- **Compact QFN36 5x5mm² Package**
- Pin to pin compatible to TC78B011FTG Sinewave Drive

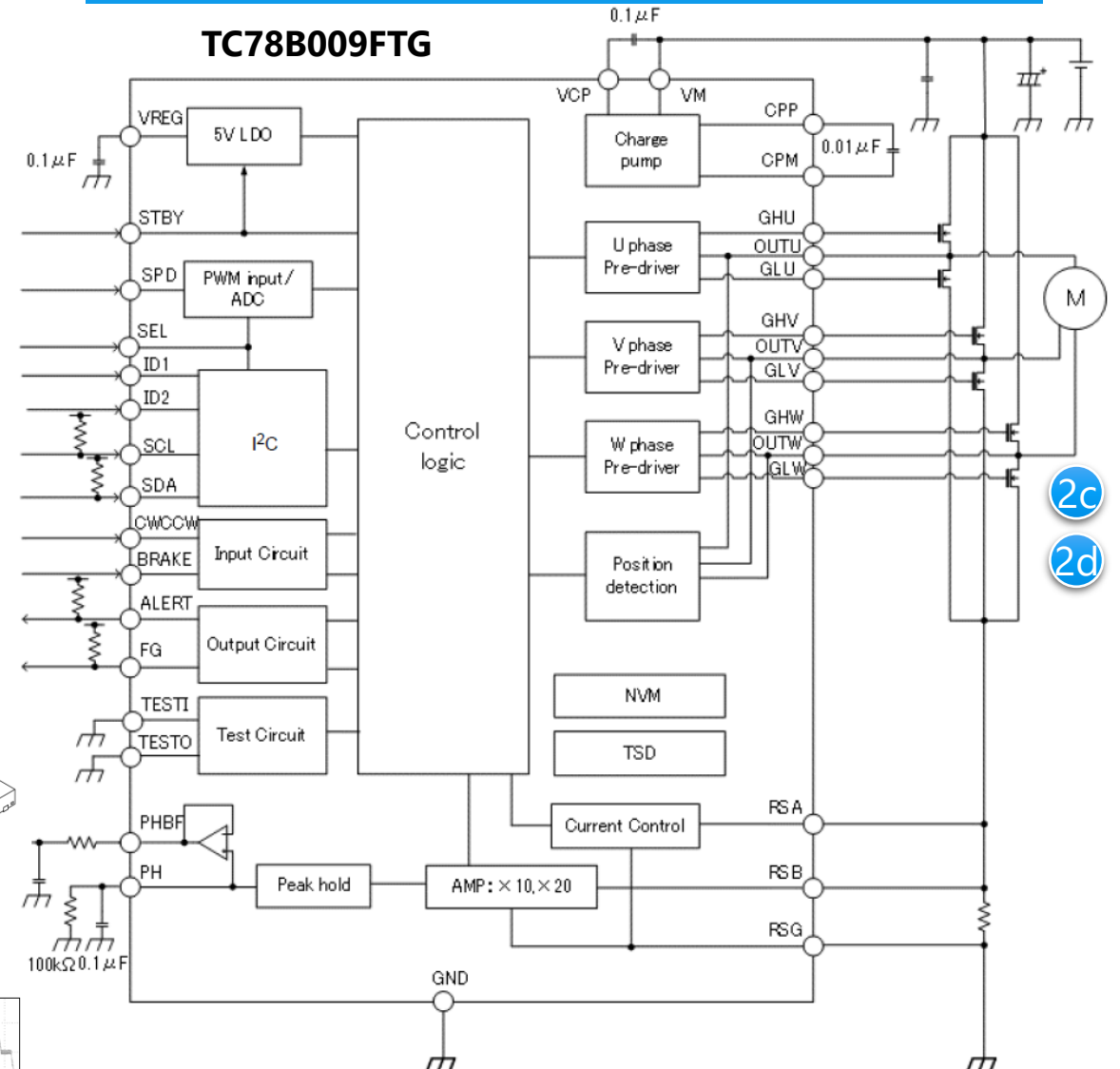


Applications

- Ideal for Fan, Pump and Garage/gate motors



Block Diagram



3 TC78B011FTG 30V, 3-Phase Sensorless Sinewave BLDC Controller+Gate-Driver

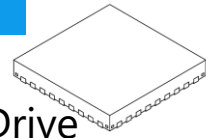
[Back to Simplified Lower Performance](#)

Features

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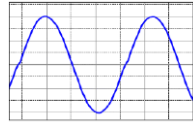
Package

- **Compact QFN36 5x5mm² Package**
- Pin to pin compatible to TC78B009FTG Trapezoidal Drive

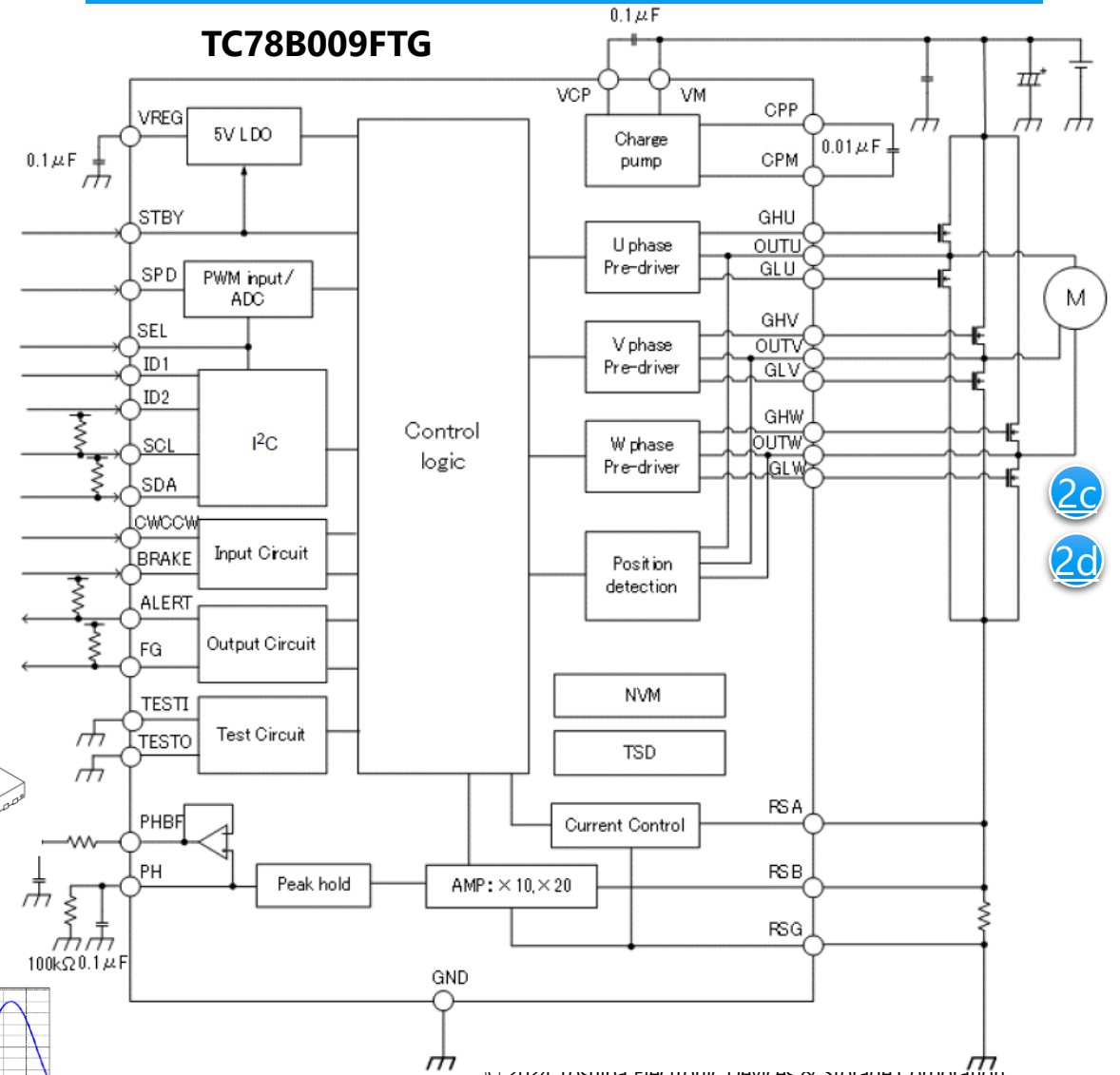


Applications

- Ideal for Fan, Pump and Garage/gate motors



Block Diagram



Value Provided

100V LV MOSFETs can be used in 3-phase inverter design to drive BLDC servo motors with highspeed power switching along with fast reverse recovery

1 Low $R_{DS(on)}$

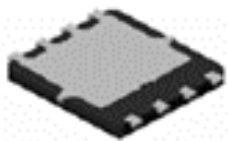
Low $R_{DS(on)}$ specs can enable efficient driving of 48V BLDC motors up to 200W

2 Body Diode for fast recovery and response

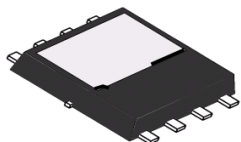
Faster response for BLDC servo motors can be expected from U-MOSIX-H process, and even a faster response from U-MOS11-HSD

3 Low I_{DSS}

Low leakage currents of 10 μ A @ $V_{DS}=100V$ enables low power draw even when subjected to absolute ratings.



SOP Advanced 5x6
- 8 pin



Dual Sided Cooling DSPOP
Advanced 5x6
- 8 pin

Line Up

Part Number	TPH3R70APL	TPW3R70APL	TPH2R70ARS
Process	U-MOSIX-H	U-MOSIX-H	U-MOS11-HSD
$R_{DS(on)}$ m Ω (typ.)	3.1	3.1	2.7
$Q_{rr}(di/dt=100A/\mu s)$ (nC)	74	74	59
Body Diode	Yes	Yes	Yes – High Speed
Package	SOP Advanced 8	DSPOP Advanced 8	SOP Advanced 8
MP Status	MP	MP	ES available (MP 2Q 2025)

Value

U-MOS11-H 100V series provides best-in class performance and improve the efficiency.

1 Low drive losses

- $R_{DS(ON)} \times Q_g$ reduction (Cell design optimization)
- Large contribution at high frequency, especially in light load

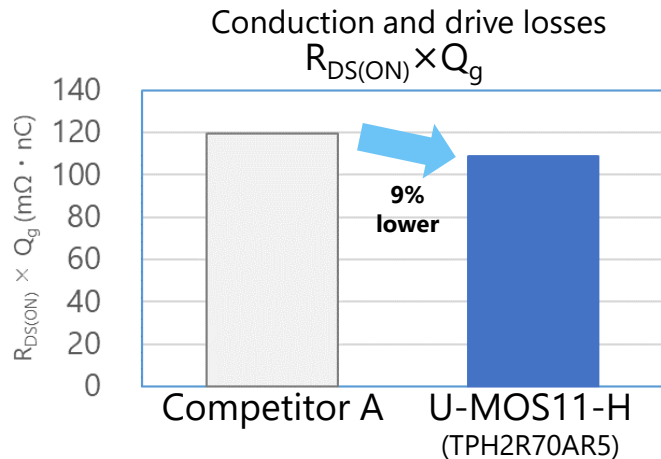
2 Low Switching losses

- $R_{DS(ON)} \times Q_{SW}$ reduction (Cell design optimization)
- Large contribution at high frequency, especially in light load

3 Low Recovery losses

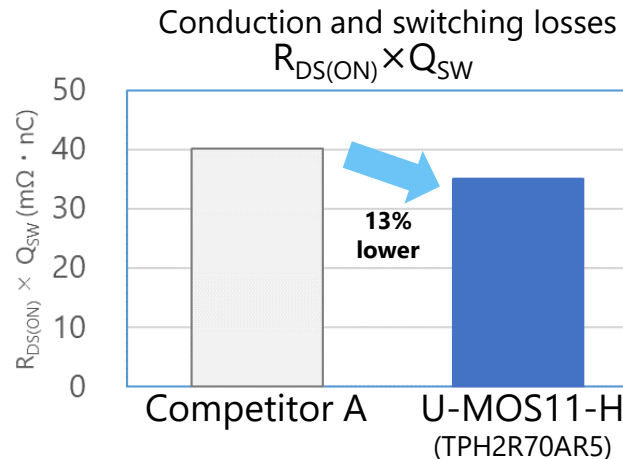
- Q_{rr} reduction (Applying Lifetime control technology)
- Large contribution at the secondary side synchronous rectifier circuit of the switching power supply unit

FOM Comparison with the Competition



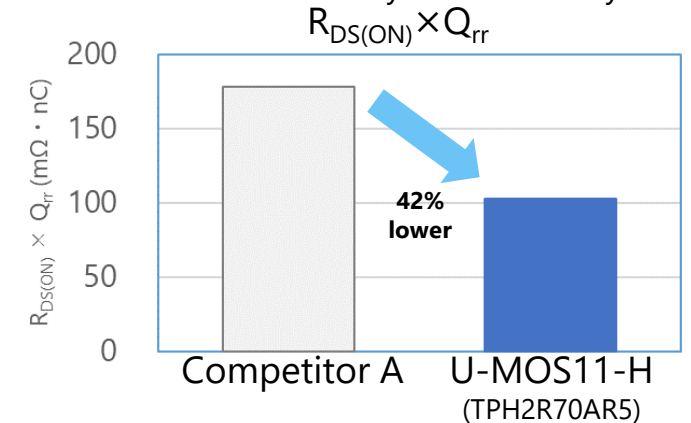
$R_{DS(ON)}$: On-resistance (figure of merit for conduction loss)
 Q_g : Gate charge (figure of merit for drive loss)

FOM : Figure of Merit



Q_{SW} : Gate switch charge (figure of merit for switching loss)
 Q_{rr} : Reverse recovery charge (figure of merit for body diode recovery loss)

As of August 2023 (as surveyed by Toshiba)
 Conduction and Body diode recovery losses



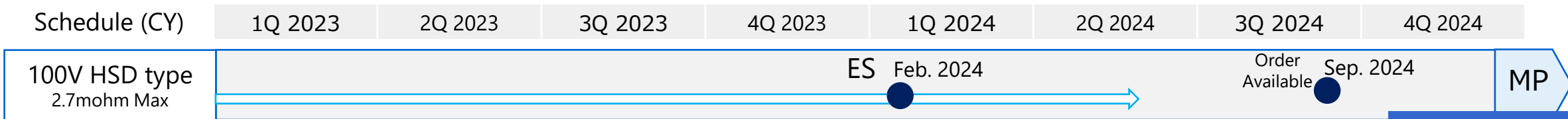
4

U-MOS11-H 100V Development Plan

Under planning : STD Type
Under development : HSD Type

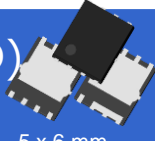



[Back Motor Control MCU](#)
[Back to New Inverter Gate Driver](#)
[Back to Simplified Lower Performance](#)

Performance of U-MOS11-H HSD is better than competitors.



ES Available

STD version is planned also (ES Sep. 2024)

Item	U-MOS11-H(HSD) (TPH2R70AR5)  5 x 6 mm	U-MOSIX-H (TPH3R70APL)  5 x 6 mm	Competitor A  5 x 6 mm	Competitor B  5 x 6 mm				
T_{ch}	175degC	★★★★	175degC	★★★★	175degC	★★★★	150degC	★
V_{th}	2.9V~4.3V Drive voltage : 8V	★★	1.5V~2.5V Drive voltage : 4.5V	★★	2.3V~3.3V Drive voltage : 8V	★★	2.0V~4.0V Drive voltage : 6V	★
$R_{DS(ON)}$ @ $V_{GS}=10V$ typ.	2.3 mΩ	★★★★	3.1 mΩ	★★	2.3 mΩ	★★★★	3.3 mΩ	★
$Q_{g(VDS=50V)}$	49 nC	★★	67 nC	★	58 nC	★★	42 nC	★★★★
$Q_{SW(VDS=50V)}$	16 nC	★★★★	21 nC	★	17 nC	★★	16*1 nC	★★★★
$Q_{oss(VDS=50V)}$	108 nC	★★	74 nC	★★★★	107 nC	★★	116*1 nC	★
$Q_{rr}(dI/dt=100A/us)$	59*1 nC	★★★★	74*1 nC	★★	81*1 nC	★	58*1 nC	★★★★

Significant Improvement

*1 Measured value on same condition

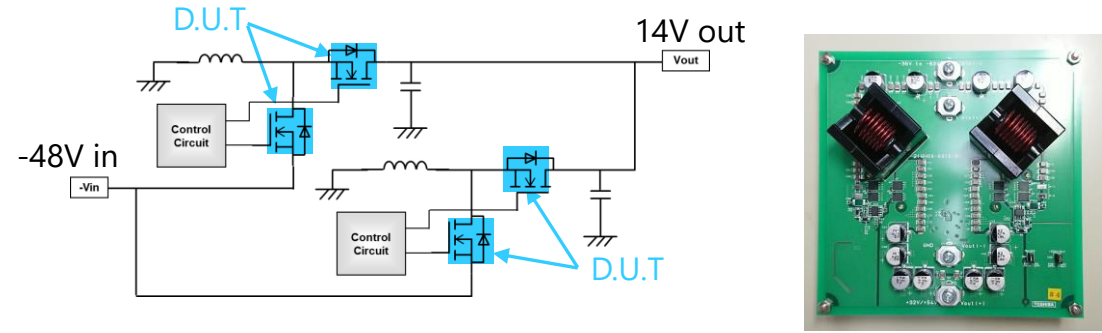
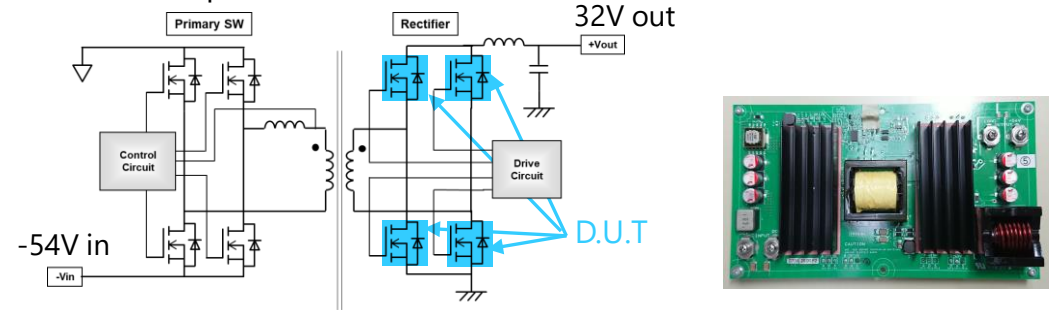
4 Comparison in 100V among U-MOS11-H HSD and Competitors

ES Available

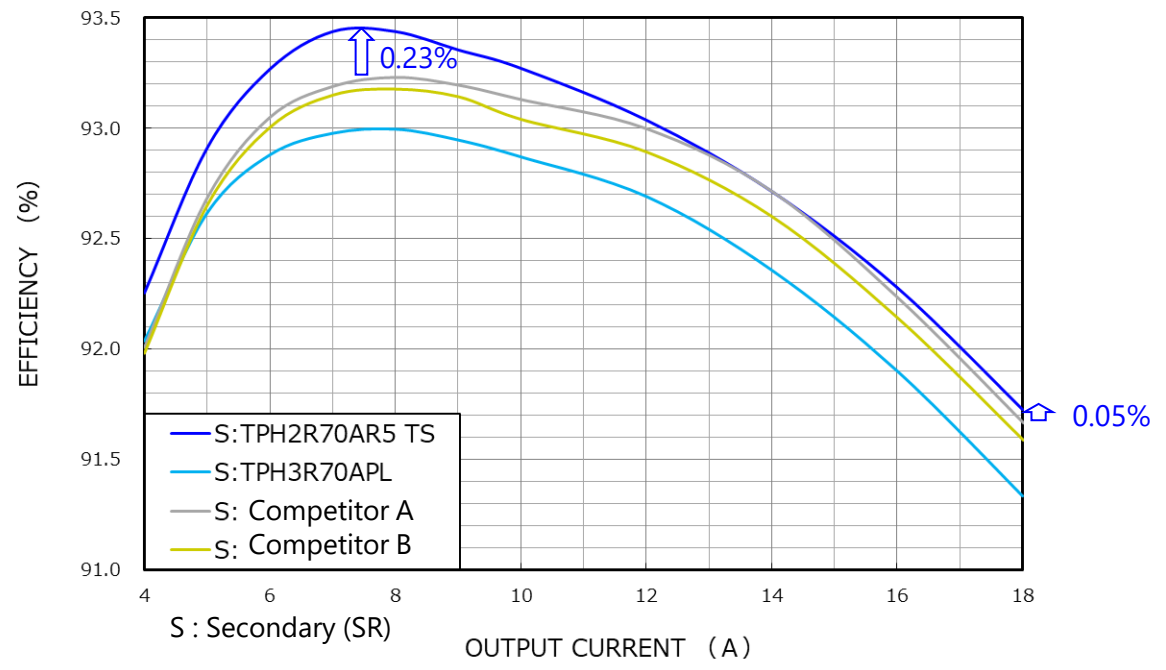
Performance of U-MOS11-H shows best performance.

[Back Motor Control MCU](#)
[Back to New Inverter Gate Driver](#)
[Back to Simplified Lower Performance](#)

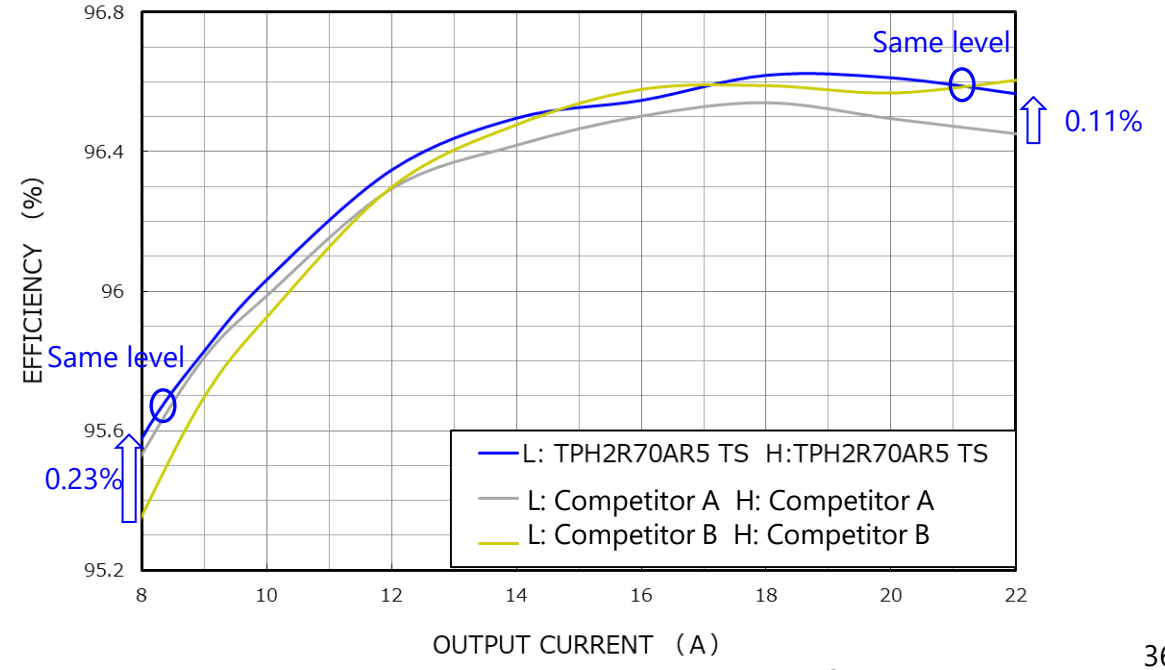
Test circuit and picture



(Reference) 1kW Isolated DC-DC reference design result
(Primary : TPH3R70APL Vout : 32V / f : 113kHz)



(Reference) 1kW Buck-Boost reference design result
(Vin : -48V / Vout : 14.5V / f : 150kHz)



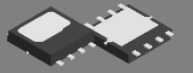
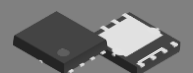
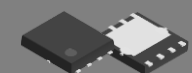
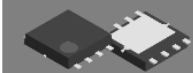
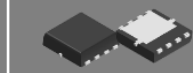
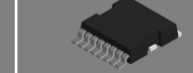



4

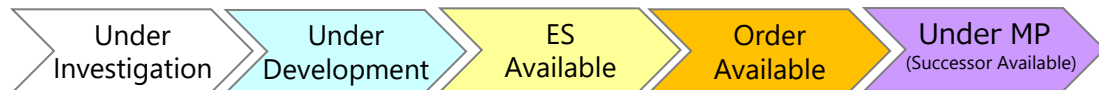
Line up plan of U-MOS 100V products

Under development : HSD Type

[Back Motor Control MCU](#)
[Back to New Inverter Gate Driver](#)
[Back to Simplified Lower Performance](#) (CY)

U-MOS11-H Line-up

R _{DS(ON)} Max (V _{GS} = 10V) (mohm)	Package														
	DSOP Advance (5x6 dual sided cooling)  5 x 6 mm	SOP Advance(E) (5x6)  5 x 6 mm	SOP Advance(N) (5x6)  5 x 6 mm	SOP Advance (5x6)  5 x 6 mm	TSON Advance (3x3)  3 x 3 mm	L-TOGL (TOLL leaded)  10 x 12 mm	DPAK (TO-252) 	TO-220 	TO-220SIS (Fullpak) 						
50							TK7S10N1Z	48							
40															
30					TPN3300ANH	33	TK11S10N1L(L)	28							
20															
15				TPH1400ANH	13.6	TPN1600ANH	16		TK22E10N1	13.8	TK22A10N1	13.8			
12						TPN1200APL(L)	11.5								
10								TK110P10PL(L)	10.6	TK110E10PL(L)	10.7	TK110A10PL(L)	10.8		
8					TPH8R80ANH	8.8	S1TR8 (N11-H) (ES Aug. 2024)	8.7		TK7R7P10PL(L)	7.7	TK40E10N1	8.2	TK40A10N1	8.2
			TPH8R10APL(L) (Order Mar. 2025)	8.1							TK7R2E10PL(L)	7.2	TK7R4A10PL(L)	7.4	
6					TPH6R30ANL(L)	6.3			TK60S10N1L	6.11	TK6R4E10PL(L)	6.4	TK6R7A10PL(L)	6.7	
					TPH5R60APL(L)	5.6									
5	TPW4R50ANH Conventional type	4.5			TPH4R50ANH1	4.5					TK65E10N1	4.8	TK65A10N1	4.8	
4	TPW3R70APL(L) Conventional type	3.7			S1TP3 (N11-H) (ES Sep. 2024)	4.3	TPH4R10ANL(L)	4.1			TK3R9E10PL(L)	3.9	TK4R1A10PL(L)	4.1	
					TPH3R70APL1(L)	3.7							TK100A10N1	3.8	
3											TK100E10N1	3.4	TK3R2A10PL(L)	3.2	
					TPH3R10AQM	3.1					TK2R9E10PL(L)	2.9			
2.5				TPH2R70AR5 (HSD) (Order Sep. 2024)	2.7										
2															
1									XPQ1R00AQB (Order 2H 2025)	1.03					



Note : Specifications and schedule of under development is just target and is subject to change without notice.

5 Low noise operational amplifier

TC75S67TU

Low-loss,
low-heat,
and power
efficiency

Small size
packages

Value Provided

[Back to AC-DC / PFC](#)

[Back to Microcontroller Peripheral](#)

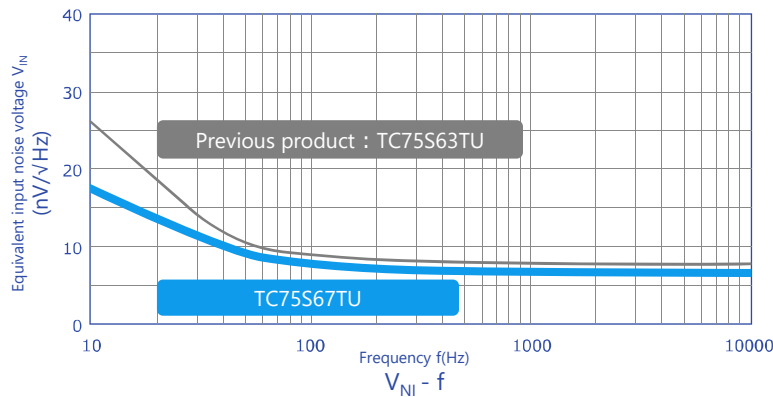
Very small signals detected by various sensors can be amplified with very low noise.

1 Low noise
 $V_{NI} = 6.0$ [nV/ $\sqrt{\text{Hz}}$] (Typ.)
@f = 1 kHz

Very small signals detected by various sensors [Note 1] can be amplified with low noise using CMOS operational amplifier by optimizing the processing, we achieved one of the industry's lowest [Note 2] input equivalent noise voltage.

Low noise
characteristics

(Toshiba internal comparison)




2 Low current consumption
 $I_{DD} = 430$ [μA] (Typ.)

The low current consumption characteristics of the CMOS processing contributes to the extension of battery life of the compact IoT devices.

3 Low voltage operation

Can be operated in case of $V_{DD} = 2.2$ to 5.5 V.

Line Up

Part number	TC75S67TU
Package	UFV 
$V_{DD,SS}$ (Max) [V]	± 2.75
$V_{DD,SS}$ (Min) [V]	± 1.1
I_{DD} (Max) [μA]	700
V_{NI} [nV/ $\sqrt{\text{Hz}}$] (Typ.) @f = 1 kHz	6

[Note 1] Sensor types: vibration detection sensor, shock sensor, accelerometer, pressure sensor, infrared sensor, temperature sensor, etc. [Note 2] Based on Toshiba data (May 2017).

For more information see: [TC75S67TU](#)

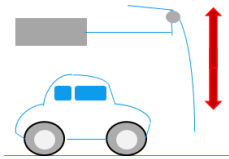
We have a lineup of Op amp suitable for various applications.

Low Noise type

TC75S67TU

Ideal for amplifying small signals .

Application example



Garage Door /
Gate Openers

New!!

I/O Rail to Rail type

TC75S102F
TC75S103F

It can operate a wide voltage signal from GND to the power supply voltage, it is ideal for low power supply voltage operation.

Application example



Battery-operated
Equipment,
Various sensors

low Quiescent current type

TC75S102F

New!!

Ideal for long-time operation equipment.

Application example



Battery-operated
equipment such as
PIR sensors,
smoke sensors

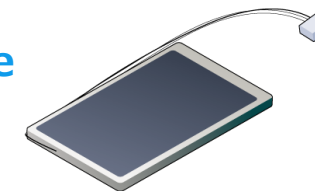
**Under
planning**

Ultra Low input offset voltage type

TC75S105F

Ideal for high precision signal amplification.

Application example



Current sensor

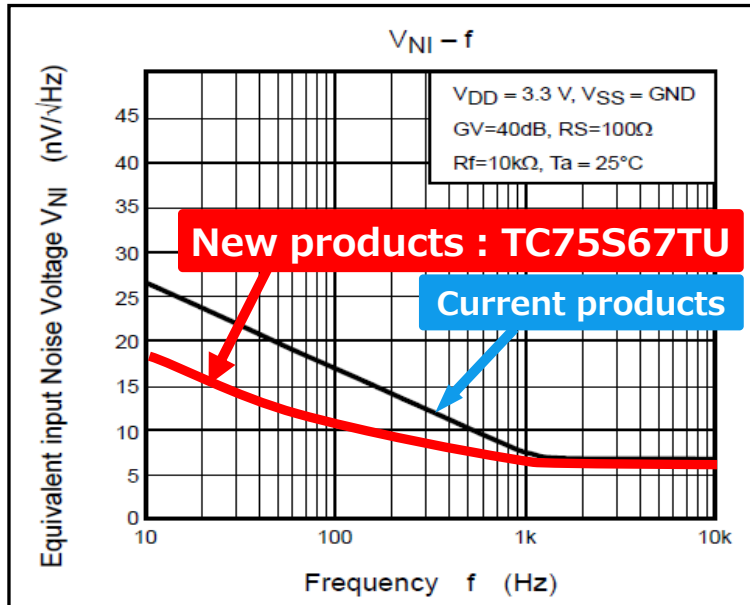
5 Toshiba Op Amp Technology

[Back to AC-DC / PFC](#)
[Back to Microcontroller Peripheral](#)

Toshiba operational amplifier ICs contribute to high-performance sensing technology with excellent performance.

New!!

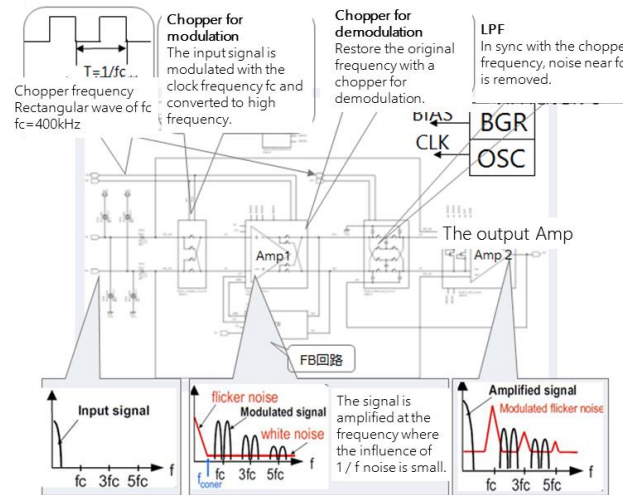
Low Noise Performance



1/f noise and thermal noise reduced.

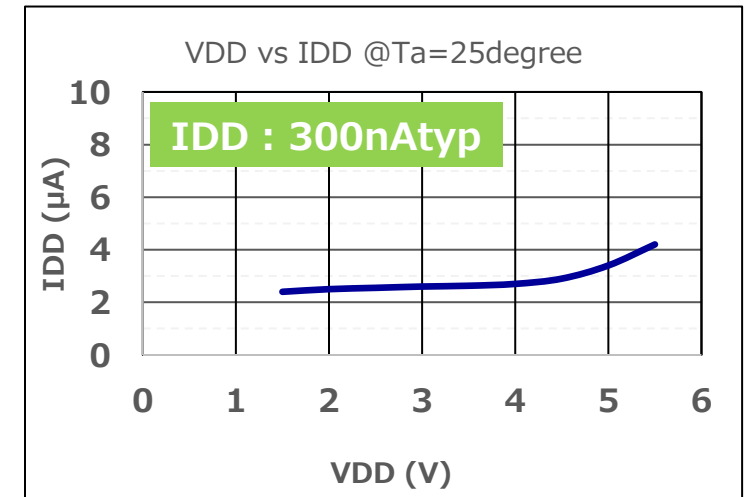
Ultra low input offset voltage.

$V_{IO} : 40\mu\text{VMax}$



We are developing ultra low offset voltage Op Amp using chopper circuit technology.

Ultra low Quiescent current.



TC75S102 series data.

It is a few μA , ultra low Quiescent current.

<https://toshiba.semicon-storage.com/us/product/linear/opamp-and-comparator.html>

5 Toshiba Op Amp Roadmap

[Back to AC-DC / PFC](#)
[Back to Microcontroller Peripheral](#)

~2021

2022

2023

2024~

Toshiba operational amplifiers contribute to high-performance sensing technology with various lineups

TC75S102F Low IDD New!!

VDD 1.5 to 5.5V
 Rail to Rail
 IDD:0.3 μ A
 VIO:1.3mV:Max



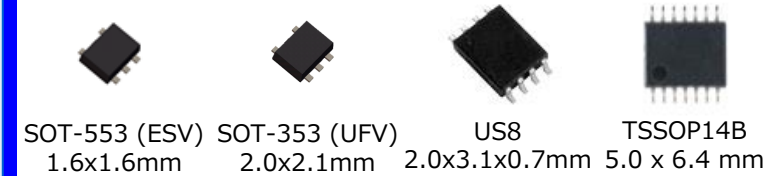
Ultra Low input offset voltage

Low Quiescent current. Low input offset voltage

Low Quiescent current
Low input offset voltage
1in1 (small PKG)/2in1/4in1

Under planning

TC75S/W/Q102/103xx



TC75S103F Low VIO New!!

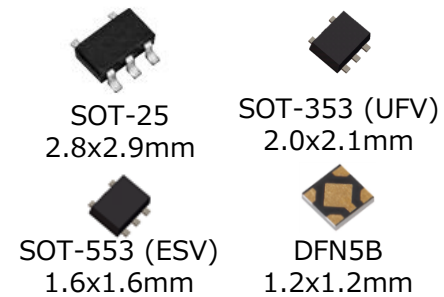
TC75S103F
 VDD 1.8 to 5.5V
 Rail to Rail
 IDD:100 μ A
 VIO:1.7mV:Max



TC75S105 series*

Under planning

VDD 1.8 to 5.5V
 Rail to Rail
 VIO:40 μ V:Max



* Sequential development from SMV package

5

Ultra Low Noise Op Amp ICs TC75S67TU

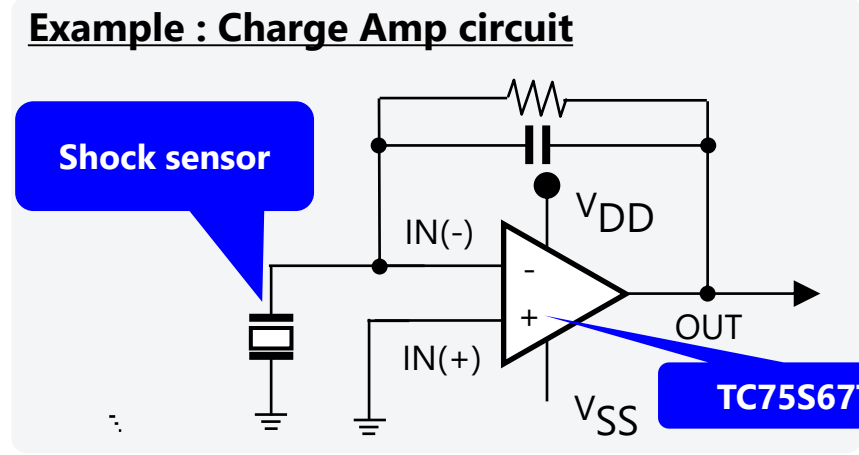
[Back to AC-DC / PFC](#)
[Back to Microcontroller Peripheral](#)

TC75S67TU

Ultra Low Noise OP-Amp

For sensor, IoT

SOT-353(UFV)
(2.1x2.0mm)



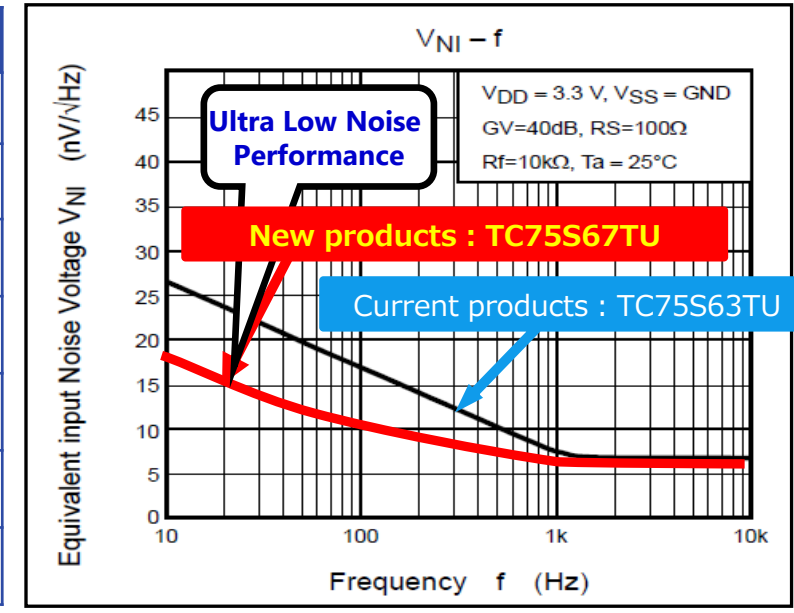
Feature

- Equivalent input Noise Voltage(V_{NI})
6.0nV/ $\sqrt{\text{Hz}}$ (typ.)
10nV / $\sqrt{\text{Hz}}$ (max)
@f=1kHz
- V_{DD} max.: 6V
- V_{IO} drift : 1 $\mu\text{V}/\text{deg}$
- CMV_{IN} : 0 to 1.3V @ $V_{DD}=2.5\text{V}$
- CMRR:70dB (min)
- I_{DD} :500 μA (typ.)

Characteristics

ITEM	Characteristics
V_{DD}/V_{EE}	2.2 to 5.5V
I_{DD}	500 μA (typ.)
Gv	80dB (min.)
V_{NI} @f=1kHz	10nV/ $\sqrt{\text{Hz}}$ (Max)
Slew rate	1.0V/ μs (typ.)
fT	3.5MHz (typ.)
Type	Ultra low noise

Equivalent input Noise Voltage



5

Low Input Offset voltage type Op Amp ICs

[Back to AC-DC / PFC](#)
[Back to Microcontroller Peripheral](#)

New!!

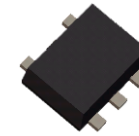
Under planning

TC75S102/103xx

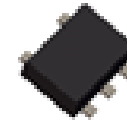
- Low Input Offset voltage
- Low VIO temperature drift
- I/O Full Range type



SOT-25(SMV)
2.8x2.9x1.1mm



SOT-353 (UFV)
2.0x2.1x0.7mm



SOT-553 (ESV)
1.6x1.6x0.55mm

Characteristics		CMOS process	
		TC75S102xx	TC75S103xx
V_{DD}, V_{SS}		1.5 to 5.5V	1.8 to 5.5V
I_{DD}		0.3μA	100μA
VIO, VIO drift		1.3mV:Max	1.3mV:Max
fT		0.6kHz	350kHz
SR		0.35V/ms	0.6V/μs
Type		<u>Ultra low IDD</u>	<u>Low IDD</u>
Single	SOT-25(SMV) (2.9x2.8mm)	New!! TC75S102F	TC75S103F New!!
	SOT-353(UFV) (2.0x2.1mm)	Under planning TEST Sample : Available	TC75S102TU Under planning TEST Sample : Available
	SOT-553(ESV) (1.6x1.6mm)	Under planning TEST Sample : Available	TC75S103FE Under planning TEST Sample : Available

5

Ultra low input offset voltage type Op Amp: TC75S105xx

[Back to AC-DC / PFC](#)
[Back to Microcontroller Peripheral](#)

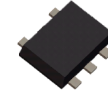
Under planning

TC75S105xx

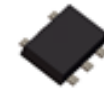
- Ultra low input offset voltage : Built-in chopper circuit
- Small thermal drift (Offset voltage)
- I/O Full Range type



SOT-25(SMV)
2.8x2.9x1.1mm



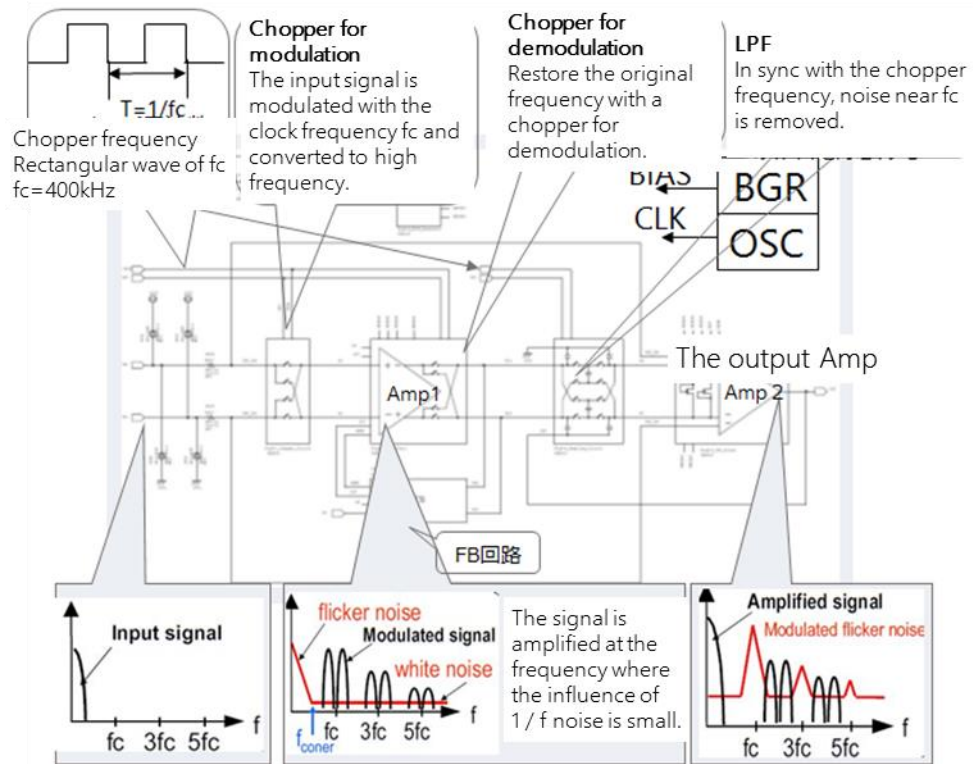
SOT-353 (UFV)
2.0x2.1x0.7mm



SOT-553 (ESV)
1.6x1.6x0.55mm

* Sequential development from SMV package

Target spec Ultra low input offset voltage



Characteristics	CMOS process
	TC75S105 Series
V_{DD}/V_{SS}	1.8~5.5V
I_{DD}	75μA:Typ
V_{IO}	40μV:Max
f_T	790kHz

*Above target specification and schedule may change without notice

5 Operational Amplifier ICs line up

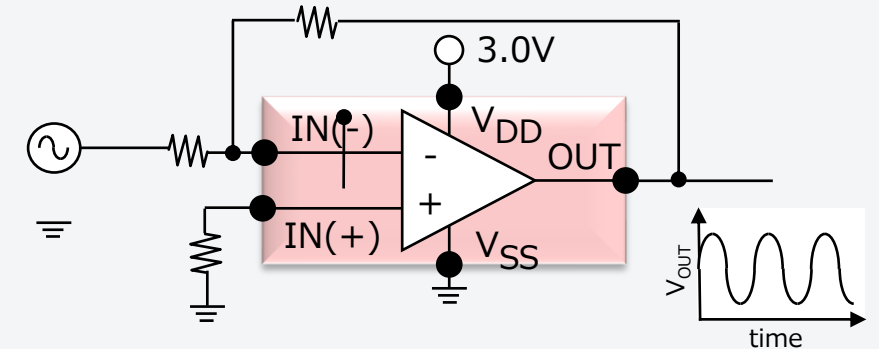
[Back to AC-DC / PFC](#)

[Back to Microcontroller Peripheral](#)

Operational Amplifier series with small package



Application example: Sensor amp.



Features		Bi-polar		CMOS						
		TA75S01 TA75W01	TA75S558 TA75W558	TC75S51 TC75W51	TC75S54 TC75W54	TC75S55 TC75W55	TC75S63	TC75S67	TC75S102	TC75S103
$V_{CC}, V_{EE} / V_{DD}, V_{SS}$		3.0 to 12V	4 to 18V	1.5 to 7.0V	1.8 to 7.0V	1.8 to 7.0V	2.2 to 5.5V	2.2 to 5.5V	1.5 to 5.5V	1.8 to 5.5V
I_{CC} / I_{DD}		0.4mA	2.5mA	60uA	100uA	10uA	500uA	430uA	0.27uA	100uA
Isink		20mA	40mA	---	700uA	450uA	1.5mA	3.5mA	0.4mA	2mA
Gv		100dB	100dB	70dB	70dB	70dB	100dB	100dB	139dB	100dB
VNI		---	2.5μVrms@f=30Hz to 30kHz	---	---	---	7.8nV/√Hz@f=1kHz	6nV/√Hz@f=1kHz	---	---
Slew rate		---	1.0V/μs	0.5V/μs	0.7V/μs	0.08V/μs	1.0V/μs	1.0V/μs	0.37V/ms	0.52V/us
fT		0.3MHz	3.0MHz	0.6MHz	0.9MHz	0.16MHz	3.5MHz	3.5MHz	0.5kHz	0.3MHz
Type		---	Low noise Wide band	Low voltage operation	Standard	Low I_{DD}	Low noise	Low noise	Input and output full range	Low I_{DD} Input and output full range
Single	SOT-353(USV) (2.0*2.1mm)	---	---	TC75S51FU	TC75S54FU	TC75S55FU	TC75S63TU*	TC75S67TU*	---	---
	SOT-25(SMV) (2.9*2.8mm)	TA75S01F	TA75S558F	TC75S51F	TC75S54F	TC75S55F	---	---	TC75S102F	TC75S103F
Dual	US8 (2.0*3.1mm)	---	---	TC75W51FK	TC75W54FK	TC75W55FK	---	---	---	---
	SOT-505 (2.9*4.0mm)	TA75W01FU	TA75W558FU	TC75W51FU	TC75W54FU	TC75W55FU	---	---	---	---

* : UFV package

6 Small surface mount LDO regulator

TCR1HF Series

Low loss,
Low heat,
and power
efficiency

Small size
packages

[Back to Microcontroller peripheral](#)

Value Provided

To meet high performance demands with optimum products, we offer from general purpose to small package devices.

1 Low dropout voltage*

Dropout characteristics have been greatly improved by the newly developed process. (50 % improvement : Toshiba comparison)

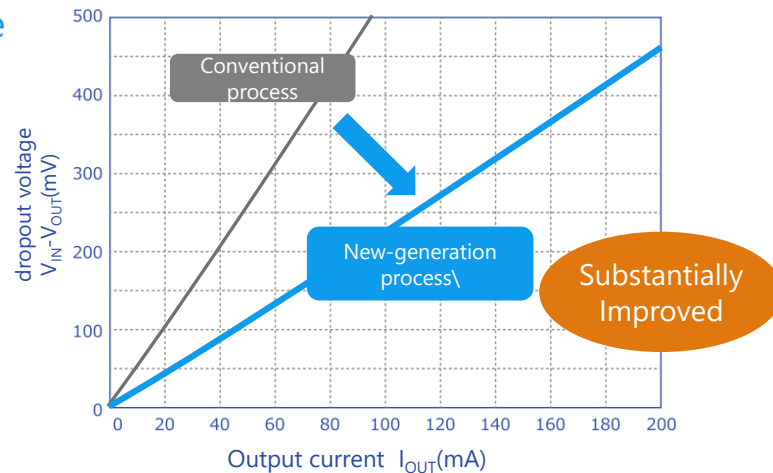
2 Low output noise voltage V_{NO}

The low output noise voltage V_{NO} is also reduced to 100 μ Vrms, making it even more applicable for analog circuits.

3 Can be used with ceramic capacitors

With improved dropout characteristics, it is now possible to use ceramic capacitors for external capacitor functions.


Low dropout voltage



Note: Toshiba internal comparison

* For post DC-DC switching regulation

Line Up

Part number	TCR1HF Series
Package	SOT25 
V_{IN} (Max) [V]	40
I_{OUT} (Max) [mA]	150
$V_{IN} - V_{OUT}$ (Max) [V]	0.66
Output range [V]	1.8 to 5.0

For more information see: [TCR1HF Series](#)

6 What is Toshiba LDO ?

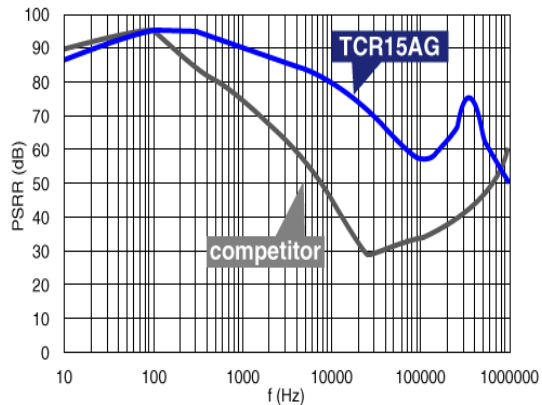
Low loss,
Low heat,
and power
efficiency

Small size
packages

[Back to Microcontroller peripheral](#)

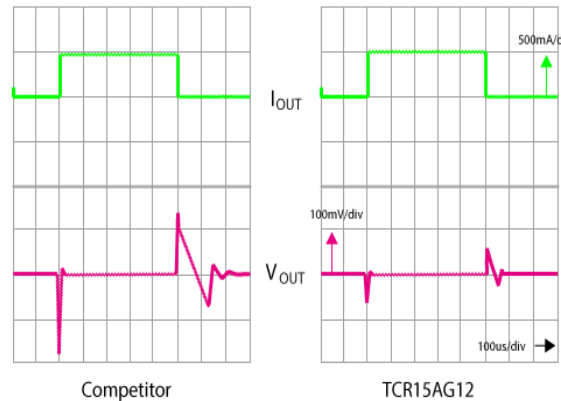
Toshiba LDOs offer Stable and Long operation

Remove Noise from Power line



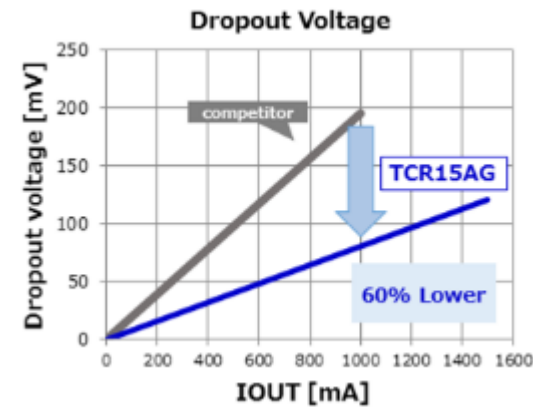
Remove Input noise by High PSRR advanced technology.

Keep Stable Output When Load changes



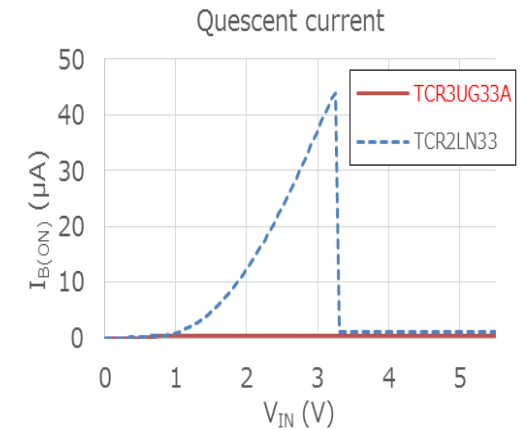
Keep V_{OUT} Stable when load current suddenly changes.

Low power Loss by Low Dropout*



Low input voltage is acceptable by low dropout

Long life operation By Low Quiescent



Keep Low I_Q with Bypass mode when V_{IN} is lower than V_{OUT} .

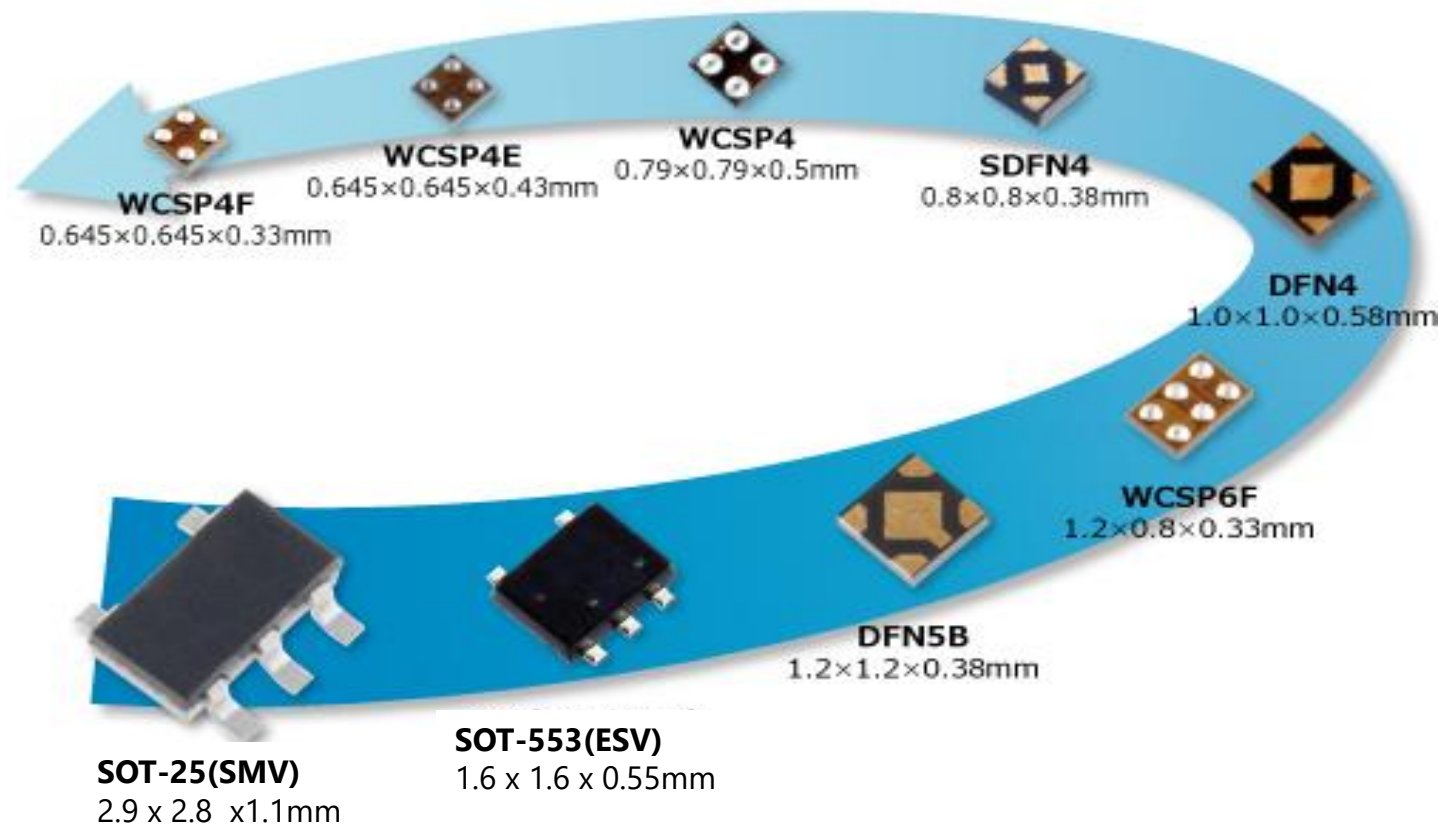
<https://toshiba.semicon-storage.com/ad/product/linear/lDO-regulator.html>

* For post DC-DC switching regulation

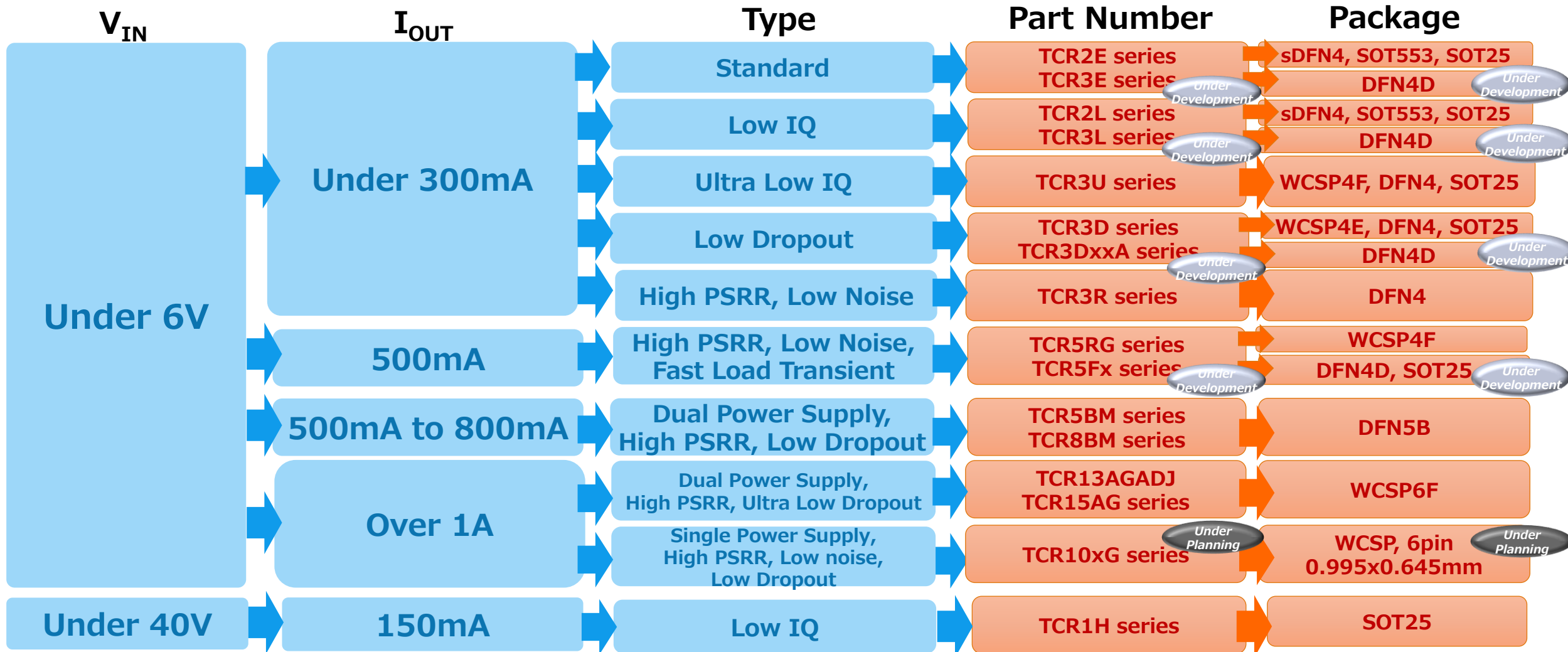
6 What is Toshiba LDO ?

Wide packaging options

Toshiba's LDO regulators are available in a wide range of packages from general-purpose packages to ultra-small WCSP and SDFN4 packages.



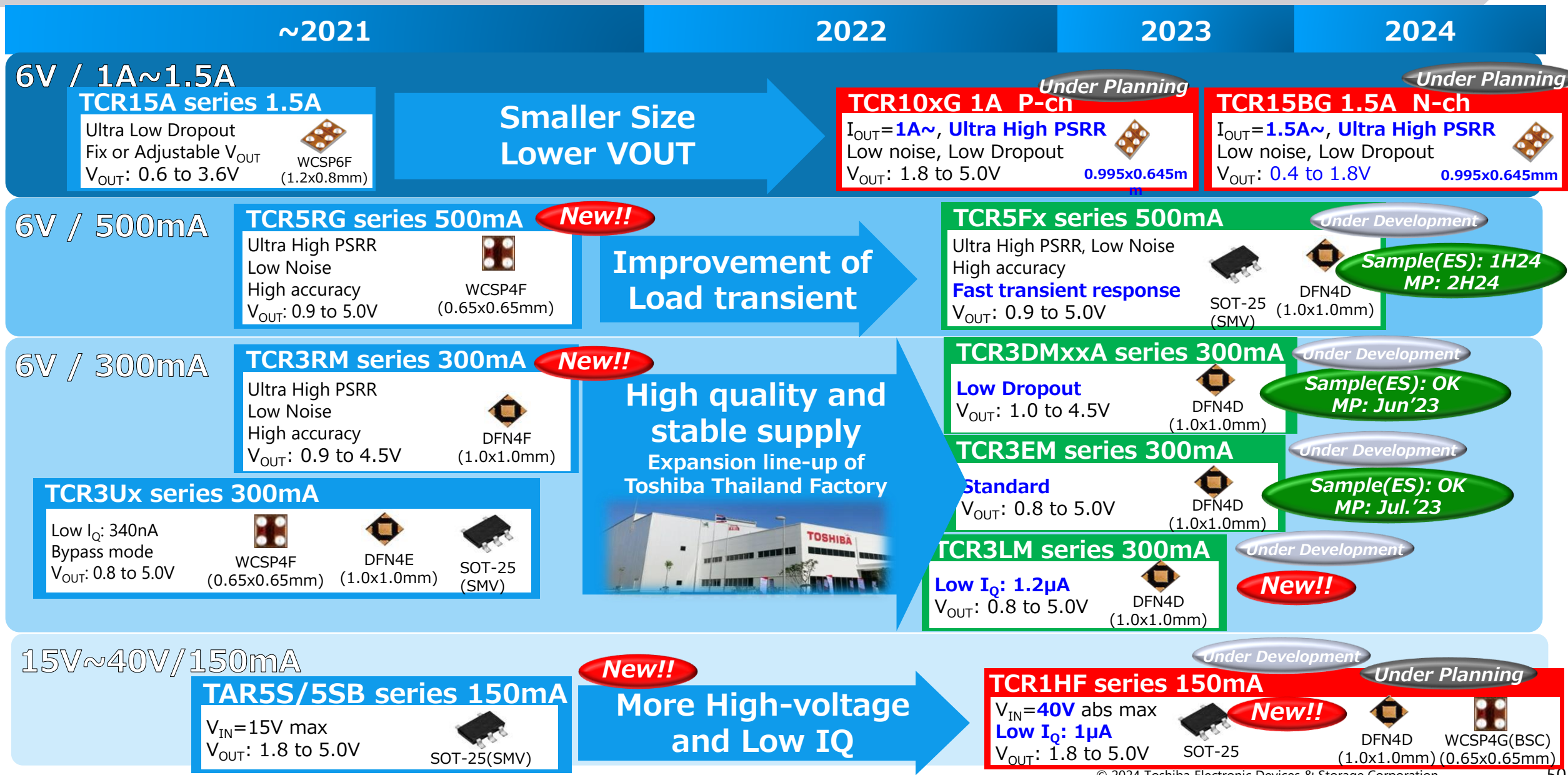
You can choose according to your desired characteristics.



6 LDO Roadmap

[Back to Microcontroller peripheral](#)

*The below target characteristics and schedule are subject to change without notice.



6 40V 150mA , Low IQ 1.0μA LDO

New

[Back to Microcontroller peripheral](#)

TCR1HF series

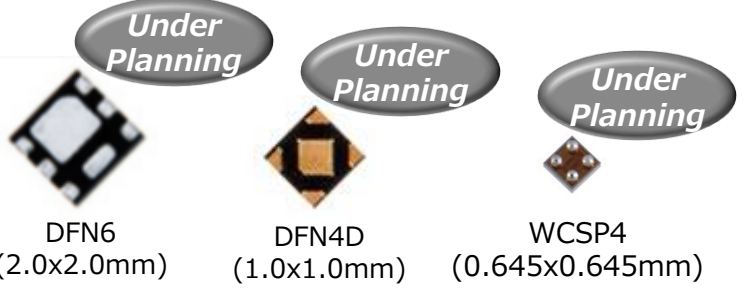
- **Maximum Input Voltage** 40V
- **Input Voltage** 4V to 36V(*1)
- **Output Current** 150mA (*2)
- **Output Voltage** 1.8V to 5.0V
- **Quiescent Current** 1μA (typ.)
- **Operation Temperature Range** -40°C to 125°C
- **Overcurrent Protection**
- **Thermal Shutdown**

(*1) V_{OUT}=5V: 6V to 36V
 (*2) I_{OUT} is Depended on Input voltage (Heat issue)

Package Lineup



SOT-25(SMV)
(2.9x2.8mm)



DFN6 (2.0x2.0mm) DFN4D (1.0x1.0mm) WCSP4 (0.645x0.645mm)

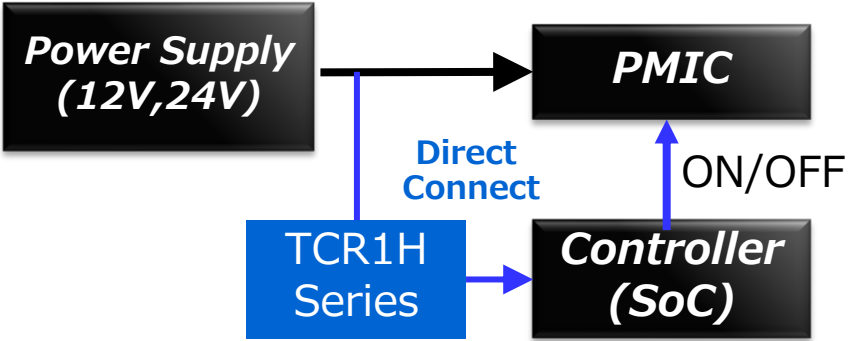
An industry-leading smallest class package (as high voltage type LDO)

In MP

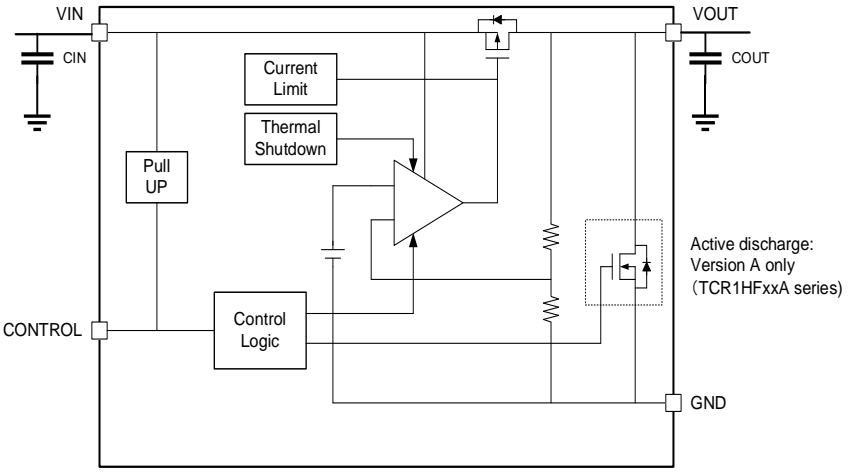
Application example

Ultra Low Power Consumption at Standby mode

Possible to supply high voltage to controller with low current consumption



Block Diagram



6 Toshiba LDO Line-up

[Back to Microcontroller peripheral](#)

Toshiba Thailand Factory PKG

	0.645x0.645mm WCSP4F, 4E t: 0.33mm(max) or 0.43mm(max)	0.8x0.8mm SDFN4E t: 0.38mm (typ.)	1.0x1.0mm DFN4E, 4F t: 0.58mm(typ.) or 0.38mm(max)	1.2x0.8mm WCSP6F t: 0.33mm (max)	1.2x1.2mm DFN5B t: 0.38mm (typ.)	1.0x1.0mm DFN4D t: 0.37mm (typ.)	1.6x1.6mm SOT553(ESV) t: 0.55mm (typ.)	2.9x2.8mm SOT-25(SMV) t: 1.1mm (typ.)
NMOS LDO				1.5A, Low Dropout TCR15AG series	800mA, Low Dropout TCR8BM series			
For Low Voltage				1.3A, Low Dropout TCR13AGADJ	500mA, Low Dropout TCR5BM series	MP: CY2Q.'24		MP: CY1Q.'24
PMOS LDO				500mA Improvement of Load transient LDO Development		500mA, Low noise, Improvement of Load transient TCR5FM series		500mA, Low noise, Improvement of Load transient TCR5FF series
For Analog Voltage	500mA, Low noise TCR5RG series	NEW	300mA, Low noise TCR3RM series	NEW				NEW
	300mA, Ultra Low IQ TCR3UG series	NEW	300mA, Ultra Low IQ TCR3UM series	NEW				300mA, Ultra Low IQ TCR3UF series
	300mA/420mA TCR3DG/4DG series		300mA TCR3DM series		MP: Jun.'23	300mA TCR3DMxxA series		300mA TCR3DF series
		200mA, Low IQ TCR2LN series			MP: OK	300mA, Low IQ TCR3LM series	200mA, Low IQ TCR2LE series	200mA, Low IQ TCR2LF series
		200mA TCR2EN series			MP: Jul'23	300mA TCR3EM series	200mA TCR2EE series	200mA TCR2EF series
High Voltage						MP: OK	40V LDO Development	150mA, High Voltage TCR1HF series

Toshiba Thailand Factory New Line-up

Value Provided

TVS diode absorbs static electricity (ESD) from external terminals, prevents circuit malfunction and protects devices.

1 Improve ESD absorbability

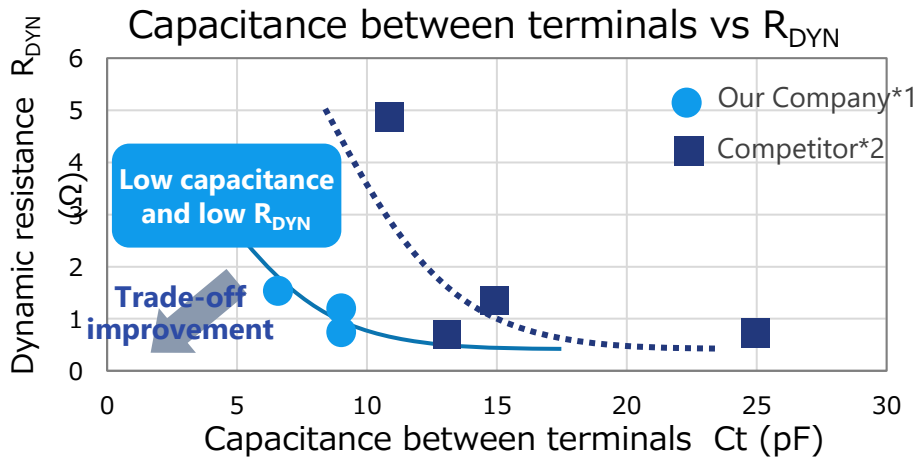
Improved absorption of ESD through our proprietary Zener process.
(Both low operating resistance R_{DYN} and low capacitance C_t)

2 High ESD immunity



Compliant products with
ISO10605 Standard > ±20 kV
IEC61000-4-2 Standard > ±20 kV (L4)

3 Suitable for high density mounting

A variety of small packages are available.



Line Up

Part number	DF3D18FU	DF3D29FU	DF3D36FU	CUHZ36V
Package	USM (SOT-323)			US2H (SOD-323HE) 
V_{ESD} [kV] @ISO10605	±30	±30	±20	±20
V_{RWM} (Max) [V]	12	24	28	32.5
C_t (Typ./Max) [pF]	9 / 10		6.5 / 8	105
R_{DYN} (Typ.) [Ω]	0.8	1.1	1.5	0.39

(NOTE) : This product is an ESD protection diode and cannot be used for purposes other than ESD protection (including but not limited to constant voltage diode applications).

*1:TOSHIBA Electronic Device & Storage Corporation

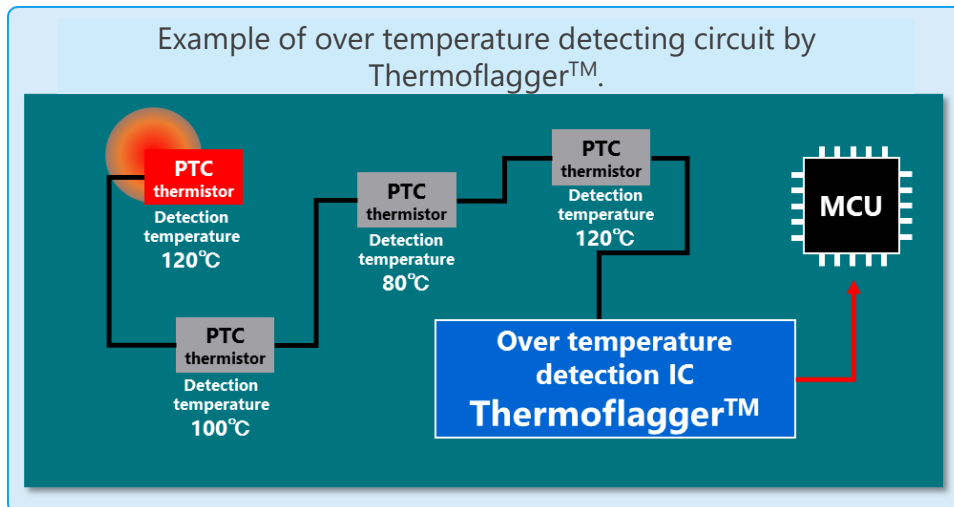
*2:Measurements of the commercial product

Value Provided

TCTH series can detect temperature rise at multiple points on the circuit board.

1 Temperature rise can be detected at multiple points


TCTH series detect an increase in resistance during over temperature by supplying a constant current (1 μA or 10 μA) to PTC (Positive Temperature Coefficient) thermistors. Multiple PTC thermistors connected in series enable to detect over temperature at multiple points on the circuit board.



2 Low current consumption and small package

TCTH01 series has $I_{DD} = 1.8 \mu\text{A}$ (Typ.) and TCTH02 series has $I_{DD} = 11.3 \mu\text{A}$ (Typ.). These packages are small size ESV type.

Line Up

Part number	TCTH011AE TCTH011BE	TCTH012AE TCTH012BE	TCTH021AE TCTH021BE	TCTH022AE TCTH022BE
Package	ESV 1.6 x 1.6 x 0.55 mm 			
V_{DD} [V]	1.7 to 5.5			
I_{DD} (Typ.) [μA]	1.8		11.3	
PTCO Output current (Typ.) [μA]	1	1	10	10
Abnormal latch function	-	Yes	-	Yes
Output circuit type	AE: push pull, BE: open drain			

[Back to Microcontroller peripheral](#)

Product Name	PTCO output current (I_{PTCO})	<Reference> PTC thermistor resistance at 25 °C	Latching function for abnormal detection	PTCGOOD (FLAG) Output type	Status
TCTH011AE	1 μ A	4.7 k Ω to 10 k Ω		Push-pull	In MP
TCTH012AE	1 μ A	4.7 k Ω to 10 k Ω	✓	Push-pull	
TCTH021AE	10 μ A	470 Ω to 1 k Ω		Push-pull	
TCTH022AE	10 μ A	470 Ω to 1 k Ω	✓	Push-pull	
TCTH011BE	1 μ A	4.7 k Ω to 10 k Ω		Open-drain	
TCTH012BE	1 μ A	4.7 k Ω to 10 k Ω	✓	Open-drain	
TCTH021BE	10 μ A	470 Ω to 1 k Ω		Open-drain	
TCTH022BE	10 μ A	470 Ω to 1 k Ω	✓	Open-drain	

Value Provided

[Back to Sensor / Display Block Diagram Detail](#)

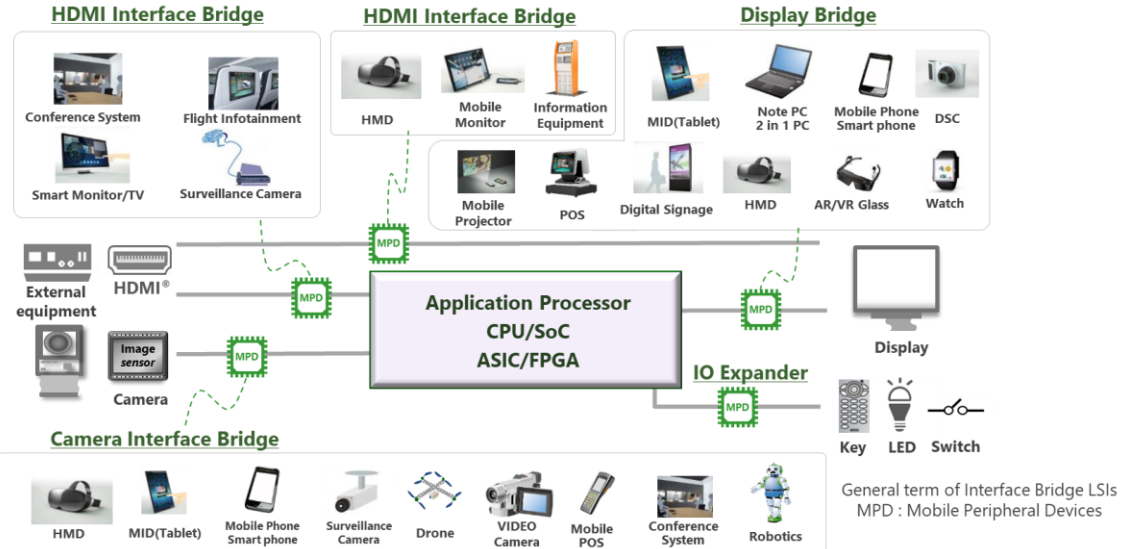
A simple way to bridge one camera interface / display interface to another.

1 Flexible Features and Operations

- Display bridge chips can support many display sizes, clock speeds, interface speeds, etc.
- Camera bridge chips can support various RAW or processed video streams as well as interface speeds, etc.

2 Support tools to help design and initialize the bridge chip into your system

- Reference circuit schematics,
- Programming spreadsheet is available to take your system parameters and create initialization routines that can be sent via I2C or SPI into the MPD bridge device.



Product	Input	Output	Package
Camera / HDMI Interface LSI			
TC358743XBG	HDMI (D2,1,0,CK)/ EDID,DDC,CEC	MIPI CSI2 4Lanes / SPD IF (I2S)	P-TFBGA64-0606-0.65
TC358746AXBG	MIPI CSI2 (4Lanes)/ Parallel (24bit)	Parallel (24bit) / MIPI CSI2 (4Lanes)	P-VFBGA72-0404-04
TC358748XBG	MIPI CSI2 (4Lanes)/ Parallel (24bit)	Parallel (24bit) / MIPI CSI2 (4Lanes)	P-VFBGA80-0707-0.65
TC358841XBG	HDMI (D2,1,0,CK)/ DDC,CEC	MIPI CSI2 4Lanes x2Link / SPD IF (I2S), Slimbus	P-VFBGA80-0707-0.65
TC358871XBG	HDMI (D2,1,0,CK)/ DDC,CEC	MIPI DSI 4Lanes x2Link / SPD IF (I2S), Slimbus	P-VFBGA80-0707-0.65
Display Interface LSI			
TC358767AXBG	MIPI DSI (4Lanes), I2C	Display Port 2 x Single Port, AUX x2chMIPI DPI 24bit	P-VFBGA81-0505-0.50
TC358867XBG	MIPI DSI (4Lanes), I2C	Display Port 2 x Single Port, AUX x2chMIPI DPI 24bit	P-VFBGA80-0707-0.65
TC358768AXBG	MIPI DP(24bit)	MIPI DSI (4Lanes)	P-VFBGA72-0404-0.4
TC358778XBG	MIPI DP(24bit)	MIPI DSI (4Lanes)	P-VFBGA80-0707-0.65
TC358777XBG	MIPI DSI (4Lanes) X2, I2S	Display Port 4 x Single Port (QuadLink), DPAUX	P-VFBGA80-0707-0.65
TC358774XBG	MIPI DSI (4Lanes)	LVDS (5Lanes(CLK,TX x4))	P-TBGA49-0505-0.65
TC358775XBG	MIPI DSI (4Lanes)	LVDS (5Lanes(CLK,TX x4)) x2ch	P-TBGA64-0606-0.65
TC358860XBG	eDP v1.4 (4 lanes)	MIPI DSI (8 lanes with 1Gbps/lane)	P-TFBGA65-0505-0.5

For more information, see: [Display Interface Bridges](#) [Camera Interface Bridges](#)

MPD Bridge Line up

Bridge IC		OUT					
		DPI SM (RGB I/F)	Parallel 8bit/16bit	LVDS	MIPI [®] DSI SM	MIPI [®] CSI-2 SM	DisplayPort™ /eDP
IN	DPI SM (RGB)			DSI SM 4lane, WUXGA → TC358768A DSI SM 4lane, WUXGA, Wider ball pitch → TC358778 DSI SM 4lane, WUXGA, Wider ball pitch, Wider temp range → TC9594		DP 2lane, WUXGA → TC358767A DP 2lane, WUXGA, Wider ball pitch → TC358867 DP 2lane, WUXGA, Wider ball pitch, Wider temp range → TC9595	
	Parallel 8bit/16bit				Parallel 24bit, CSI-2 SM 4lane → TC358746A Parallel 24bit, CSI-2 SM 4lane, Wider ball pitch → TC358748 Parallel 24bit, CSI-2 SM 4lane, Wider ball pitch, Wider temp range → TC9591		
	MIPI [®] DSI SM	TC358767A ← DSI SM 4lane, PCLK 100MHz TC358867 ← DSI SM 4lane, PCLK 100MHz, Wider ball pitch TC9595 ← DP 2lane, WUXGA, Wider ball pitch, Wider temp range	DSI SM 4lane, LVDS 1link, UXGA → TC358774 DSI SM 4lane, LVDS 1link, UXGA, Wider temp range → TC9592 DSI SM 4lane, LVDS 2link, WUXGA → TC358775 DSI SM 4lane, LVDS 2link, WUXGA, Wider temp range → TC9593		DSI SM 4lane, DP 2lane, WUXGA → TC358767A DSI SM 4lane, DP 2lane, WUXGA, Wider ball pitch → TC358867 DP 2lane, WUXGA, Wider ball pitch, Wider temp range → TC9595 DSI SM 8lane, DP 4lane, WQXGA, Wider ball pitch → TC358777		
	MIPI [®] CSI-2 SM		TC358746A ← CSI-2 SM 4lane, Parallel 24bit TC358748 ← CSI-2 SM 4lane, Parallel 24bit, Wider ball pitch TC9591 ← CSI-2 SM 4lane, Parallel 24bit, Wider ball pitch, Wider temp range				
	HDMI [®]				WUXGA60fps CSI-2 SM 4lane → TC358743 WUXGA60fps CSI-2 SM 4lane, Wider ball pitch → TC358743A WUXGA60fps CSI-2 SM 4lane, Wider ball pitch, Wider temp range → TC9590 4k2k30fps/WQXGA60fps, DSI SM 8lane → TC358871	TC358841 ← 4k2k30fps CSI-2 SM 8lane	


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 Wide Ball Pitch Product

 Wide Temp. Range Product

Value provided

High performing rail-to-rail output gate drivers for MOSFETs, IGBTs and SiC MOSFETs

1 High Performance

High Speed: 150ns (max)

Low Skew : +/- 80ns

Wide operating temperatures available

2 Low Power

Supply current: 3mA (max)

Threshold current: 4mA (max)

Several output current versions available

3 Popular configurations

Direct replacements for

- ACPL-P340/W340 (TLP575x, TLP575x(LF4))
- ACPL-P345/P346 (TLP577x)

SO6L

Creepage : 8mm

DTI : 0.4mm

Bvs : 5000Vrms

Standard



LF4 forming



Line up				
lop 1.0A	TLP5751	TLP5751H	TLP5771	TLP5771H
lop 2.5A	TLP5752	TLP5752H	TLP5772	TLP5772H
lop 4.0A	TLP5754	TLP575H	TLP5774	TLP5774H
Package	SO6L, SO6L(LF4)			
Operating Temp	-40°C ~110°C	-40°C ~125°C	-40°C ~110°C	-40°C ~125°C
Supply Voltage	15V ~ 30V		10V ~ 30V	
UVLO	13.5V (max)		9.5V (max)	
Supply Current	3mA (max)			
Propagation Delay	150ns (max)			
Propagation Skew	+/- 80ns			
Threshold Input Current	4mA (max)		2mA (max)	

For more information see: [TLP75xx Series](#)


10a Toshiba Gate Driver Couplers

[Back to AC-DC / PFC](#)

For more efficient and reliable Systems


Target Devices	
Parameter	Application
	Temp.
	Frequency
	CMTI
	Gate Current
	Gate Voltage

MOSFET / IGBT



$T_a : 110 \Rightarrow 125 \text{ }^\circ\text{C}$
 Under 100 kHz
 Under 50 kV/ μs
 Around $\pm 5 \text{ A}$
 $\pm 10 \text{ V} / +18 \text{ V}, -10 \text{ V}$

SiC MOSFET



$T_a : 125 \text{ }^\circ\text{C}$
 50 to **400 kHz**
 50 to **100 kV/ μs**
 Around $\pm 5 \text{ A}$
 $+18 \text{ V}, -5 \text{ V}$

Existing Product Examples

TLP152 / TLP151A 


Gate Driver for power **MOSFETs** and **IGBTs**

Main Specification:
 100/110°C, 20kV/ μs , $\pm 2.5 \text{ A}$, 10 to 30V

TLP5705H 


Gate Driver for **IGBTs** and **SiC MOSFETs**

Main Specification:
 125°C, 50kV/ μs , $\pm 5 \text{ A}$, 15 to 30V

TLP5774H / TLP5754H 

Gate Driver for power **MOSFETs, IGBTs** and **SiC MOSFETs**

Main Specification:
 125°C, 35kV/ μs , $\pm 4 \text{ A}$, 15/10 to 30V

TLP5212 / TLP5222 

Smart Gate Driver for **IGBTs** and **SiC MOSFETs**

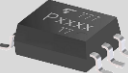
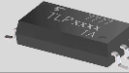
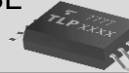
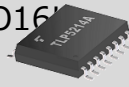
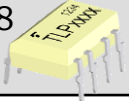
Main Specification:
 110°C, 25kV/ μs , $\pm 2.5 \text{ A}$, 15 to 30V

For more information see: [Gate Drive Photo Coupler](#)

Recommended package

New Products

Under development

Creepage & clearance		5mm	8mm	8mm	8mm	7 or 8mm
Isolation voltage		3750 Vrms	5000 Vrms	5000 Vrms	5000 Vrms	3750 Vrms
Peak output current	Propagation Delay time(max)	SO6 	SO6L 	SO8L 	SO16 	DIP8 
6.0 A	500 ns					TLP358H*H
5.0 A	500 ns		TLP5705H*H			
4.0 A	150 ns		TLP5754*R TLP5754H*R*H TLP5774*R*10 TLP5774H*R*H*10	TLP5814H*AMC	TLP5214*R*OC*AMC TLP5214A*R*OC*AMC	
2.5 A	150 ns		TLP5752*R TLP5752H*R*H TLP5772*R*10 TLP5772H*R*H*10			
	200 ns	TLP152*10	TLP5702 TLP5702H*H	TLP5832	TLP5212*OC*AMC TLP5222*OC*AMC *AR	TLP352*H
	300 ns				TLP5231*R*OC	
	500 ns					TLP250H*H TLP350H*H
1.0 A	150 ns		TLP5751*R TLP5751H*R*H TLP5771*R*10 TLP5771H*R*H*10			
0.6 A	200 ns	TLP155E*10				
	500 ns	TLP151A*10	TLP5701*10			TLP351A*10
	700 ns	TLP151*10				TLP351H*10*H

*R = Full swing output (rail to rail)
 H = Operation temperature: 125 °C (max)
 10 = Operation voltage: 10 V (min)

*L = Low LED current: IFLH=2mA (max)
 *AR = Auto Reset from the protection mode
 *OC = AMC Over voltage detection (VDESAT)

*AMC = Active Miller clamp

Value provided

Highly integrated 4.0A output current SiC gate drive photocoupler in an efficient SO16L package

1 Protects against overcurrent of power devices

Gate driving with isolation with two LEDs

Feedback function for fault mode

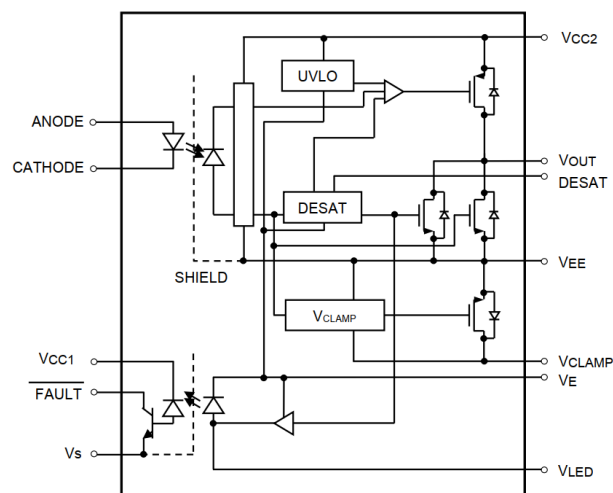
Built-in protective functions

2 Protective Functions

- Soft shut down in time to prevent the driven transistor from any short circuit breakdown
- Active mirror clamp to help flow damaging current away from driven device

3 Small Size Package

In an efficient SO16L package



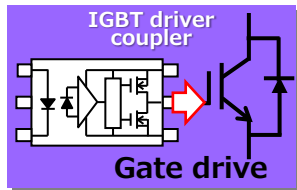
Line up

Part number	TLP5214	TLP5214A	TLP5814H*
Total Supply Voltage (V)	15 to 30		15 to 23
Peak Output Current (A)	-4.0 / +4.0		-4.8 / +6.8
Supply Current (mA)	3.8		6.8
Threshold input current MAX (mA)	6		3.0
Switching time tpLH/tpHL MAX (ns)	150		150
DESAT leading edge blanking time Typ. (μs)	0.2	1.1	NONE
VtClamp threshold voltage for Mirror Clamp (V)	3.0	2.5	2.5
Package	SO 16L		SO 8L

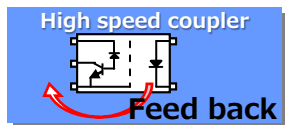
* Under Development

SGD protects against overcurrent of power devices.

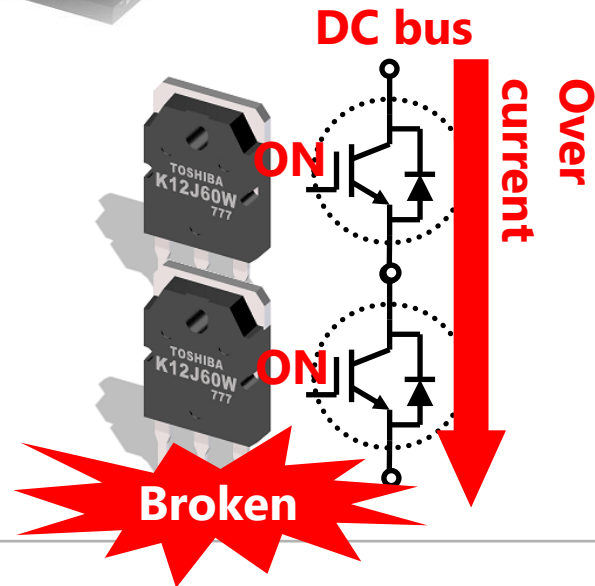
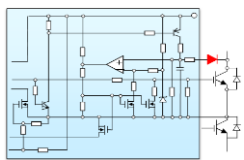
① gate driving with isolation



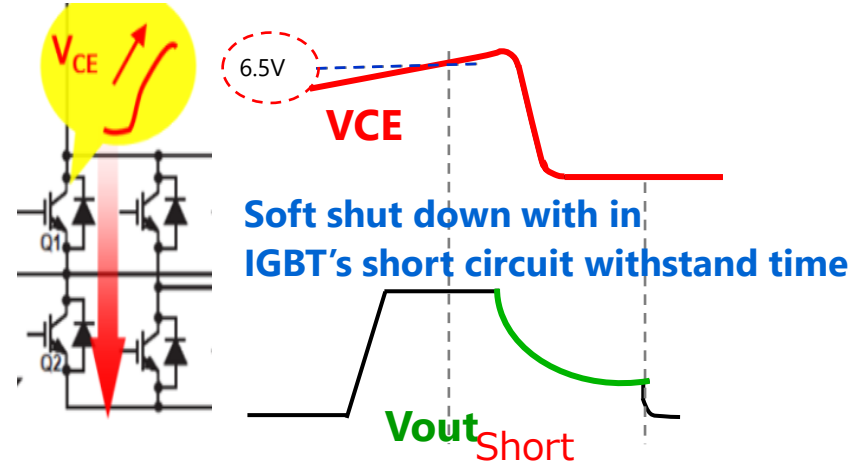
② Feed back function for fault mode



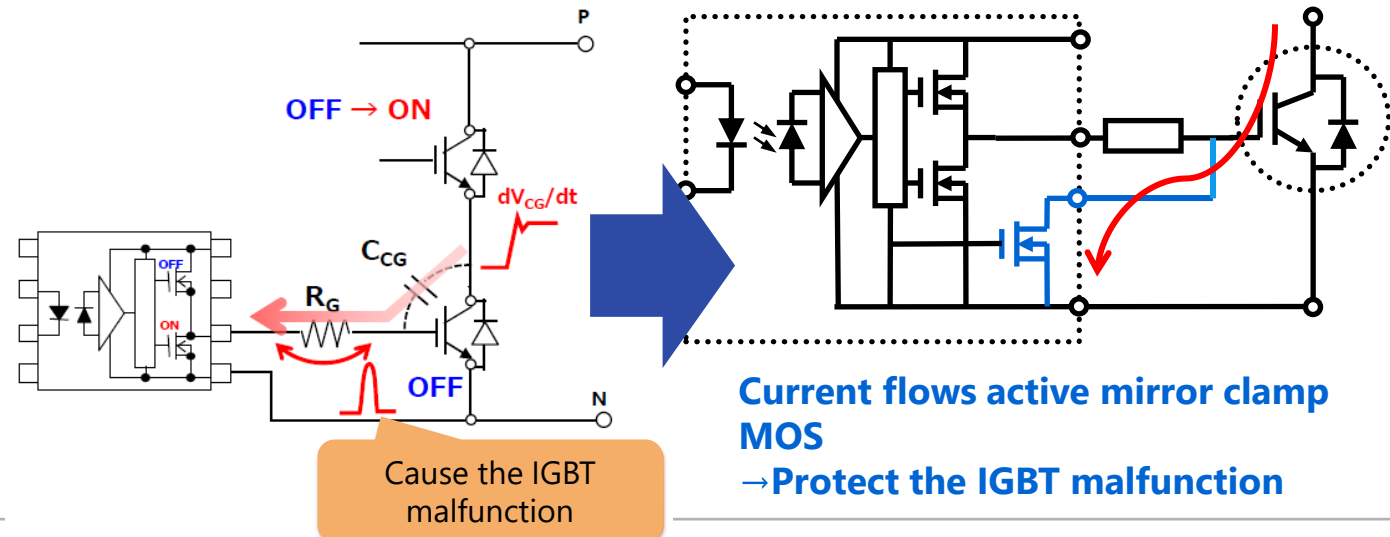
② Protective functions



Protective function-1 Soft shut down



Protective function-2 Active mirror clamp



■ Differences between TLP5214A and TLP5214

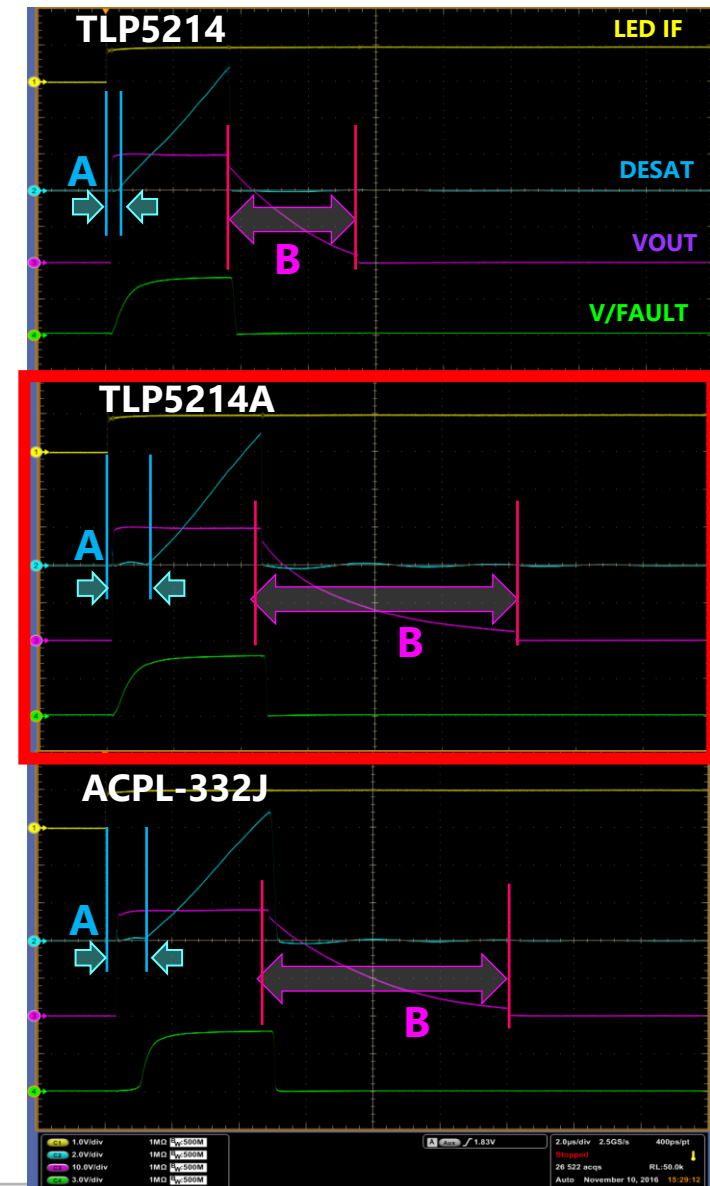
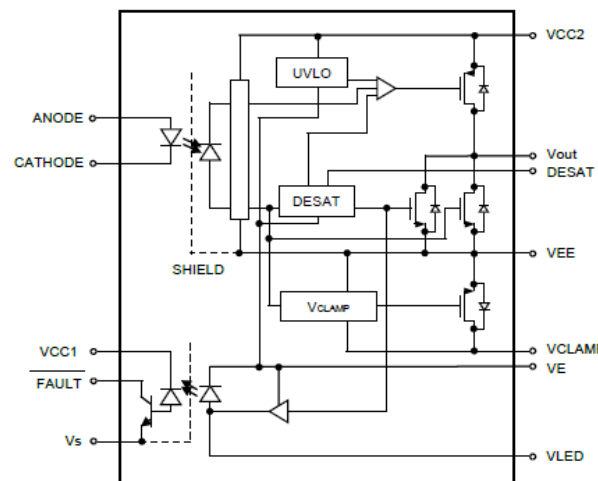
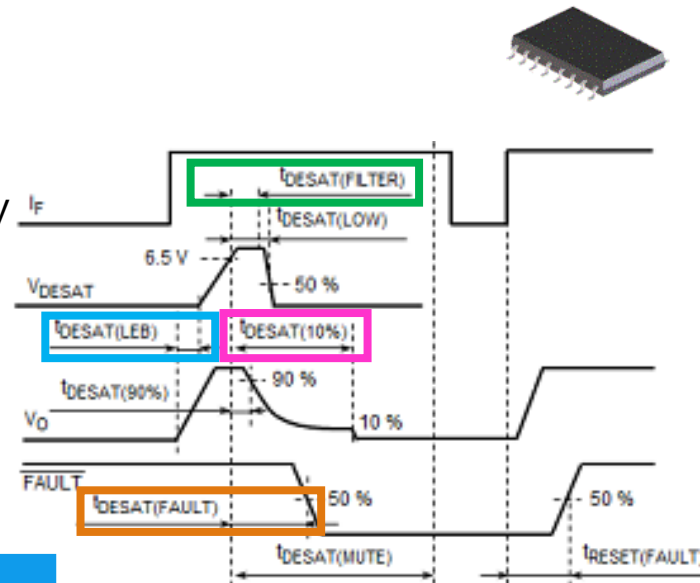
Longer **DESAT mask** $t_{DESAT(LEB)}$ (~0.2 → ~1.1 μs typ.)

Longer **soft shutdown time** (~3.5 → ~7 μs typ.)

Added “**DESAT filter time**” for high noise immunity
(90 ns typ.)

$t_{DESAT(FAULT)}$ (0.5 μs → 0.55 μs typ.)

	TLP5214	TLP5214A
$t_{DESAT(LEB)}$	0.2 μs (typ.)	1.1 μs (typ.)
$t_{DESAT(10\%)}$	3.5 μs (typ.)	7.0 μs (typ.)
DESAT filter time	-	90 ns (typ.)
$t_{DESAT(FAULT)}$	0.5 μs (max.)	0.55 μ (max.)



Operation behavior
[during protection mode]

Built in MOSFET for miller clamp prevent self turn on of power devices easily.

[Back to AC-DC / PFC](#)

Feature

- Active Miller Clamp function
- High peak output current
- High CMTI

Part name	TLP5814H
Peak output current (Max.)	+6.8 / -4.8 A
Peak clamp sink current (Max.)	6.8 A
CMTI (Min.)	±70 kV/μs
Operating temp. (max)	125 °C
Supply voltage	15 to 23 V

SO8L

Creepage : 8 mm

DTI : 0.4 mm

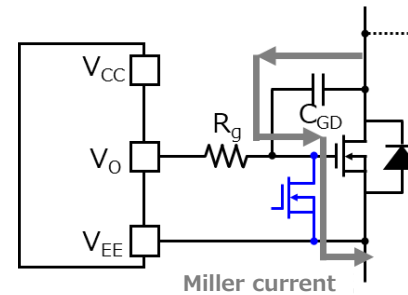
Bvs : 5000 Vrms



To avoid self turn-on.

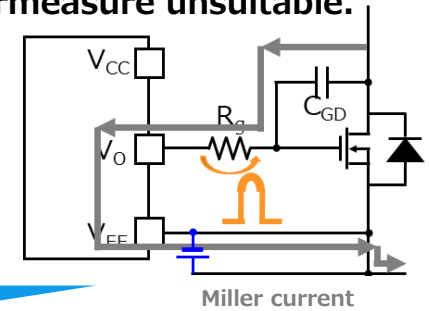
Adding a shunt circuit

It is necessary to add a bypass element and its control circuit.

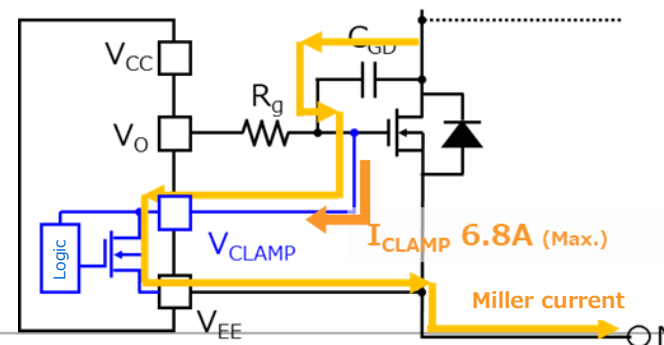


Using a negative gate power supply

SiC-MOSFETs have a low gate negative bias tolerance, making this countermeasure unsuitable.



Self turn on prevention with TLP5814H



SiC MOSFET

Provides robust self-turn-on prevention for SiC-MOSFETs

IGBT

Reduced negative bias power supply circuit

Smart Gate Driver Comparison Table

* Under development

[Back to AC-DC / PFC](#)

Item	Symbol	TLP5214	TLP5214A	TLP5814H*
Total supply voltage	VCC2-VEE	15 ~ 30 V		13 ~ 23 V
Negative supply voltage	VE-VEE	-15 V ~ 0 V		-
Positive supply voltage	VCC2-VE	15V ~ 30 - (VE - VEE)		-
Positive supply voltage	VCC1	3.3 ~ 5.5 V		13 ~ 23 V
Threshold input current	IFHL	6 mA		3 mA
Peak output current	IOPH/IOPL	-4.0 / +4.0 A (max.)		-4.8 / +6.8 A
Propagation delay time	tpLH, tpHL	50 ~ 150 ns		150 ns max
Propagation delay skew	tpsk	-80 ~ +80 ns (max.)		-75 ~ +75 ns
DESAT threshold voltage	VDESAT	6 ~ 7.5 V	5.9 ~ 7.5 V	None
Blanking cap. charge current (typ.)	ICHG	-0.24 mA		
DESAT leading edge blanking time (typ.)	tDESAT(LEB)	0.2 μs	1.1 μs	
Soft shut down time (typ.)	tDESAT(10%)	3.5 μs	7.0 μs	
Clamp pin threshold voltage (typ.)	VtClamp	3.0 V	2.5 V	2.5 V
Clamp low level sinking current (typ.)	ICL	1.8 A	1.8 A	3.8 A
UVLO P threshold (typ.)	UVLO+	11.6 V	11.6 V	12.5 V
UVLO N threshold (typ.)	UVLO-	10.3 V	10.3 V	11.0 V
Common Mode Transient Immunity (min.)	CMTI	± 35 kV/μs	± 35 kV/μs	± 70 kV/μs
Fault reset method		LED trigger		LED trigger

<https://toshiba.semicon-storage.com/us/semiconductor/product/isolators-solid-state-relays/detail.TLP5214.html>

Value Provided

Reduction in required board area and improving reliability enabling maintenance-free operation.

1 High current transfer ratio

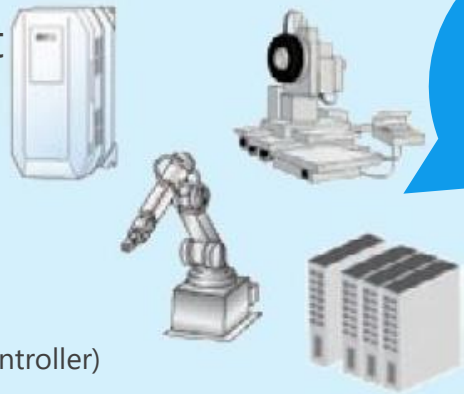
This is a high-isolation photocoupler that optically couples a phototransistor and a GaAs infrared light emitting diode. High current transfer ratio is realized at $I_F = 5 \text{ mA}$.

2 Operating temperature is expanded to 110 °C

It is designed to operate even under severe temperature environments such as those for inverter devices, robots, machine tools, and high output power supplies.


Industrial equipment

- General purpose inverter
- Servo amplifier
- Robot
- Machine Tool
- High output power supply
- Security equipment
- Semiconductor tester
- PLC (Programmable Logic Controller)
- Garage Door/Gate Opener**



High level of
insulation
and noise
blocking

Line Up

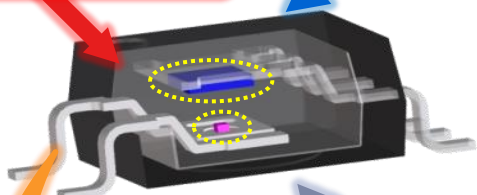
Part number	TLP383
Package	4 pin SO6L 
BV_S (Min) [Vrms]	5000
T_{opr} [deg.C]	-55 to 110

10c Advantage of TOSHIBA Isolation devices

TOSHIBA

1 Long Life Time LED

2 High Performance Detector Chip

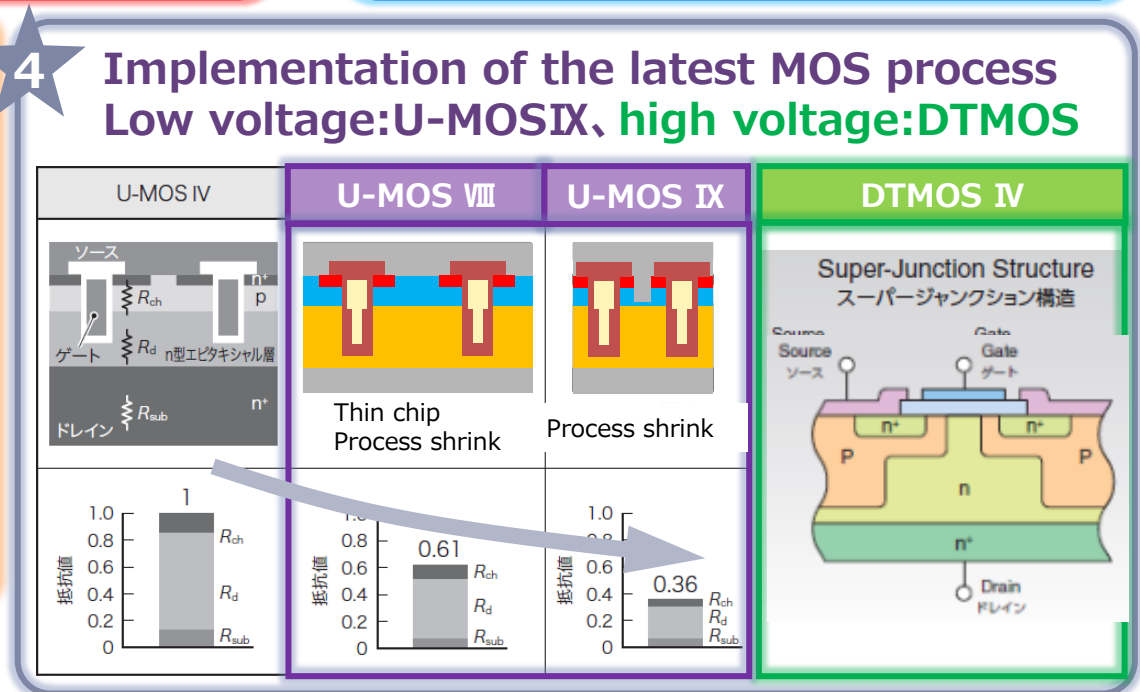
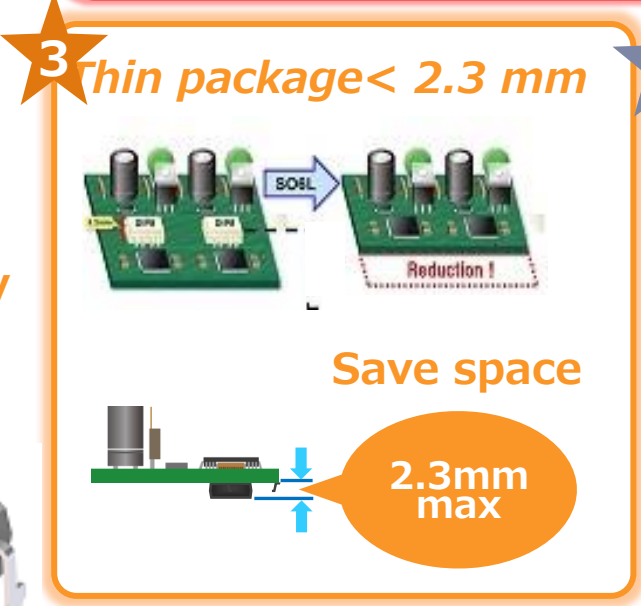
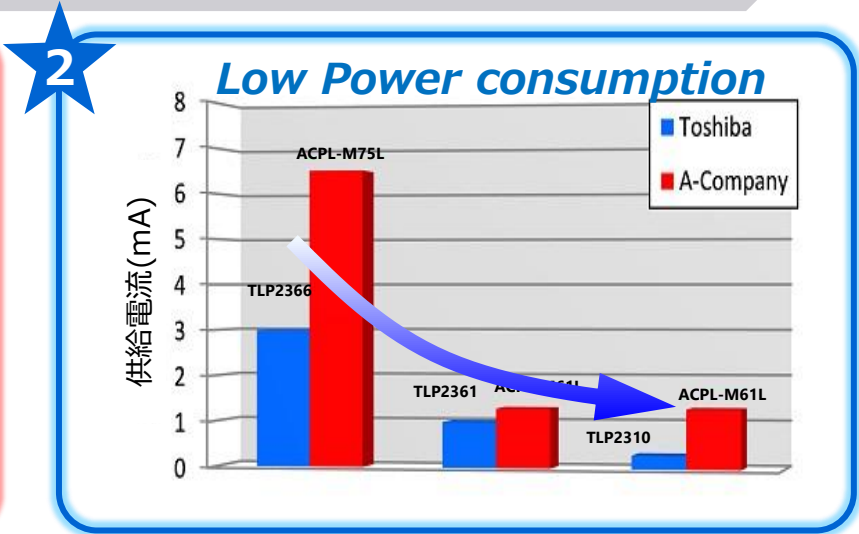
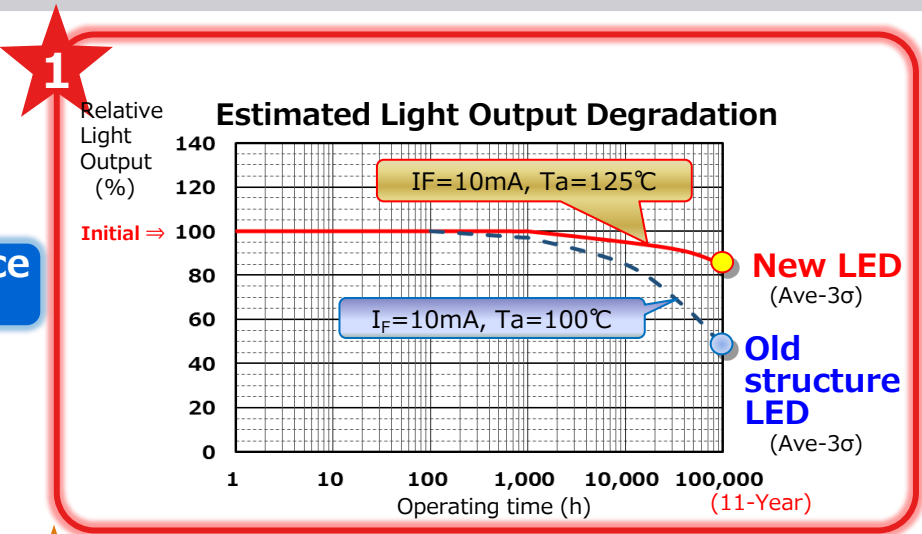
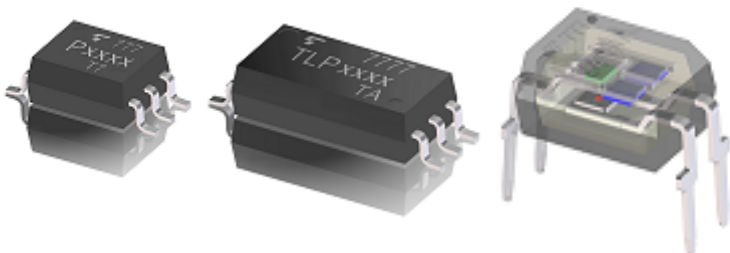



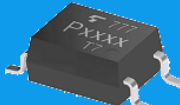
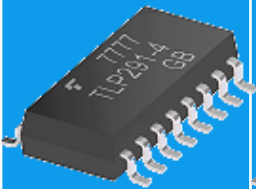
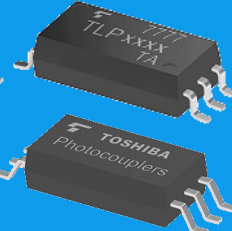
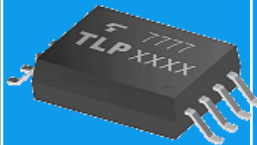
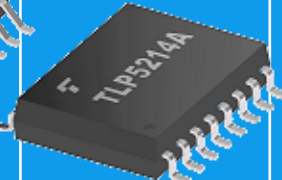


4 Latest Gen. MOSFET

High efficiency line products

3 New Package

S04, S06, S016
S06L, S08L, S016L, DIP4





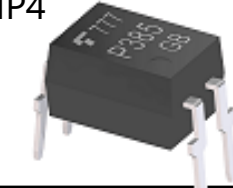
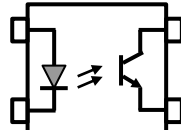
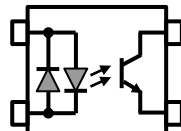
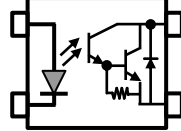
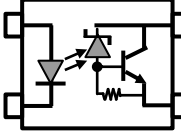


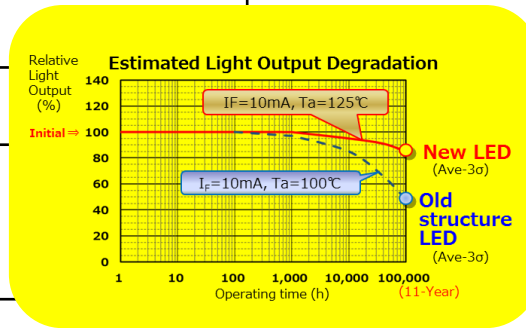
Package	SO4	SO6	SO16	SO6L SO6L(LF4)	SO8L(LF4)	SO16L	DIP4	SOP4
Items								 White mold
Creepage	5mm			8mm				5mm
Internal clearance	0.4mm			0.4mm				TBD
BVs (min.)	3,750Vrms			5,000Vrms				1,500Vrms
Transistor	✓	✓ (4pin)	✓	✓ (4pin)			✓	
Gate driver		✓ (5pin)		✓ (6pin)	✓	✓		
IPM interface		✓ (5pin)		✓ (6pin)				
High speed logic		✓ (5pin)		✓ (6pin)	✓			
MOSFET relay		✓ (4pin)				✓ (U.D.)	✓	✓
Photo voltaic		✓ (4pin)						
TRIAC		✓ (4pin)						
Isolation amp.					✓			

DIP4 : SMD lead forming : LF1, LF4
(LF4) : Wide lead forming

10c Tr. Coupler line-up

[Back to AC-DC / PFC](#)

Creepage / Clearance		5 mm	5 mm	5 mm	8 mm	7 or 8 mm
Isolation Voltage		3750Vrms	2500/3750Vrms	3750Vrms	5000Vrms	5000Vrms
Package		SO4 	SO16 	4pin-SO6 	4pin-SO6L 	DIP4 
Feature						
	DC input	TLP291(SE	TLP291-4	TLP185(SE	TLP385	TLP785
	Low input	TLP293	TLP293-4	TLP183	TLP383	
	350V VCEO			TLP188	TLP388	TLP628M
	AC input	TLP290(SE	TLP290-4	TLP184(SE		
	Low input	TLP292	TLP292-4	TLP182		TLP620M
	Darlington					
	300V VCEO			TLP187	TLP387	TLP627M
	High speed					
	Low input			TLP2301	TLP2701	



Using Long Life Time LED

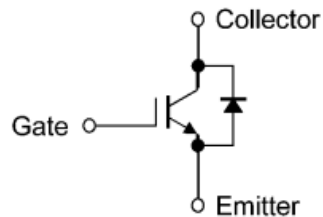
For more information see: [Transistor Output Photocoupler](#)

Value provided

Sixth generation hard switching devices capable for high voltage (600V or more) and high current (15A or more) application. Lineup of low $V_{CE(SAT)}$ products are effective in reducing conduction loss.

1 High speed, low saturation voltage

By adapting a thin wafer punch-through structure, high speed turn-off characteristics and low $V_{CE(SAT)}$ characteristics are realized.



2 High breakdown tolerance

Toshiba has a lineup of products with high breakdown tolerance (short circuit withstand time t_{sc} and reverse bias safe operating area RBSOA).

3 Enhancement type

Since Collector current does not flow when gate voltage is not applied for enhancement devices, handling is easy.

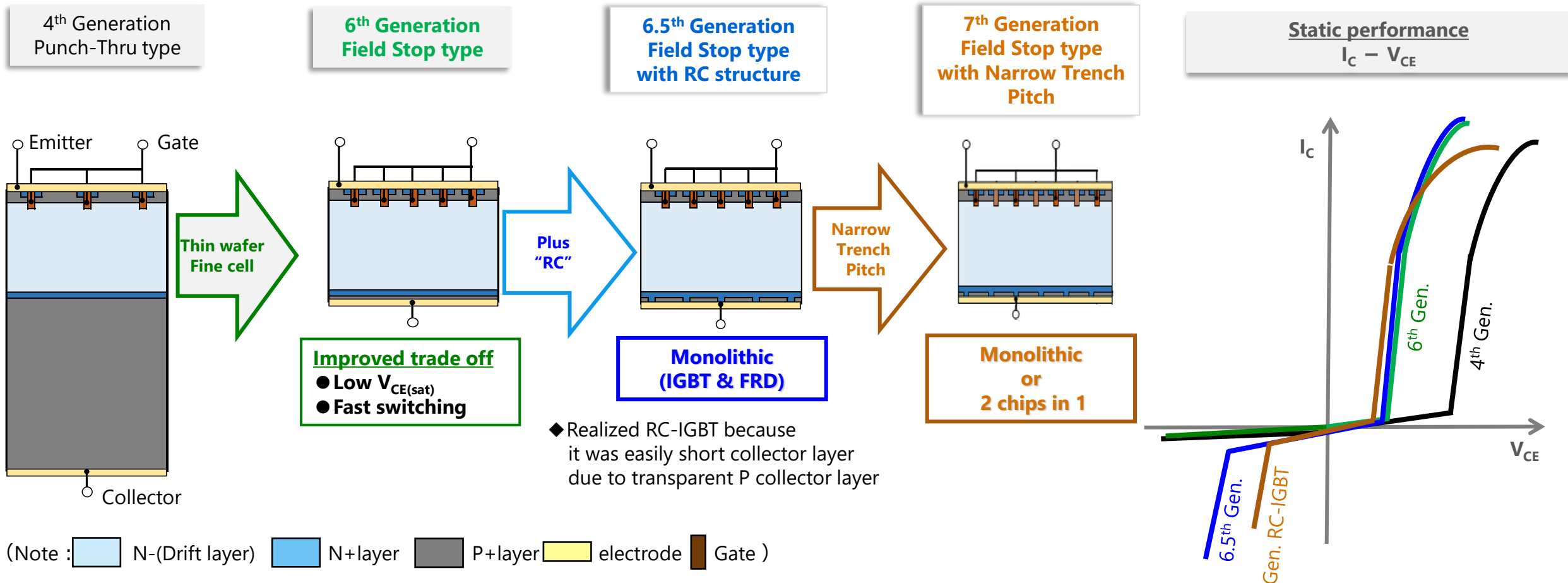


Line up

Part Number	I_C (A)	$V_{CE(SAT)}$ @ I_C (V, typ)	P_C (W)	Package
GT15J341	15	1.5	30	TO-220SIS
GT20J341	20	1.5	45	TO-220SIS
GT30J341	30	1.5	230	TO-3P(N)
GT50J123*	50	1.9	230	TO-3P(N)
GT30J65MRB	30	1.4	200	TO-3P(N)

* No built-in Body Diode

Toshiba continues to make improvements into the 7th Generation IGBT achieving Low $V_{CE(sat)}$ and high-speed switching by high density.



Part number (Only out source)	Application example : ●(Main) ▲(Sub)					Design · Constructer · Package			Maximum ratings								
	Hard switching	Soft switching							IGBT part						Di part		
	General	General	Fast switching	Current resonance	Voltage resonance		Gen.	Built-in Di(*)	Package	Pc	V _{CES}	I _C		I _{CP}	tsc	I _F	I _{FP}
	○ Motor ○ Inverter	○ PFC (Percial Switching)	○ PFC (Active switching)	○IH cocker (Built-in) (All metal)	○ IH cocker (on table) ○ hot-water heater ○ IH rice cocker ○ Copier ○ Micro Wave Oven (toner heating)	○ AC100V				○ AC200V	(W)	(V)	(A)		(A)	(μs)	(A)
○ Garage Door/Gate Openers ○ Air Conditioner	○Air conditioner (Power Supply)	○Air conditioner ○FA machine ○Welding machine					DC	DC	DC 25°C	DC 100°C	Pulse	T _j ≤ 150°C	DC	Pulse			
GT15J341	●	▲	▲				6th	Yes 2in1	TO-220SIS	30	600	15	8	60	5	15	
GT20J121		●					6th	Non	TO-220SIS	40	600	20	-	80	-	-	-
GT20J341	●	▲	▲				6th	Yes 2in1	TO-220SIS	45	600	20	11	80	5	20	
GT30J122A		●					4th	Non	TO-3P(N)	120	600	30	-	100	-	-	-
GT30J341	●	▲	▲	●			6th	Yes 2in1	TO-3P(N)	230	600	59	33	120	5	48	60
GT50J123	●	▲	▲				6th	Non	TO-3P(N)	230	600	59	33	120	5	-	-
GT50JR21				●			6.5th	Yes RC	TO-3P(N)	230	600	50	49	100	-	40	100
GT50JR22(STA1)			▲	●			6.5th	Yes RC	TO-3P(N)	230	600	50	44	100	-	40	100
GT50JR22(S1WLD)		▲	●	▲			6.5th	Yes RC	TO-3P(N)	230	600	50	40	100	-	40	100
New GT30J65MRB		▲	●	▲			7th	Yes RC	TO-3P(N)	200	650	60	30	120	-	30	60
New GT30J110SRA					●		6.5th	Yes RC	TO-3P(N)	312	1100	60	30	120	-	60	120
GT60PR21					●		6.5th	Yes RC	TO-3P(N)	333	1100	60	53	120	-	30	120
GT40QR21					●		6.5th	Yes RC	TO-3P(N)	230	1200	40	35	80	-	20	80
New GT20N135SRA						●	6.5th	Yes RC	TO-247	312	1350	40	20	80	-	40	80
New GT30N135SRA						●	6.5th	Yes RC	TO-247	348	1350	60	30	120	-	60	120
GT40RR21						●	6.5th	Yes RC	TO-3P(N)	230	1350	40	33	80	-	20	80

Part number (Only out source)	Design · Constructer · Package			Maximum ratings								Electrical characteristics												
				IGBT part						Di part		IGBT part										Di part		
	Gen.	Built-in Di(*)	Package	P _c	V _{CES}	I _C		I _{CP}	t _{sc}	I _F	I _{FP}	V _{CE(sat)}	V _{GE(OFF)}			C _{ies}		Switching Time typ.			V _F			
				(W)	(V)	(A)	(A)	(μs)	(A)	(A)	(V)	(V)			(pF)		(μs)			(V)				
			DC	DC	DC 25°C	DC 100°C	Pulse	T _j ≤ 150°C	DC	Pulse	max	condit ion I _C /V _{GE}	min	max	condit ion I _C	typ.	condit ion V _{CE}	t _r	t _f	condition V _{CE} /I _C /R _G /Load	max	conditi on I _F		
GT15J341	6th	Yes 2in1	TO-220SIS	30	600	15	8	60	5	15		2.0	15A/15V	3.5	6.5	1.5mA	1390	10V	0.03	0.08	300V/15A/33Ω/L	2.0	15A	
GT20J121	6th	Non	TO-220SIS	40	600	20	-	80	-	-	-	1.8	20A/15V	4.5	7.5	20mA	2700	10V	0.1	0.27	300V/20A/39Ω/R	-	-	
GT20J341	6th	Yes 2in1	TO-220SIS	45	600	20	11	80	5	20		2.0	20A/15V	3.5	6.5	2mA	1790	10V	0.04	0.05	300V/20A/33Ω/L	2.1	20A	
GT30J122A	4th	Non	TO-3P(N)	120	600	30	-	100	-	-	-	2.8	50A/15V	3.0	6.0	50mA	2500	10V	0.2	0.2	300V/50A/39Ω/R	-	-	
GT30J341	6th	Yes 2in1	TO-3P(N)	230	600	59	33	120	5	48	60	2.0	30A/15V	3.5	6.5	3mA	2900	10V	0.06	0.04	300V/30A/24Ω/L	2.3	30A	
GT50J123	6th	Non	TO-3P(N)	230	600	59	33	120	5	-	-	2.5	50A/15V	4.5	6.5	3mA	2900	10V	0.06	0.04	300V/30A/24Ω/L	-	-	
GT50JR21	6.5th	Yes RC	TO-3P(N)	230	600	50	49	100	-	40	100	2.0	50A/15V	4.5	7.5	50mA	2700	10V	0.18	0.08	300V/50A/39Ω/R	2.1	15A	
GT50JR22(STA1)	6.5th	Yes RC	TO-3P(N)	230	600	50	44	100	-	40	100	2.2	50A/15V	4.5	7.5	50mA	2700	10V	0.18	0.05	300V/50A/39Ω/R	2.1	15A	
GT50JR22(S1WLD)	6.5th	Yes RC	TO-3P(N)	230	600	50	40	100	-	40	100	2.5	50A/15V	4.5	7.5	50mA	2700	10V	0.18	0.05	300V/50A/39Ω/R	2.1	15A	
New New	GT30J65MRB	7th	Yes RC	TO-3P(N)	200	650	60	30	120	-	30	60	1.8	30A/15V	4.2	6.2	30mA	2150	25V	0.03	0.04	400V/15A/56Ω/L	1.5	15A
	GT30J110SRA	6.5th	Yes RC	TO-3P(N)	312	1100	60	30	120	-	60	120	1.8	30A/15V	5.0	7.0	60mA	2700	25V	0.14	0.17	600V/60A/10Ω/R	1.8	30A
	GT60PR21	6.5th	Yes RC	TO-3P(N)	333	1100	60	53	120	-	30	120	2.5	60A/15V	4.5	7.5	60mA	2350	10V	0.16	0.16	600V/60A/51Ω/R	2.5	15A
	GT40QR21	6.5th	Yes RC	TO-3P(N)	230	1200	40	35	80	-	20	80	2.7	40A/15V	4.5	7.5	40mA	1500	10V	0.12	0.2	600V/40A/39Ω/R	2.6	15A
New New	GT20N135SRA	6.5th	Yes RC	TO-247	312	1350	40	20	80	-	40	80	2.4	40A/15V	5.3	7.3	40mA	2700	25V	0.09	0.25	600V/40A/10Ω/R	2.5	20A
	GT30N135SRA	6.5th	Yes RC	TO-247	348	1350	60	30	120	-	60	120	2.6	60A/15V	5.3	7.3	60mA	2700	25V	0.14	0.25	600V/60A/10Ω/R	2.4	30A
	GT40RR21	6.5th	Yes RC	TO-3P(N)	230	1350	40	33	80	-	20	80	2.8	40A/15V	4.5	7.5	40mA	1500	10V	0.12	0.21	600V/40A/39Ω/R	2.8	15A

Value provided

Efficient high-power switching with good heat conductivity.

1 Built-in SBD

Low V_F with built-in SBD

- Toshiba $V_F = 1.35V$ typ.
- Competitor $V_F = 3.2 \sim 4.6V$ typ.
- > Good reliability due to no ON resistance drift!

2 Low $R_{on} * Q_{gd}$

Low $R_{on} * Q_{gd}$ values allows for efficient and fast switching

$R_{on} * A$ improves after each generation

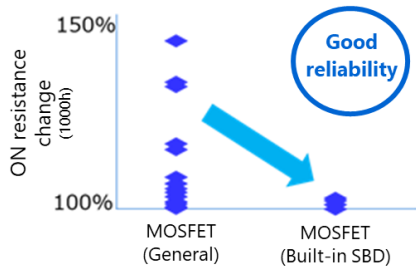
Low R_{on} temperature coefficient (tempco).

3 Wider V_{GSS} Rating

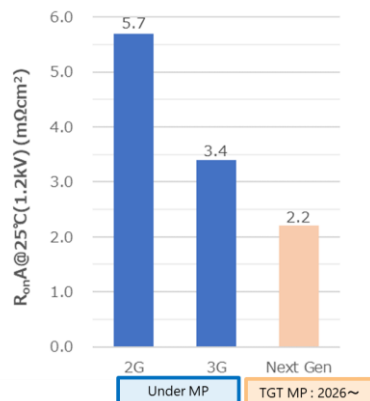
Wider V_{GSS} ratings compared with the competition's SiC MOSFETs

V_{GSS} : -10V ~ 25V (Recommend: 18V)

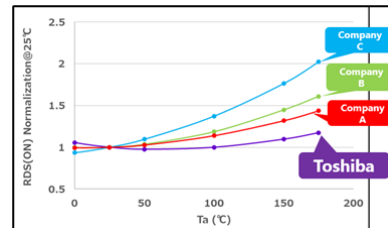
Built-in SBD prevents ON resistance drift over time, thus good reliability



Trend of $R_{on} * A$



Lowest R_{on} tempco



Line up

Part number	TW015N120C	TW030N120C	TW045N120C	TW015Z120C	TW030Z120C	TW045Z120C
Package	TO-247			TO-247-4L		
Abs Max I_D (A)	100	60	45	100	60	45
$R_{ds(on)}$ Typ (Ω)	15	30	45	15	30	45
Q_{gd} (nC)	158	82	57	158	82	57
C_{ISS} ($V_{DS}=300V$) (pF)	6000	2925	1969	6000	2925	1969

Line up of SiC MOSFET (650V)

※1 : V_{GSS} pulse
 ※2 : $T_a=25\text{ }^\circ\text{C}$
 ※3 : $V_{GS}=18\text{ V}$, $T_a=25\text{ }^\circ\text{C}$
 ※4 : $V_{DS}=400\text{ V}$, $f = 100\text{ kHz}$

[Back to AC-DC / PFC](#)

Generation	Package	Part Number	Absolute Maximum Ratings					Electrical Characteristics			ES	CS	MP
			V_{DSS} (V)	V_{GSS} (V) ※1	I_D (A) ※2	T_j ($^\circ\text{C}$)	T_{stg} ($^\circ\text{C}$)	$R_{DS(ON)}$ typ. ($m\Omega$)※3	Q_g typ. (nC)	C_{iss} typ. (pF)※4			
3rd Generation	TO-247	TW015N65C	650	-10~ 25	100	175	-55~ 175	15	128	4850	OK	OK	OK
		TW027N65C			58			27	65	2288	OK	OK	OK
		TW048N65C			40			48	41	1362	OK	OK	OK
		TW083N65C			30			83	28	873	OK	OK	OK
		TW107N65C			20			107	21	600	OK	OK	OK
	TO-247-4L	TW015Z65C			100			15	128	4850	OK	OK	OK
		TW027Z65C			58			15	128	4850	OK	OK	OK
		TW048Z65C			40			27	65	2288	OK	OK	OK
		TW083Z65C			30			48	41	1362	OK	OK	OK
		TW107Z65C			20			83	28	873	OK	OK	OK
	New TOLL	TW015U65C			100			15	128	4850	2Q 2024	3Q 2024	4Q 2024
		TW027U65C			58			27	65	2288	2Q 2024	3Q 2024	4Q 2024
		TW048U65C			40			48	41	1362	2Q 2024	3Q 2024	4Q 2024
		TW083U65C			30			83	28	873	2Q 2024	3Q 2024	4Q 2024
	New DFN8×8	TW(030)V65C			(58)			(30)	(65)	(2288)	2Q 2024	3Q 2024	4Q 2024
		TW(050)V65C			(40)			(50)	(41)	(1362)	2Q 2024	3Q 2024	4Q 2024
		TW(090)V65C			(30)			(90)	(28)	(873)	2Q 2024	3Q 2024	4Q 2024
		TW(120)V65C			(20)			(120)	(21)	(600)	2Q 2024	3Q 2024	4Q 2024

Note: The information in this table is subject to change without notice.

Value

[Back to AC-DC / PFC](#)

U-MOSX-H 150V series provides best-in class performance and improve the efficiency.

1 Low Switching losses

- $R_{DS(ON)} \times Q_{SW}$ reduction (Cell design optimization)
- Large contribution at high frequency, especially in light load

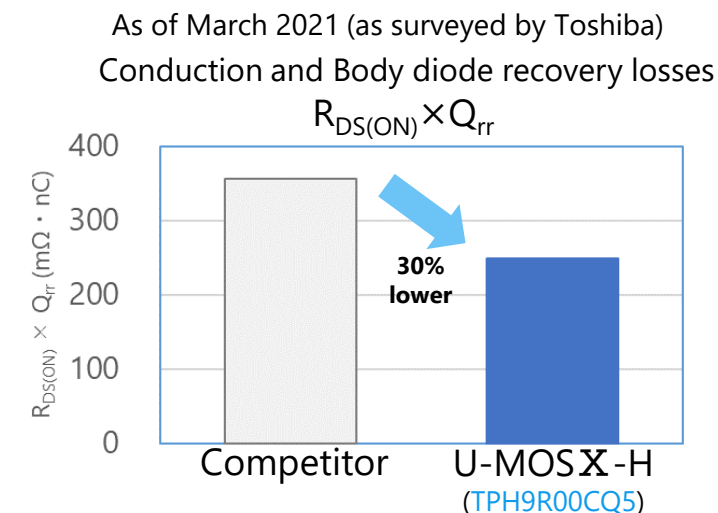
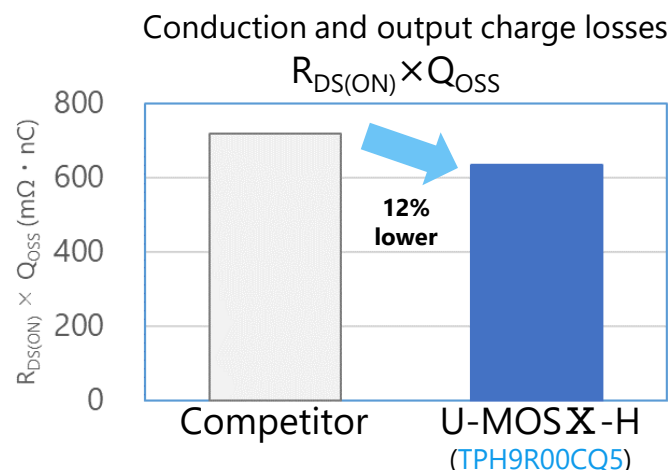
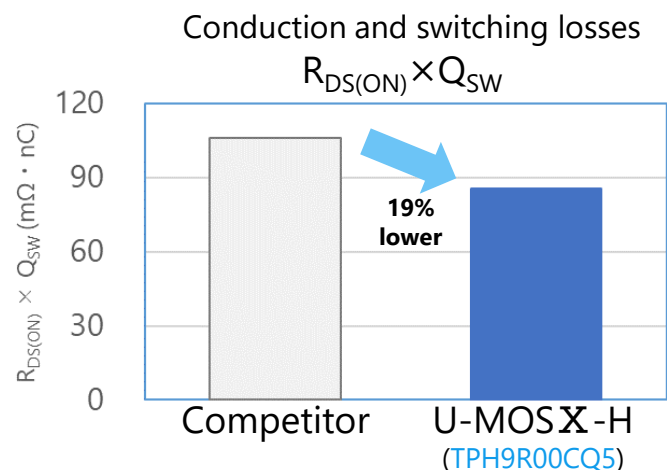
2 Low Output charge losses

- $R_{DS(ON)} \times Q_{OSS}$ reduction (Cell design optimization)
- * Loss reduction of high-side MOSFET when used in low side of totem pole

3 Low Recovery losses

- Q_{rr} reduction (Applying Lifetime control technology)
- Large contribution at the secondary side synchronous rectifier circuit of the switching power supply unit

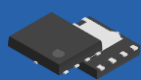
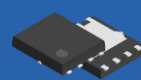
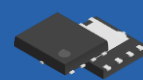
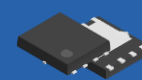
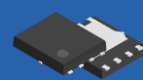
FOM Comparison with the nearest competitor



FOM : Figure of Merit $R_{DS(ON)}$: On-resistance (figure of merit for conduction loss)
 Q_{SW} : Gate switch charge (figure of merit for switching loss)

Q_{OSS} : Output charge (figure of merit for output charge loss)
 Q_{rr} : Reverse recovery charge (figure of merit for body diode recovery loss)

Performance of U-MOSX-H STD/HSD is better than that of either U-MOSVIII-H or competitors.

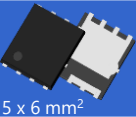
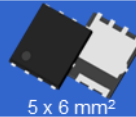
Item	U-MOSVIII-H (TPH1500CNH)		U-MOSX-H(STD) (TPH9R00CQH)		U-MOSX-H(HSD) (TPH9R00CQ5)		Competitor A		Competitor B	
	 5 x 6 mm		 5 x 6 mm		 5 x 6 mm		 5 x 6 mm		 5 x 6 mm	
V_{DSS}	150 V	-	150 V	-	150 V	-	150 V	-	150V	-
V_{th}	2.0~4.0V	★	3.3~4.3V	★★★★	3.1~4.5V	★★★★	3.0~4.6V	★★	2.5~4.5V	★
T_{ch}	150 °C	★★	175 °C	★★★★	175 °C	★★★★	150 °C	★★	150 °C	★★
$R_{DS(ON)}$ @VGS=10V typ.	13 mΩ	★	7.3 mΩ	★★★★	7.3 mΩ	★★★★	7.9 mΩ	★★	7.0 mΩ	★★★★
$R_{DS(ON)} * Q_{SW}$	107 mΩ·nC	★	85*1 mΩ·nC	★★	85*1 mΩ·nC	★★	106 mΩ·nC	★	62*1 mΩ·nC	★★★★
$R_{DS(ON)} * Q_{oss}$	(897) mΩ·nC	★	635 mΩ·nC	★★★★	635 mΩ·nC	★★★★	719 mΩ·nC	★★	707 mΩ·nC	★★
$R_{DS(ON)} * Q_{rr}$	(2483) mΩ·nC	★	971*1mΩ·nC	★★	248*1mΩ·nC	★★★★	356*1mΩ·nC	★★★★	826*1mΩ·nC	★★

*1 Measured value on same condition

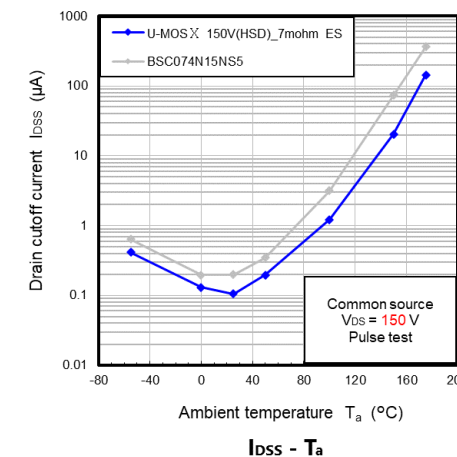
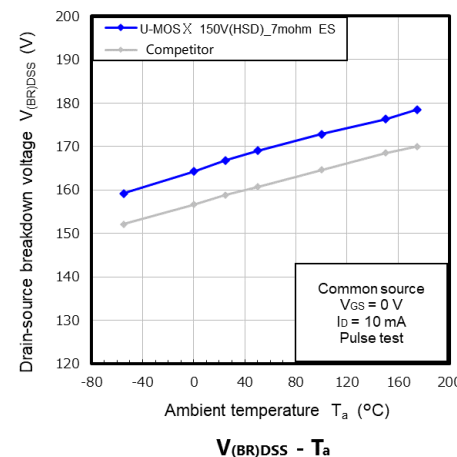
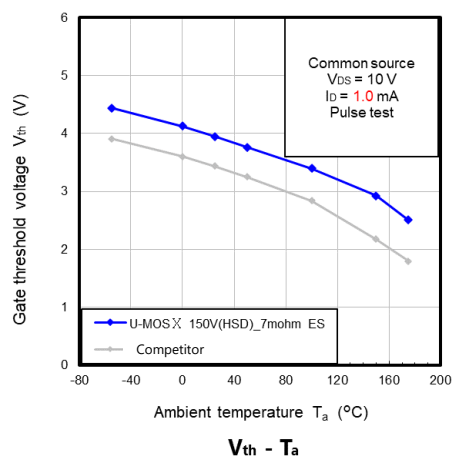
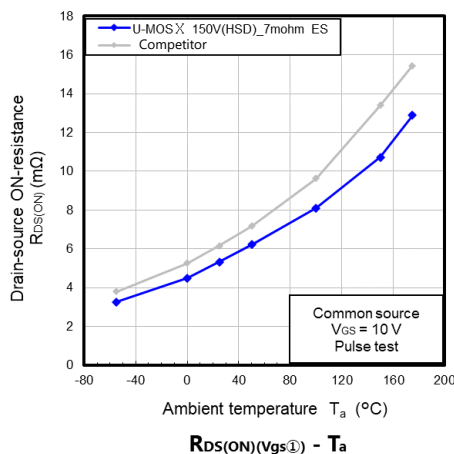
For more information see: [U-MOS 150V MOSFET](#)

Added a new product outperforming competitors in terms of $R_{DS(ON)}$.

ES : Apr. 2024
Order Available : Oct. 2024

Item	U-MOSX -H(HSD) (TPM7R10CQ5) S1TN0  5 x 6 mm ²	Competitor A  5 x 6 mm ²
Operation Temp	175degC	175degC
$I_{DSstyp}(@100degC)$	1.52 μA^*1	3.18 μA^*1
V_{th}	3.1V~4.5V	3.0~4.6V
$R_{DS(ON)typ}$ at 10V	5.4*1 m Ω	6.2*1 m Ω
$Q_{sw}(V_{DS}=75V)$	18*1 nC	15*1 nC
$Q_{oss}(V_{DS}=75V)$	107*1 nC	108*1 nC
$Q_{rr}(dI/dt=100A/us)$	43*1 nC	55*1 nC

*1 Measured value on same condition (Toshiba devices are equal to datasheet).

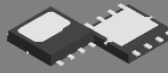

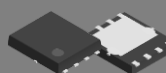
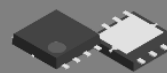
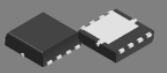


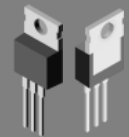
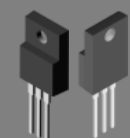


12a Line up plan of U-MOS 150V products

New Product

[Back to AC-DC / PFC](#)

(CY)

$R_{DS(ON)}$ Max ($V_{GS}=10V$) (mohm)	Package												
	DSOP Advance (dual sided cooling) 5 x 6 mm 	SOP Advance(E) (5x6) 5 x 6 mm 	SOP Advance(N) (5x6) 5 x 6 mm 	SOP Advance (5x6) 5 x 6 mm 	TSOP Advance (3x3) 3 x 3 mm 		L-TOGL (TOLL leaded) 10 x 12 mm 	DDPAK (TO-252) 	TO-220 	TO-220SIS (Fullpak) 			
50				TPH5900CNH	59	TPN5900CNH	59						
40						TPN4800CQH	48						
30			TPH3300CQH (Order Mar. 2025)	33	TPH3300CNH	33	TPN3300CQH (ES Jan. 2025)	33					
25							TPN3300CQ5 (HSD) (ES 3Q 2025)	33					
20			TPH2200CQH (Order Mar. 2025)	22									
15	TPW1500CNH Conventional type	15.4			TPH1500CNH1	15.4							
					TPH1400CQH	14.1							
					TPH1400CQ5 (HSD)	14.1							
12													
10				TPH1100CQ5 (HSD)	11.1	TPH9R00CQH	9			TK9R6E15Q5(HSD) (Order Jul. 2024)	9.6	TK9R7A15Q5(HSD) (Order Jul. 2024)	9.7
				TPH9R00CQH	9								
				TPH9R00CQ5 (HSD)	9								
8	S1VM7 (HSD) (ES 2H 2025)	7.1	TPM7R10CQ5(HSD) (ES Oct. 2024)	7.1						TK7R2E15Q5(HSD) (Order Jul. 2024)	7.2	TK7R4A15Q5(HSD) (Order Jul. 2024)	7.4
6													
5										TK4R9E15Q5(HSD) (Order Jul. 2024)	4.9	TK5R0A15Q5(HSD) (Order Jul. 2024)	5.0



Note : Specifications and schedule of under development is just target and is subject to change without notice.

For more information see: [U-MOS 150V MOSFET](#)

Value

U-MOSX-H 80V series provides best-in class performance and improve the efficiency.

1 Low drive losses

- $R_{DS(ON)} \times Q_g$ reduction (Cell design optimization)
- Large contribution at high frequency, especially in light load

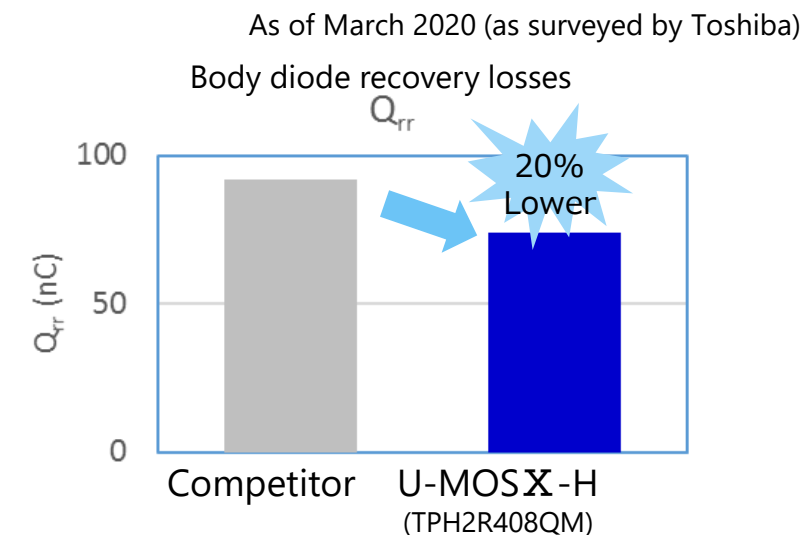
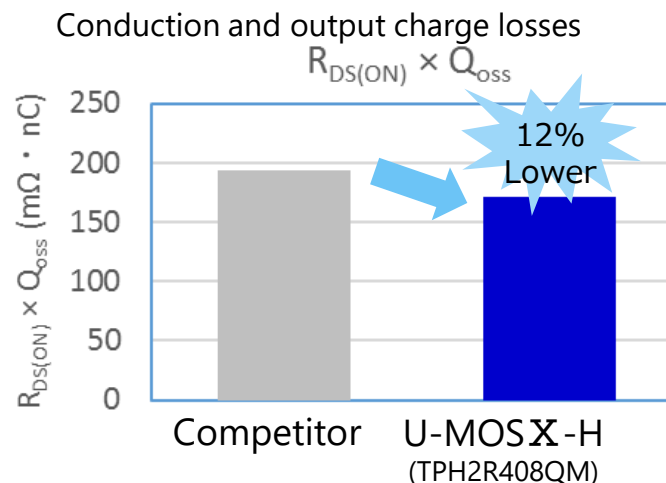
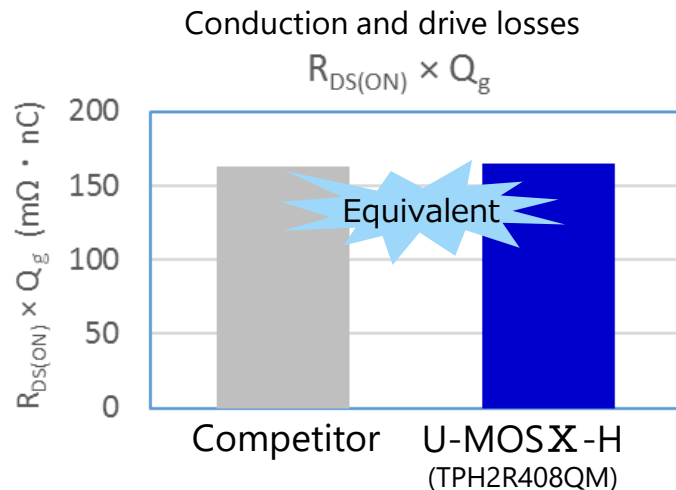
2 Low Output charge losses

- $R_{DS(ON)} \times Q_{OSS}$ reduction (Cell design optimization)
- * Loss reduction of high-side MOSFET when used in low side of totem pole

3 Low Recovery losses

- Q_{rr} reduction (Cell design optimization)
- Large contribution at the secondary side synchronous rectifier circuit of the switching power supply unit

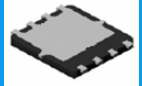
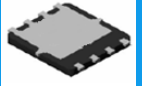
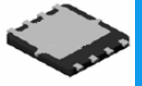
FOM Comparison with nearest competitor



FOM : Figure of Merit $R_{DS(ON)}$: On-resistance (figure of merit for conduction loss)
 Q_g : Gate charge (figure of merit for drive loss)

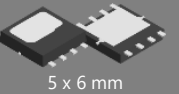
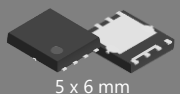
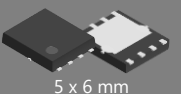
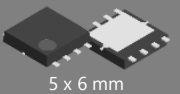
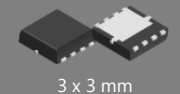
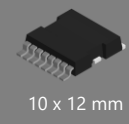

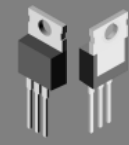
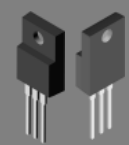
Q_{OSS} : Output charge (figure of merit for output charge loss)
 Q_{rr} : Reverse recovery charge

Performance of U-MOSX-H is better than that of either U-MOSVIII-H or Competitor

Item	U-MOSVIII-H [TPH4R008NH]	 5 x 6 _{mm}	U-MOSX-H [TPH2R408QM]	 5 x 6 _{mm}	Competitor	 5 x 6 _{mm}
V_{DSS}	80 V	-	80 V	-	80 V	-
V_{th}	2.0~4.0V	★	2.5~3.5V	★★★	2.2~3.8V	★★
T_{ch}	150 °C	★★	175 °C	★★★	150 °C	★★
$R_{DS(ON)}$ @ V_{GS} = 10V typ.	3.3 mΩ	★	1.9 mΩ	★★★	2.2 mΩ	★★
$R_{DS(ON)} * Q_{sw}$	59.4 mΩnC	★	53.2 mΩnC	★★★	55 mΩnC	★★
$R_{DS(ON)} * Q_{oss}$	247 mΩnC	★	171 mΩnC	★★★	194 mΩnC	★★
SOA	Standard	★	Wider	★★	Wider	★★

For more information see: [U-MOS 80V MOSFET](#)

Line up plan of U-MOS 80V products

R _{DS(ON)} Max (V _{GS} = 10V) (mohm)	Package												
	DSOP Advance (5x6 dual sided cooling)  5 x 6 mm	SOP Advance(E) (5x6)  5 x 6 mm	SOP Advance(N) (5x6)  5 x 6 mm	SOP Advance (5x6)  5 x 6 mm	TSOP Advance (3x3)  3 x 3 mm	L-TOGL (TOLL leaded)  10 x 12 mm	DPAK (TO-252) 	TO-220 	TO-220SIS (Fullpak) 				
30					TPN30008NH	30							
25													
20					TPN19008QM	19							
15													
12				TPH12008NH	12.3	TPN13008NH	13.3	TK35E08N1	12.2	TK35A08N1	12.2		
10						TPN12008QM	12.3						
8				TPH8R008QM	8.8	TPH8R008NH	8	TPN8R408QM	8.4	TK46E08N1	8.4	TK46A08N1	8.4
6								TK7R0E08QM	7				
6				TPH6R008QM	6			TK6R9P08QM	6.9		TK6R8A08QM	6.8	
5								TK5R1P08QM	5.1	TK5R3E08QM	5.3	TK5R1A08QM	5.1
5											TK72A08N1	4.5	
4	S1SK0 (ES 1H 2026)	4		TPH4R008	4			TK72E08N1	4.3				
4	TPV4R008NH Conventional type	4		TPH4R008QM	4								
3				TPH3R008QM	3			TK3R3E08QM	3.3	TK100A08N1	3.2	TK100A08N1	3.2
3								TK100E08N1	3.2	TK3R2A08QM	3.2	TK3R2A08QM	3.2
2.5				TPH2R408QM	2.43	TPH2R408QM	2.43	TK2R4E08QM	2.44	TK2R4A08QM	2.44	TK2R4A08QM	2.44
2	S1VM5 (ES 2H 2025)	2	TPM1R908QM (ES Feb. 2025)	1.94									
1													
0.8								XPQR8308QB (Order 2H 2025)	0.83				



Note : Specifications and schedule of under development is just target and is subject to change without notice.

For more information see: [U-MOS 80V MOSFET](#)

Value provided

Electronic fuse (eFuse IC) can be used repeatedly to protect circuits from abnormal conditions such as overcurrent and overvoltage.

1 Can be used repeatedly

When overcurrent flows through the electronic fuse (eFuse IC), the internal detection circuit operates and switches off the internal MOSFET. It is not destroyed by a single overcurrent and can be used repeatedly.

2 IEC 62368-1 certified

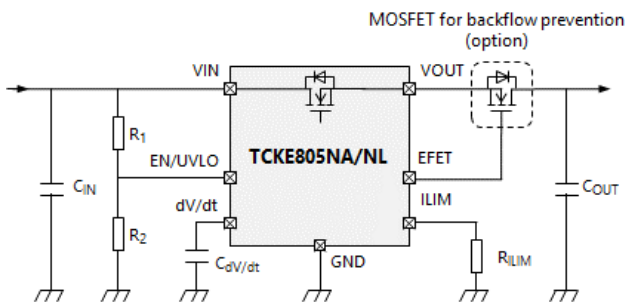
Toshiba's eFuse ICs are certified to the international safety standard IEC 62368-1 (G9: Integrated circuit (IC) current limiters) and contribute to robust protection and simplification of circuit design.

3 Rich protection functions

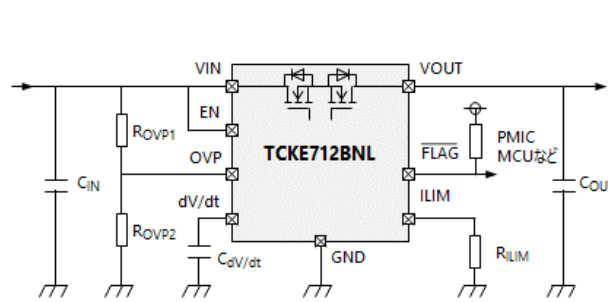
TCKE8 Series: Short-circuit protection, overcurrent protection, overcurrent clamp function, overvoltage clamp function, thermal shut down, inrush current suppression, backflow prevention (optional), etc.

TCKE7 Series: Short-circuit protection, overcurrent protection, overvoltage protection, thermal shut down, FLAG signal output, backflow prevention (built-in), etc.

Reference circuit example of TCKE8 Series



Reference circuit example of TCKE7 Series



Line up

Part number	TCKE800NA/NL	TCKE805NA/NL	TCKE812NA/NL	TCKE712BNL
Package	WSN10B 3.0 x 3.0 x 0.75 mm			WSN10 3.0 x 3.0 x 0.75 mm
V _{IN} [V]	4.4 to 18			4.4 to 13.2
I _{OUT} (Max) [A]	5			3
R _{ON} (Typ.) [mΩ]	28			53
Return function	NA: Automatic return NL: Latch type (external signal control)			Latch type (external signal control)
V _{OVC} (Typ.) [V]	-	6.04	15.1	Adjustable

[Back to AC-DC / PFC](#)

Value provided

Toshiba 3rd generation SiC Schottky barrier diode provides low power loss and durable performance1 Low V_F

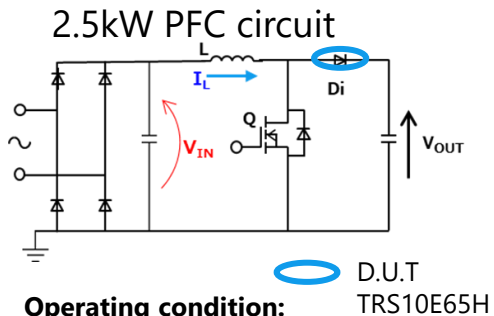
Thin wafer technology and new metal adaption leads to industry lowest forward Voltage, V_F

2 Low I_R

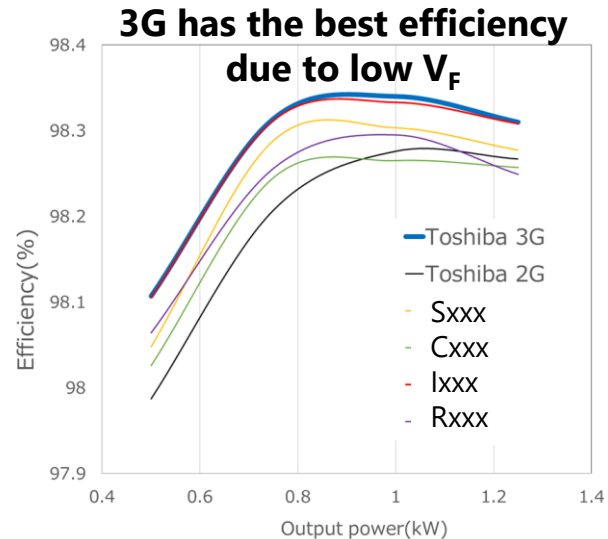
- Low reverse current I_R achieved by modified JBS structure and adjusting the process for 3rd generation

3 Large I_{FSM}

Large peak forward surge current I_{FSM} achieved by modified JBS structure and adjusting the process for 3rd generation



Operating condition:
 V_{IN} =200V AC
 V_{OUT} =400V DC
 f =65kHz
 MOSFET=TK040Z65Z w/external gate resistance=4.7 Ω
 T_a =25 $^{\circ}$ C



Line up

Part number	TRS6E65H
Forward Voltage V_F (V) Typ	1.2
Total Capacitive Charge Q_C (nC) Typ	17
Reverse Current I_R (μ A) Typ	1.1
N-R peak fwd surge current* I_{FSM} (A)	41
N-R peak fwd surge current** I_{FSM} (A)	310
Package**	TO-220-2L

* f =50Hz (half sine wave t =10ms) T_c =25 $^{\circ}$ C





**Square wave t =10 μ s T_c =25 $^{\circ}$ C

***Other packages available: TO247 and DFN8x8



650V SiC SBD Lineup

[Back to AC-DC / PFC](#)

$I_{F(max)}$ (A)	TO-220F-2L	TO-220-2L		TO-247-CT	DFN8 × 8
					
	2 nd gen.	2 nd gen.	3 rd gen.	2 nd gen.	3 rd gen.
	$V_F=1.45V(typ.)$	$V_F=1.45V(typ.)$	$V_F=1.2V(typ.)$	$V_F=1.45V(typ.)$	$V_F=1.2V(typ.)$
2		TRS2E65F MP	TRS2E65H MP		
3		TRS3E65F MP	TRS3E65H MP		
4	TRS4A65F MP	TRS4E65F MP	TRS4E65H MP		TRS4V65H MP
6	TRS6A65F MP	TRS6E65F MP	TRS6E65H MP		TRS6V65H MP
8	TRS8A65F MP	TRS8E65F MP	TRS8E65H MP		TRS8V65H MP
10	TRS10A65F MP	TRS10E65F MP	TRS10E65H MP		TRS10V65H MP
12	TRS12A65F MP	TRS12E65F MP	TRS12E65H MP	TRS12N65FB MP	TRS12V65H MP
16				TRS16N65FB MP	
20				TRS20N65FB MP	
24	Under Investigation	ES Under development	ES Available	Under MP	TRS24N65FB MP



[See: SiC SBD](#)

Note.1: The information in this table is subject to change without notice.

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Contact address: <https://toshiba.semicon-storage.com/us/contact.html>



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