

STM32MP1 microprocessor broadening STM32 MPU family

Press Presentation

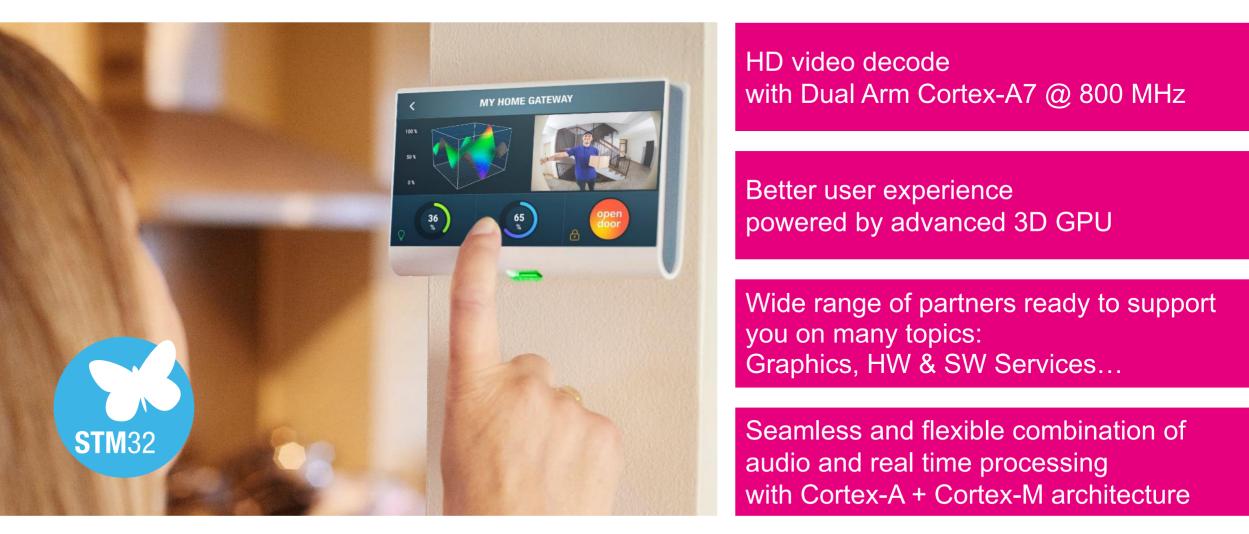






I could make a Smart Home Gateway with advanced HMI and HD video

Advanced HMI with graphics and video on top of real time applications







I could find an Industrial grade processor for my applications

Industrial grade microprocessor for demanding applications



Industrial qualification <u>combining</u> both: 100% operating time during 10 years Junction temperature: - 40°C to 125°C

10 years longevity commitment renewed every year

Industrial connectivity, advanced analog Cortex-M4 for real time processing

Advanced security for Industry 4.0

4 packages available in pitch 0.5 & 0.8mm





I could easily improve my applications with Artificial Intelligence

Embedding various Neural Networks for cutting-edge applications



TensorFlow Lite native support running on Cortex-A7 / Linux



STM32Cube.AI tool for machine learning running on Cortex-M4



Camera and audio interfaces to simplify input devices' integration





STM32MP1 - Constantly Improving





A broader STM32 MPU ecosystem to reduce development time & cost



Boosting performances with Dual Cortex-A7 @ 800MHz





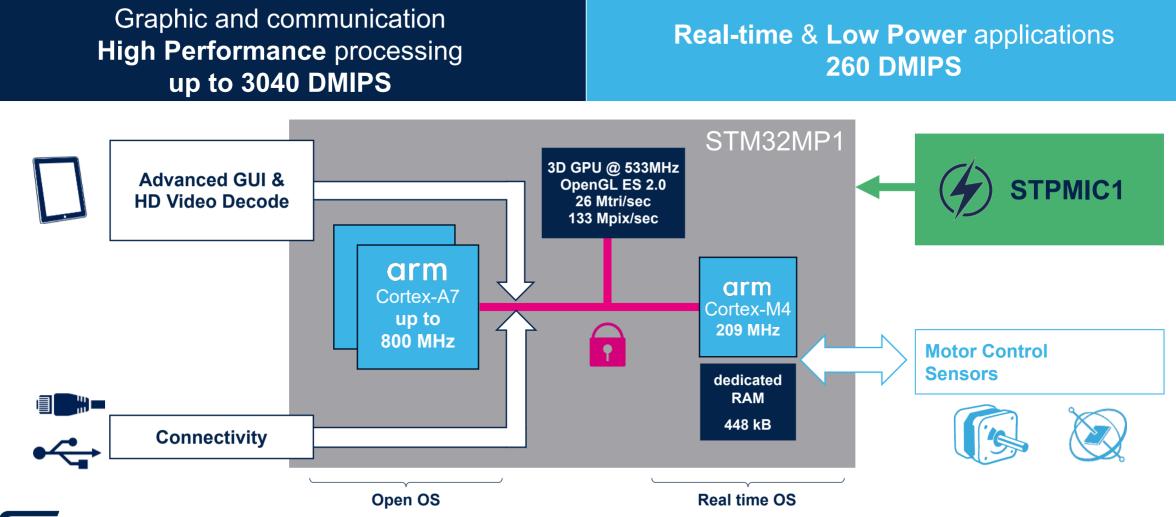
Boosting performances Broadening possibilities



A Scalable Solution to best meet customers' needs



Boosting application possibilities





Secure architecture for trusted devices





ENCRYPTION DECRYPTION AUTHENTICATION

- Duplicated resources on Cortex-A7 and Cortex-M4
 - Crypto and Hash Hardware Engines
 - TRNG
- Secure boot (ROM)
- Unique ID



CONFIDENTIALITY ANTI-TAMPERING

- TrustZone
- Secure RAMs and Peripherals
- Secure RTC with Active Tamper
- T^o, V and 32KHz sensor monitoring
- Cortex-M4 resources HW isolation
- Secure OS support: OP-TEE



Development and production programmers with provisioning and authentication

Some of the above features are optional and require to procure dedicated part numbers. Please refer to product specification



A broader STM32 MPU ecosystem to reduce development time & cost





Enhance your added value by relying on ST and authorized Partners' solutions



Solutions for EDGE computing & IoT from sensors up to the Cloud

Simplifying Android[™] development

A growing base of ST Authorized Partners

ST continuous investment into the most recognized Open Source standards

Android is a trademark of Google LLC.



Create cloud based applications with STM32MP1 solutions

Complete support of main cloud provider



IBM Watson support soon





Example of STM32MP1 Discovery board used for EDGE processing



Simplifying Android[™] development



Reduce development time & cost with pre-build Solutions provided by ST:

- Free of charge AOSP enablement
- Various Android[™] packages

Extra headroom (up to 800MHz Cortex-A) for better user experience

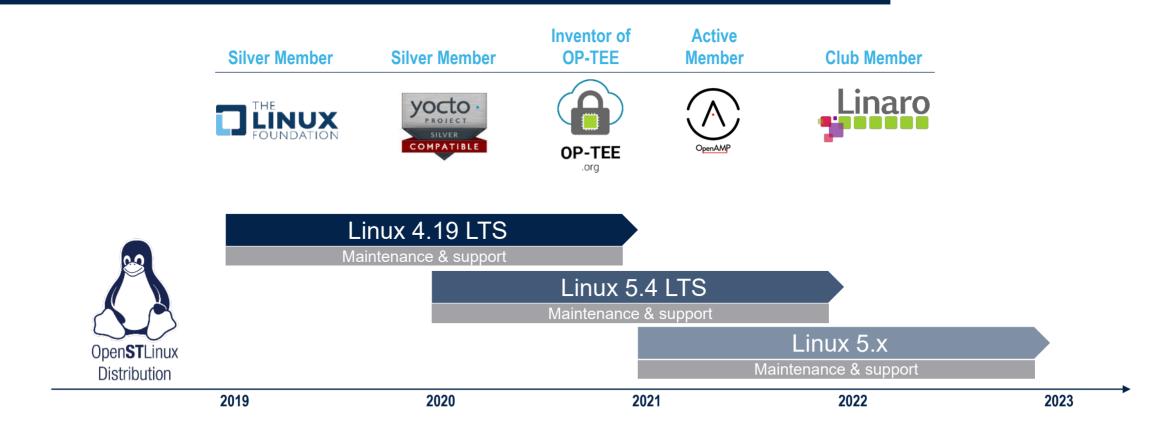
Exclusive plug-in to bridge real-time Cortex-M and Android environments provided in the SDK

Android is a trademark of Google LLC.



Continuous investment in Linux to make customers' design simpler and more efficient

ST is continuously upstreaming Linux drivers to the Linux community



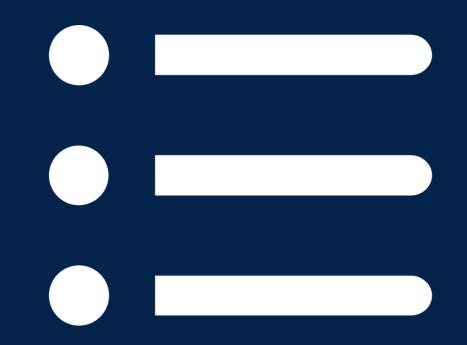




A growing base of partners addressing customers' challenges



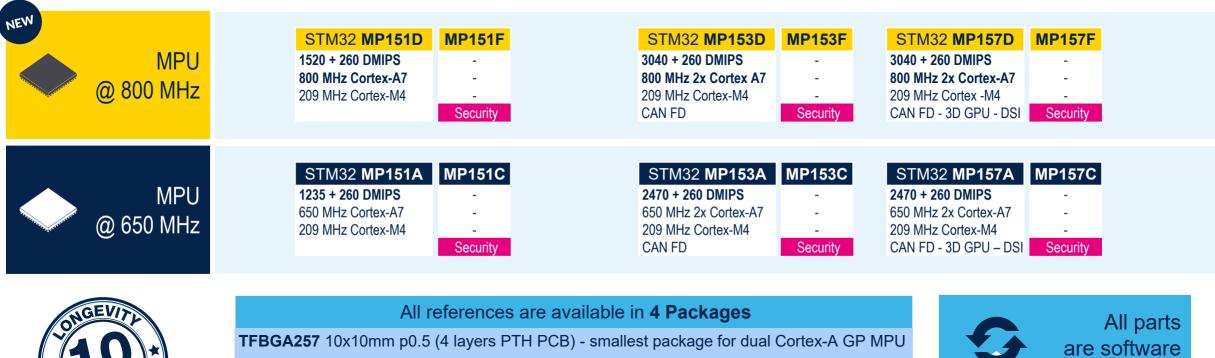
STM32MP1 Line-up







Expanding the STM32MP1 portfolio now 48 part numbers



TFBGA361 12x12mm p0.5 (4 layers PTH + Laser via PCB)

LFBGA354 16x16mm p0.8 (4 layers PTH PCB)

LFBGA448 18x18mm p0.8 (6 layers PTH PCB)

All parts are software and pin to pin compatible

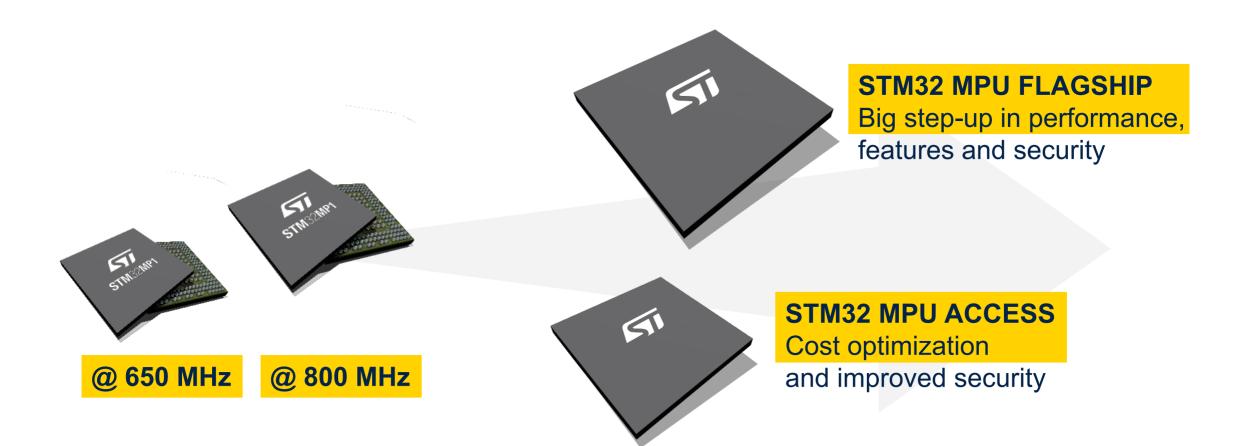
Arm[®] Cortex[®] core

Cortex-A7 + Cortex-M4

Dual Cortex-A7 + Cortex-M4



Building the future STM32 MPU portfolio expansion





STM32



STM32MP1 - your new companion for advanced applications







A broader STM32 MPU ecosystem to reduce development time & cost



Releasing your creativity

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Arm® Dual Cortex up to 800 MH L1 32kB I L1 256kB L2 Cac	Iz 32kB D	Arm® Cortex® -M4 209 MHz PU MPU
External Memories	DDR3/DDR3L/LPDDR2/LI	PDDR3 32-bit @ 533 MHz
3x SDMMC	Dual Quad-SPI	16-bit SLC NAND 8-bit ECC
Internal Memories	MCU System RAM 384kB	MCU Retention RAM 64kB
System RAM 256kB	Back up RAM 4kB	OTP fuse 3kb
	Graphics	System
Connectivity 10/100M or Gigabit Ethernet GMAC 3x USB 2.0 Host/OTG	3D GPU OpenGL ES 2.0 @ 533 MHz MIPI-DSI controller LCD-TFT controller	5x LDOs Internal and External Oscillators MDMA + 2x DMA Reset and Clock
with 2x HS PHY Camera interface HDMI-CEC	Security TrustZone	3x watchdogs Up to 176 GPIOs
2x CAN FD MDIO slave DFSDM (8 channels/6 filters) 6x SPI / 3x I ² S 6x I ² C 4x UART + 4x USART	AES 256, TDES* SHA-256, MD5, HMAC 3x Tamper Pins with 1 active Secure Boot* Secure RAMs Secure Peripherals	Control 2x 16-bit advanced motor control timers 15x 16-bit timers 2x 32-bit timers
4x SAI SPDIF	Secure RTC	Analog 2x 16-bit ADCs
	Analog true RNG	2X TO-UILADUS

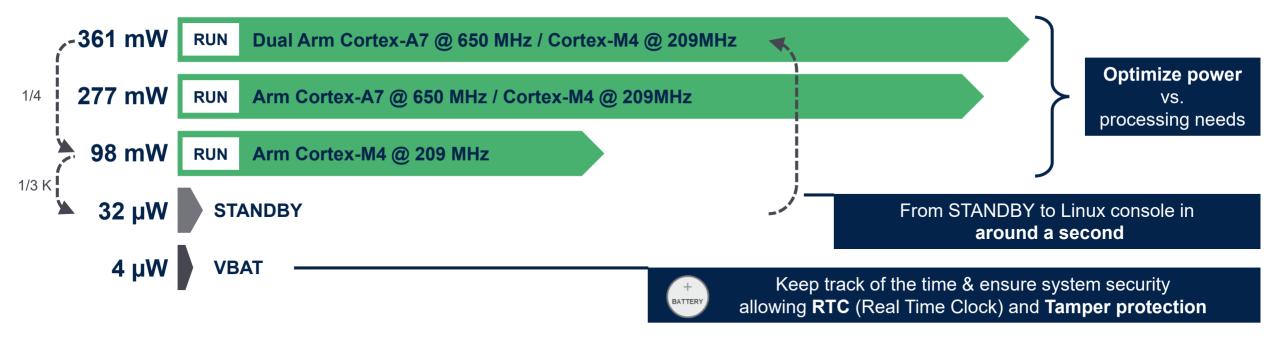
STM32MP157 block diagram



*available for STM32MP157C and STM32MP157F only

Flexible architecture for power efficiency

Power figures at 650MHz

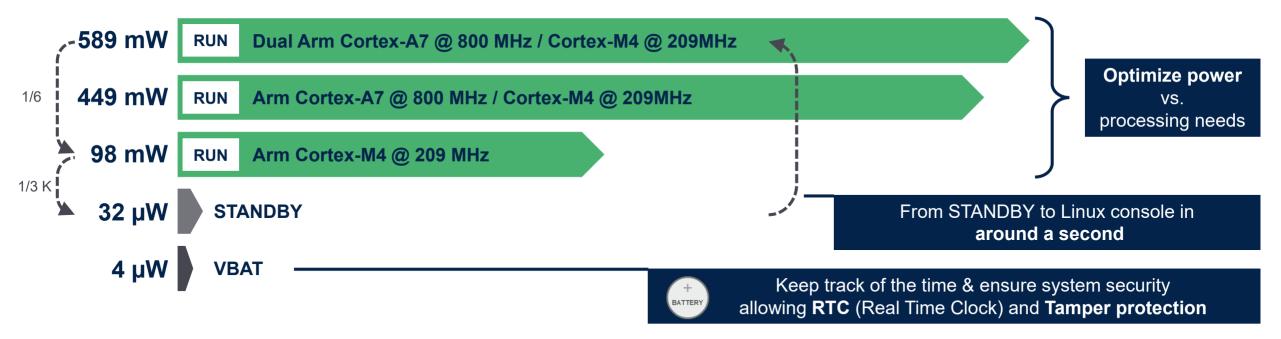


Typ @ VDDCORE = 1.2V, VDD = 3.3V @ 25 °C, Peripherals OFF



Flexible architecture for power efficiency

Power figures at 800MHz

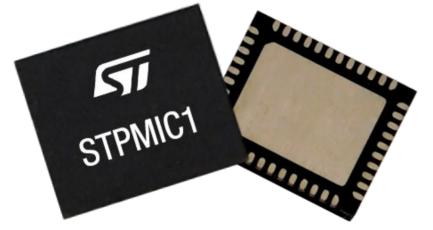


Typ @ VDDCORE = 1.2V, VDD = 3.3V @ 25 °C, Peripherals OFF



STPMIC1 power management IC dedicated to STM32MP1 MPU

Simplify your design and optimize power consumption



DC/DCs & LDOs for - STM32MP1

- STM32MP
- Memories
- External devices

Optimized power consumption

BOM savings for typical applications

Small PCB footprint vs. full discrete solution



Thank you

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